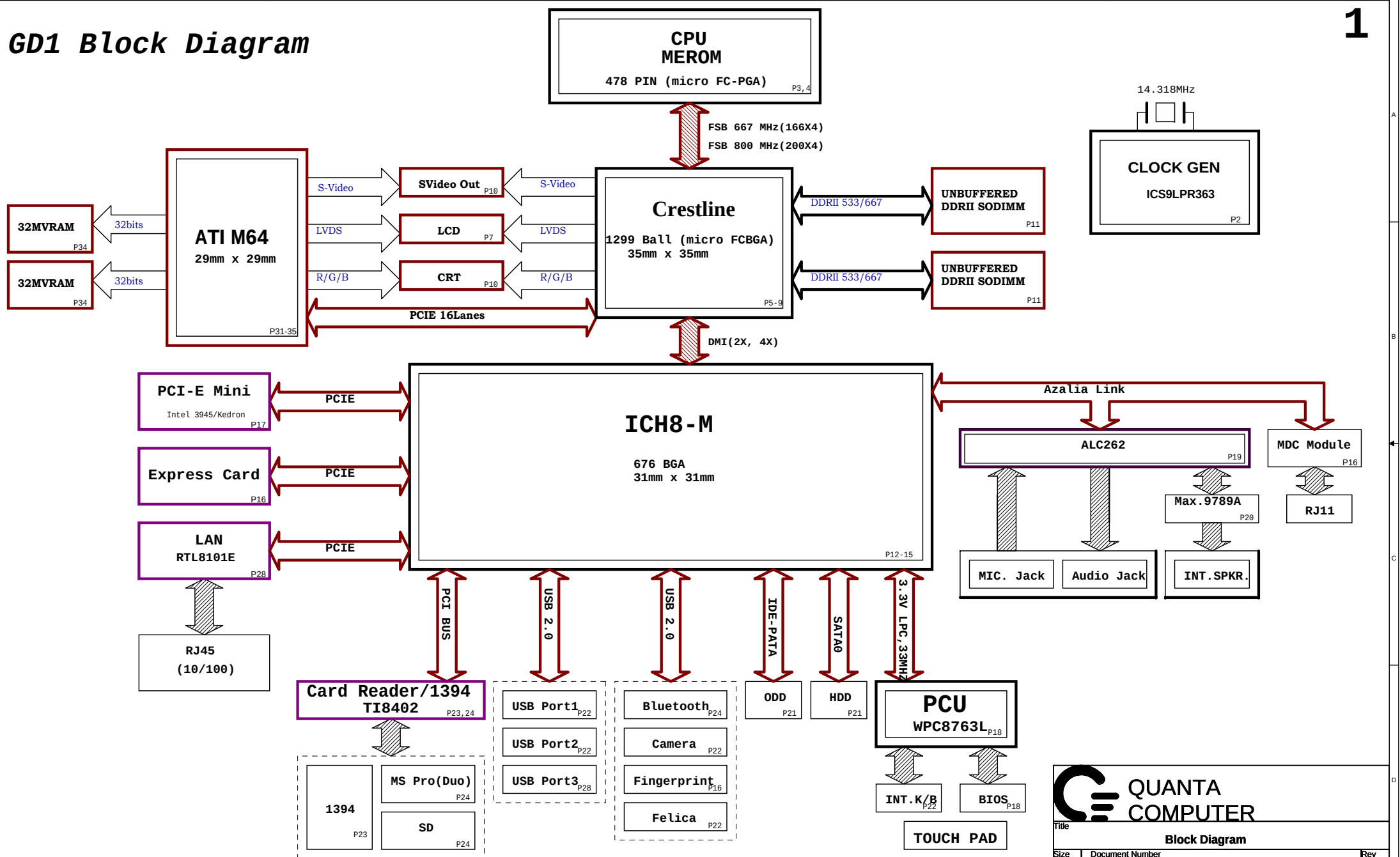


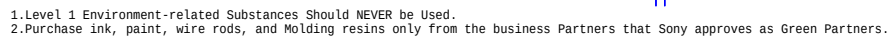
GD1 Block Diagram

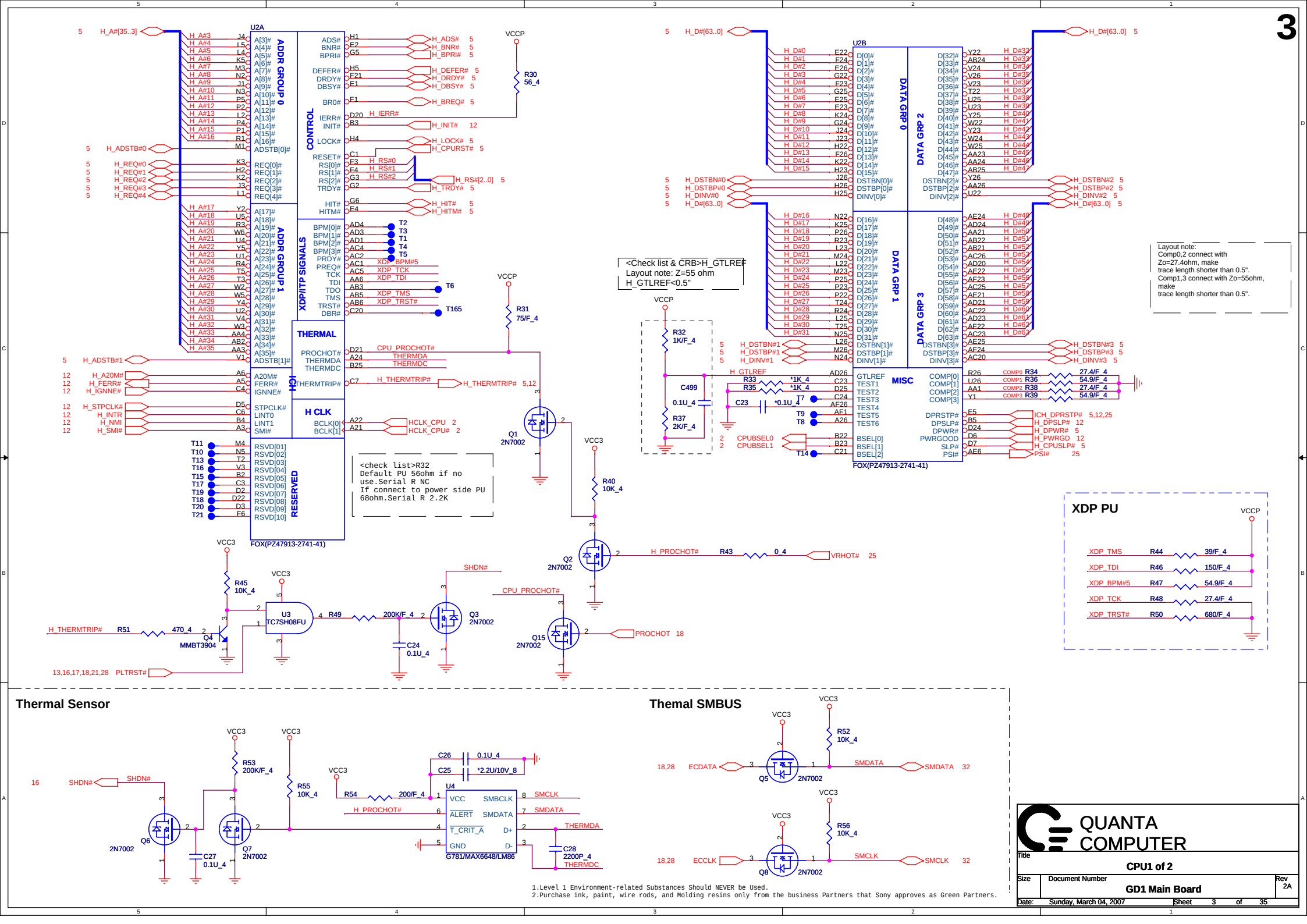
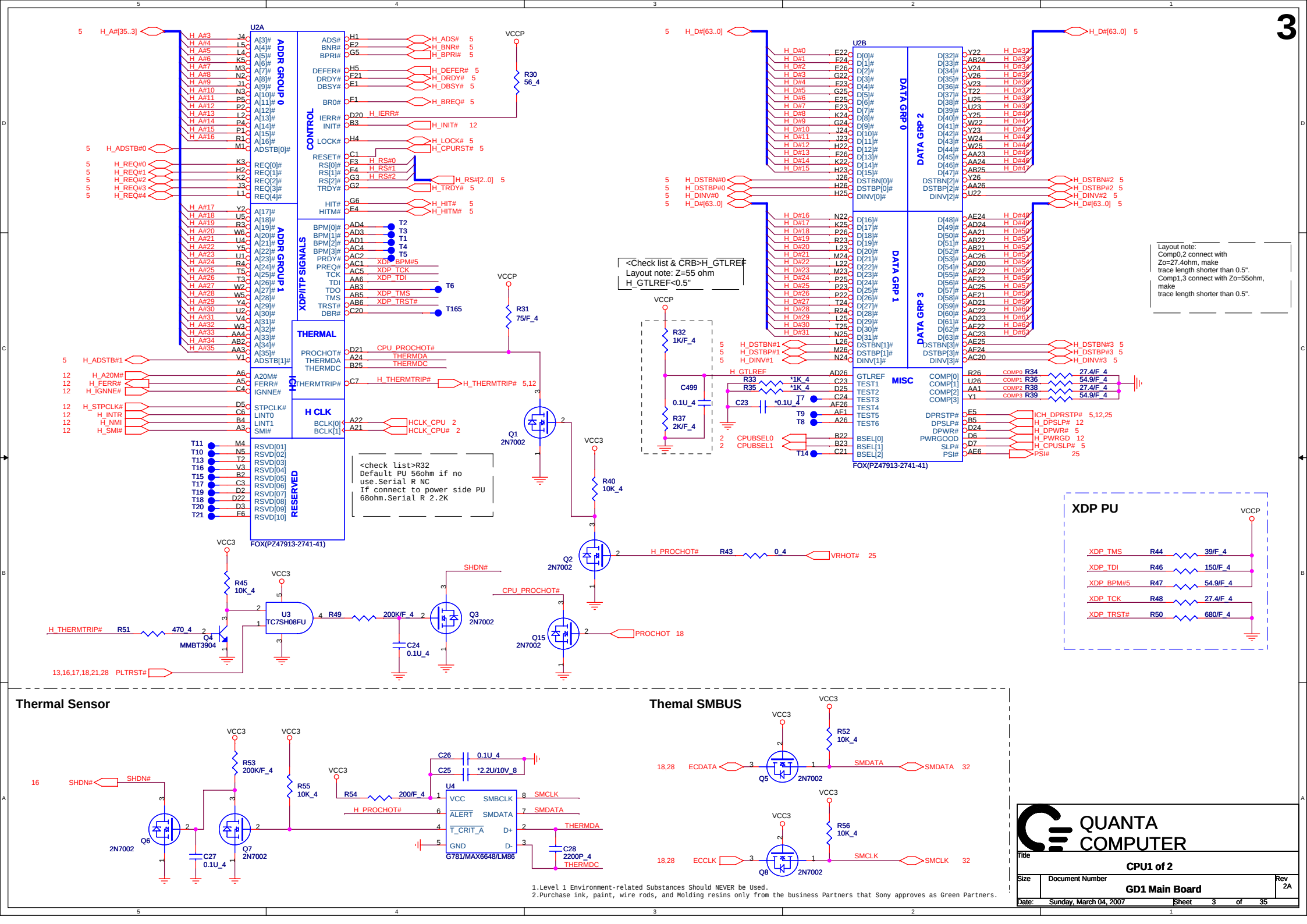
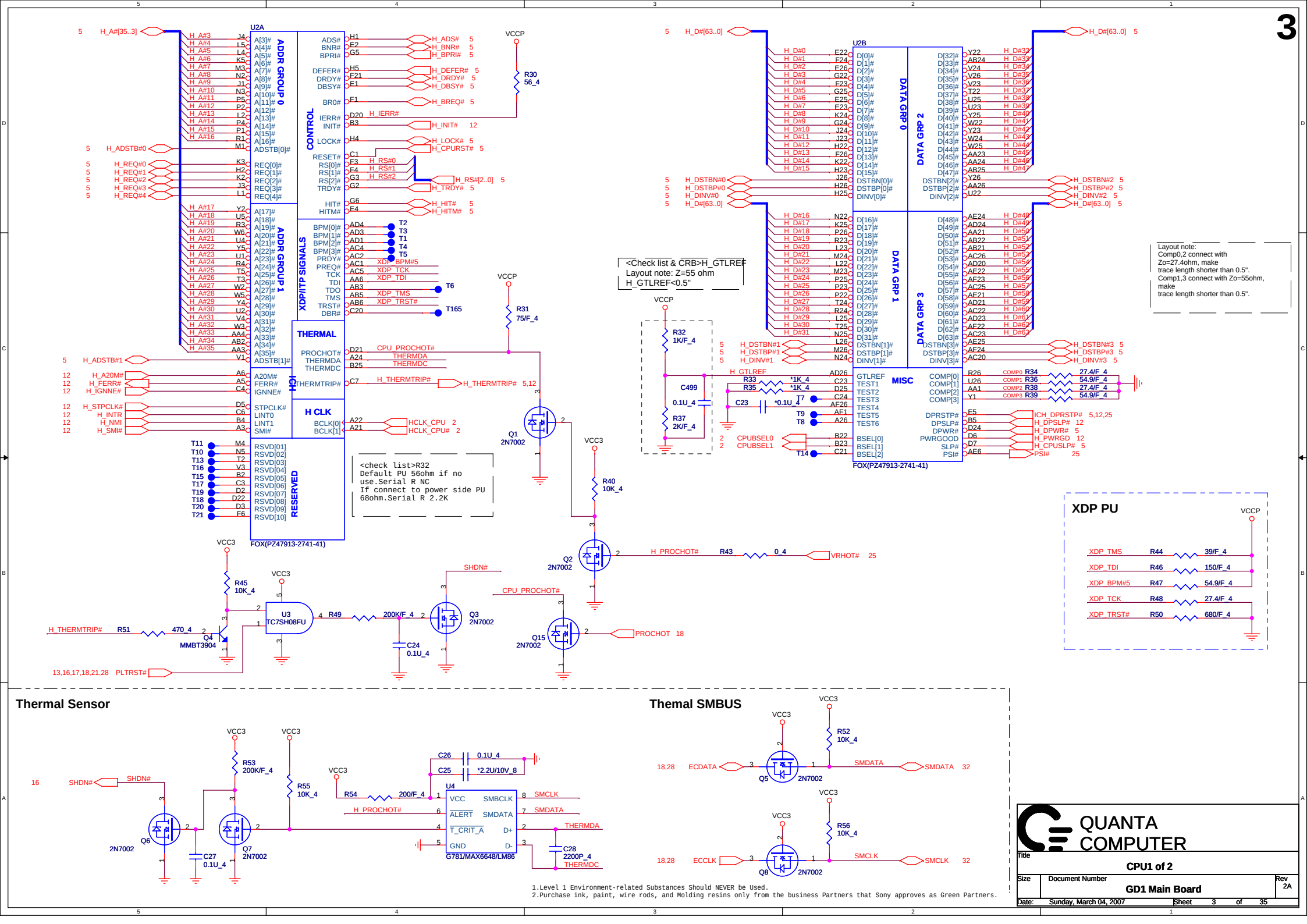
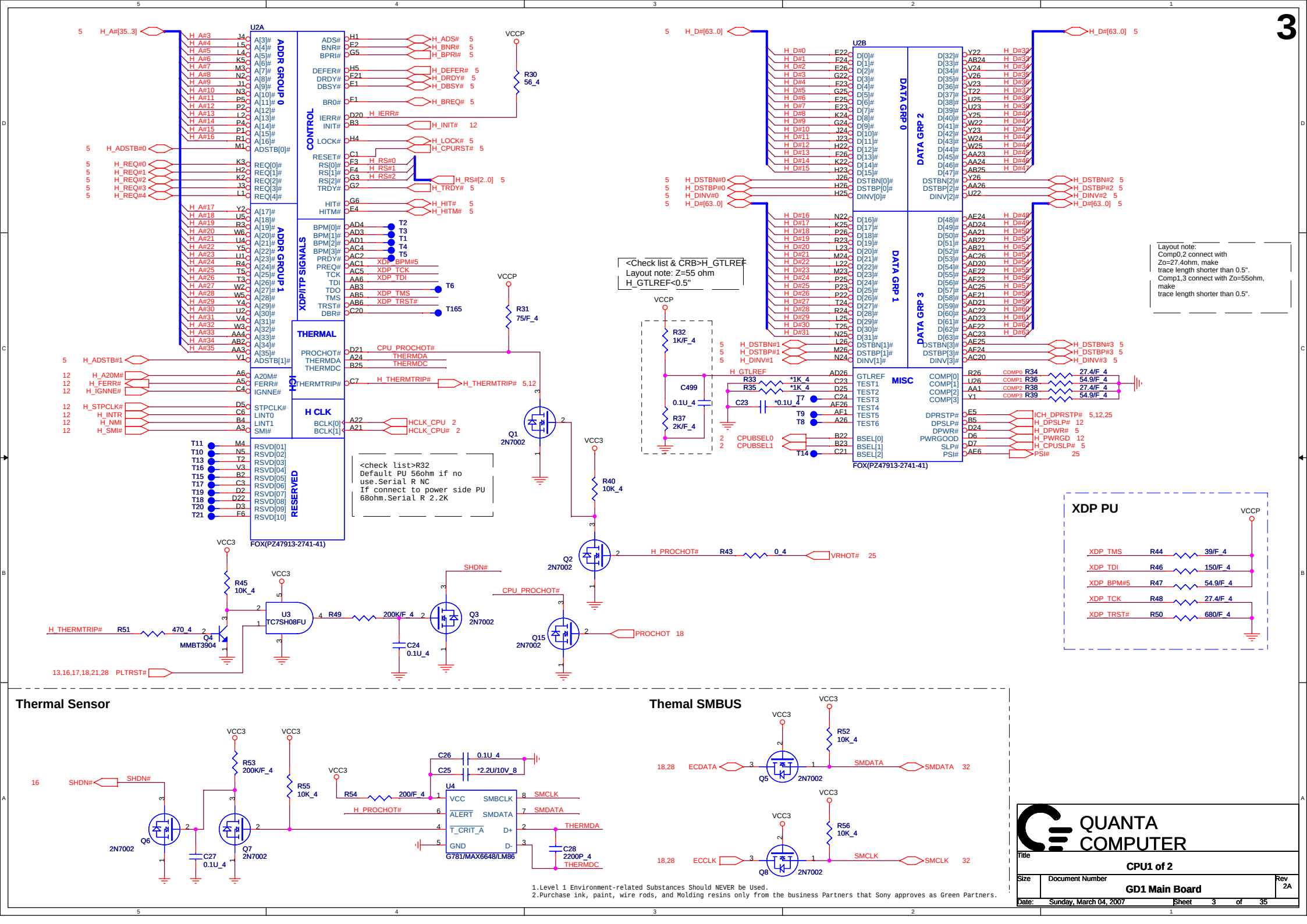
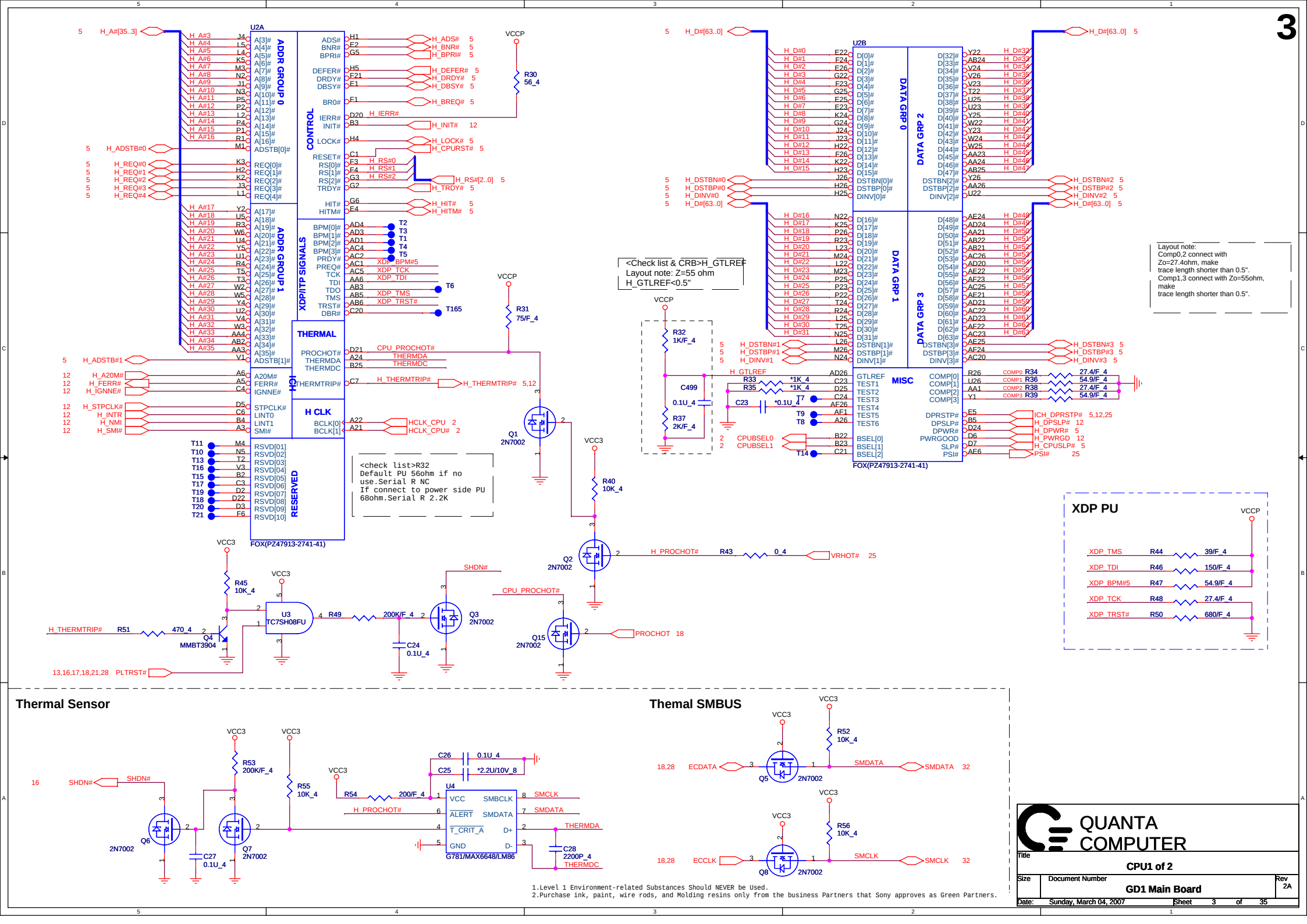
1

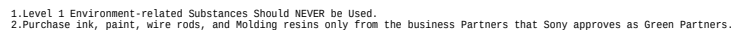


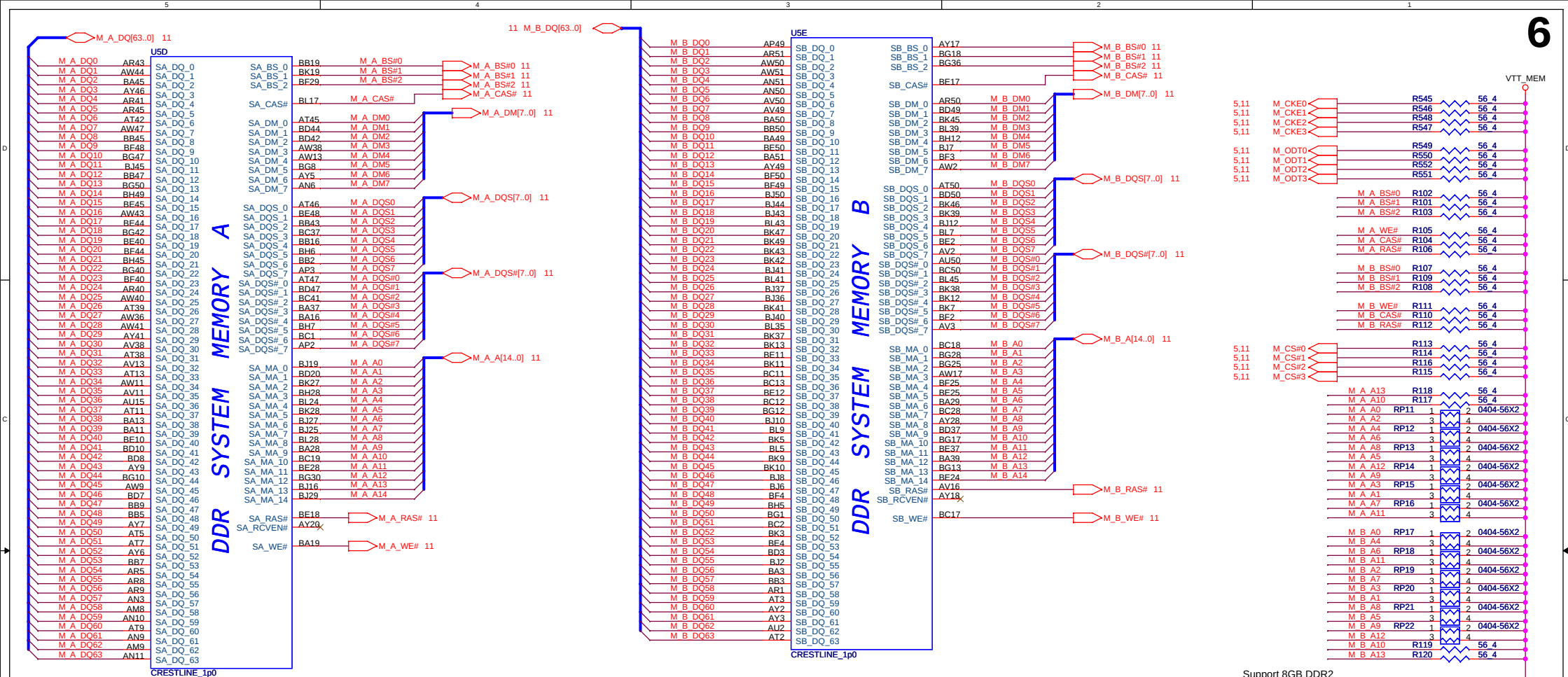
1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

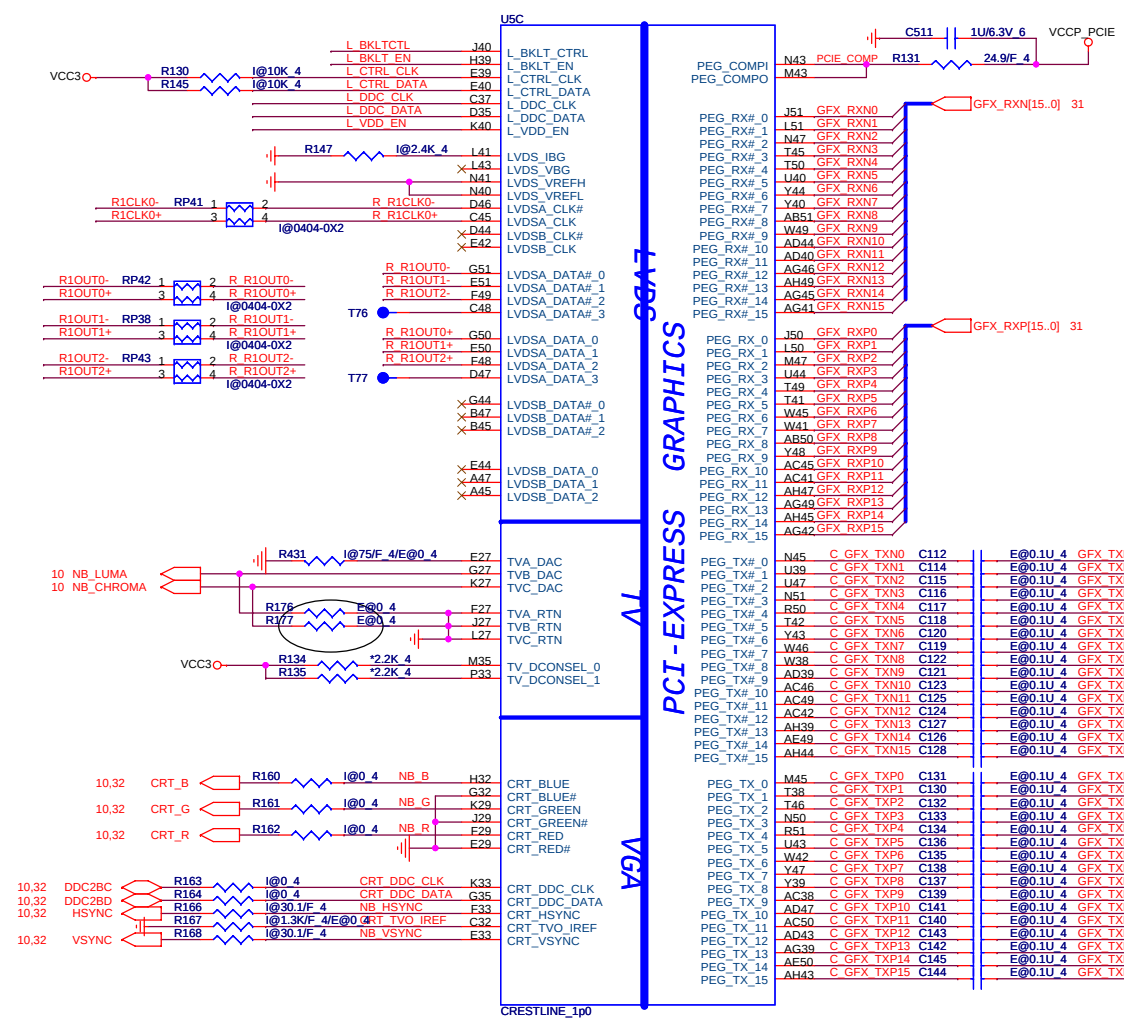
			
QUANTA COMPUTER			
Title			
Block Diagram			
Size B	Document Number		Rev 2A
GD1 Main Board			
Date:	Saturday, March 03, 2007	Sheet	1 of 35



[illegible][illegible]



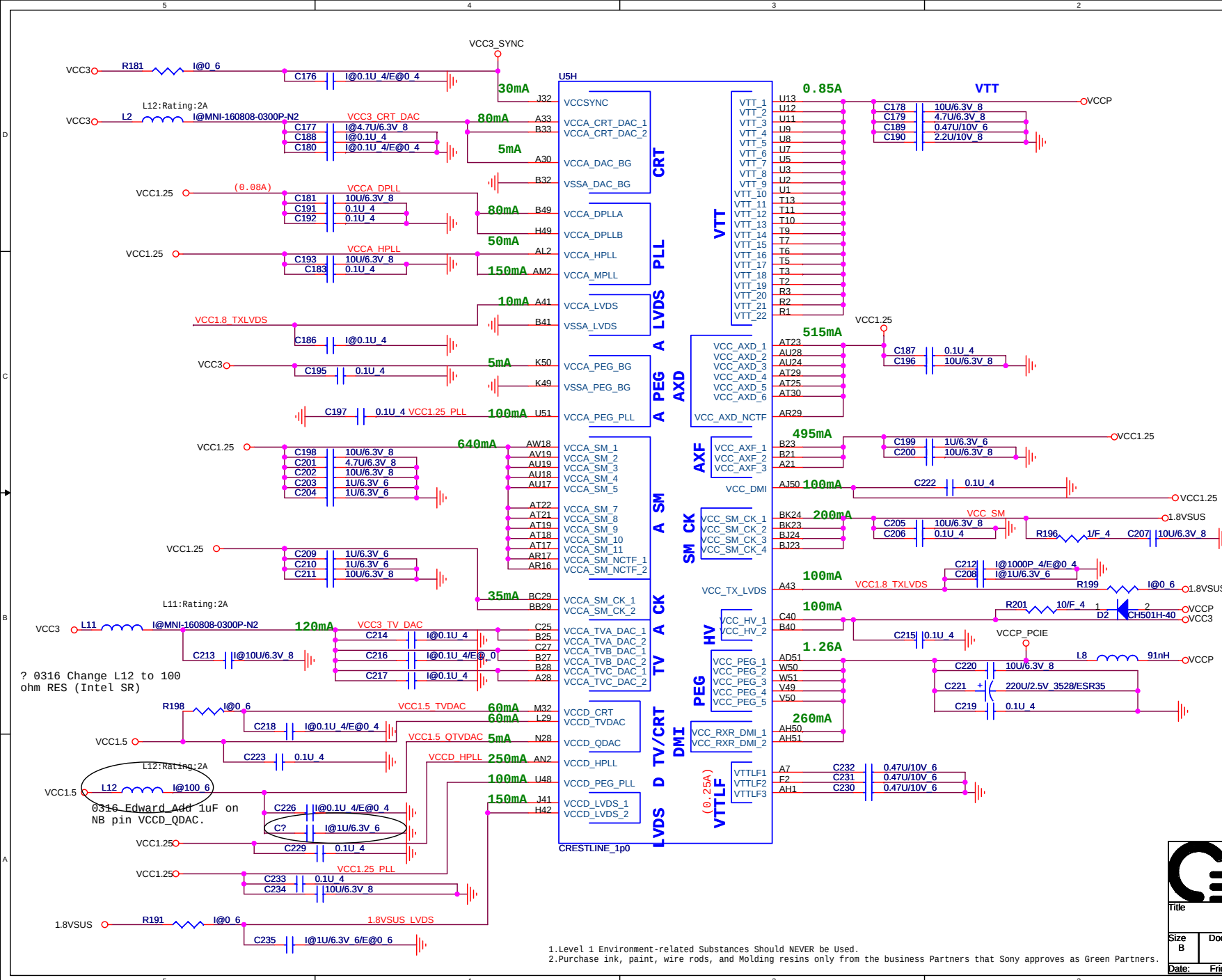




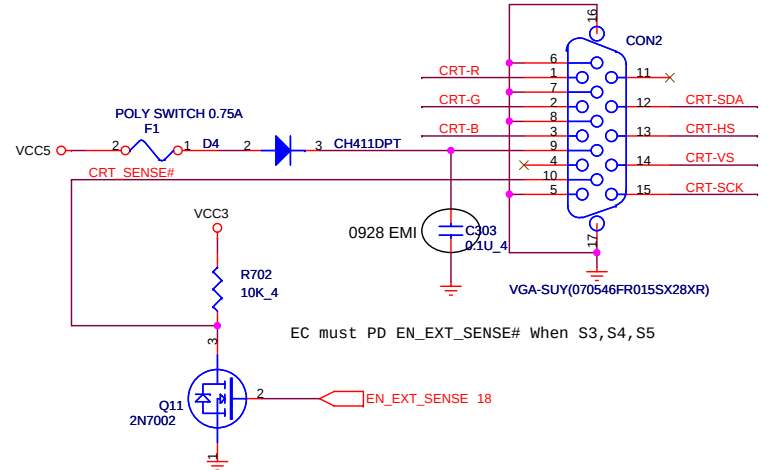
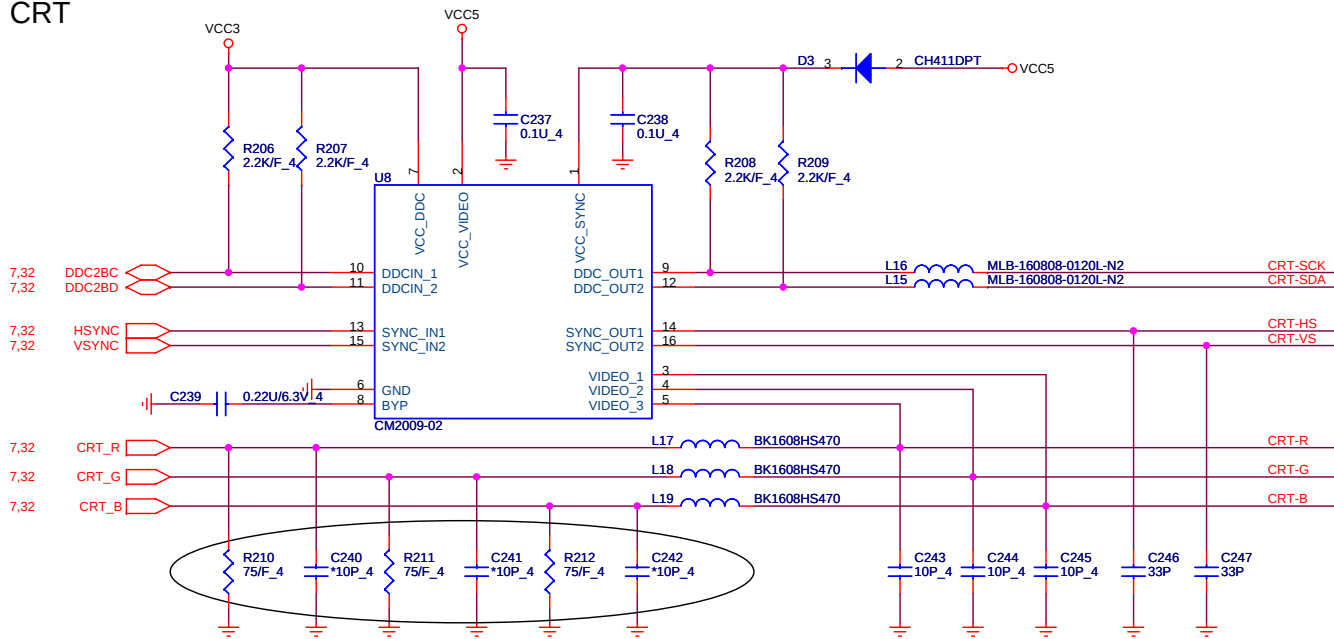
Signal	965GM	965PM
VCCA_DAC_BG	L8 C214 R181	V V X
VCCSYNC	R177 C201 C202 R178	V V X V
VCC3_CRT_DAC	L7 C203 C211 R179	V V V V
VCCD_LVDS	R198 C265 C264 R199	V V V V
VCCA_LVDS	R183 C208 C209 R184	0_4 0_01u_4 0_1u_4 NC
VCC_TX_LVDS	R192 C240 C238 R191	V V V V



Title			
GMCH Power 2 5 of 5			
Size B	Document Number		Rev 2A
GD1 Main Board			
Date:	Friday, March 16, 2007	Sheet	9 of 35

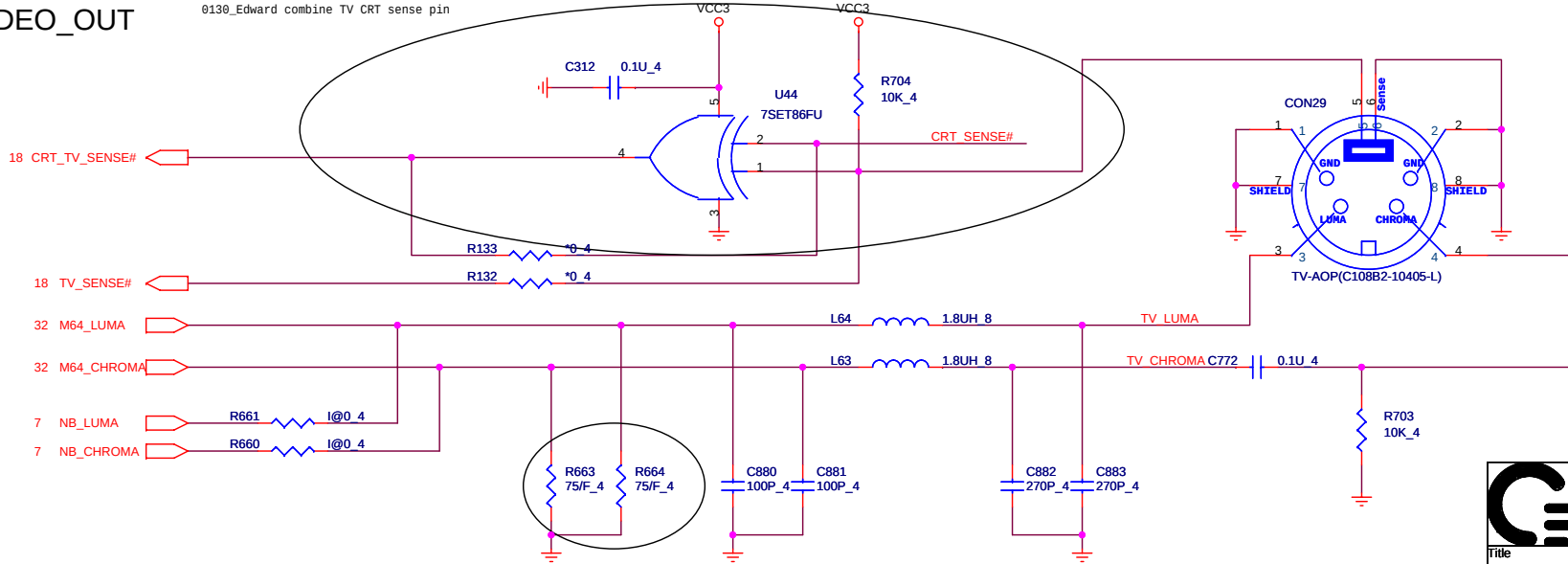


- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners



S-VIDEO_OUT

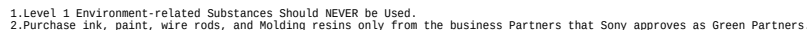
0130_Edward combine TV CRT sense pin

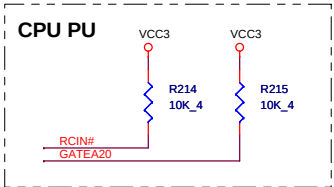


QUANTA
COMPUTER

Title		
CRT & S-VIDEO		
Size	Document Number	Rev
B	GD1 Main Board	2A
Date:	Sunday, March 04, 2007	Sheet 10 of 35

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.





ICH_RSVD0	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal operation(Default)
1	1	Set PCIe port config bit 1

ICH8-M LAN100_SLP Strap
(Internal VR for VccLAN1_05 and VccCL1.05)

ICH_INTVRMEN	Low = Disable High = Enable(Default)
--------------	---

ICH8-M Internal VR Enable strap
(Internal VR for Vccsus1_05,VccSus1_5 and VccCL1_5)

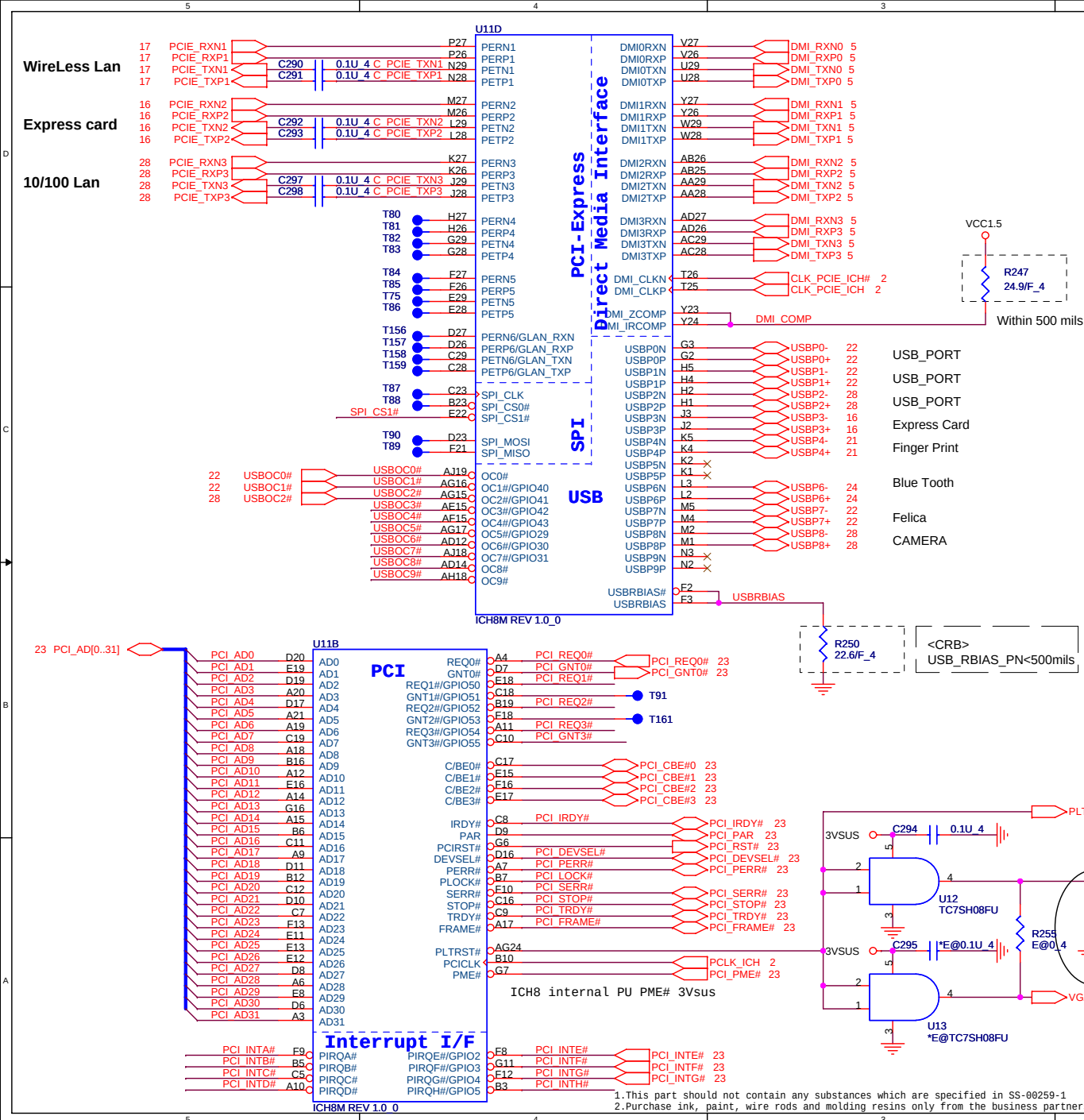
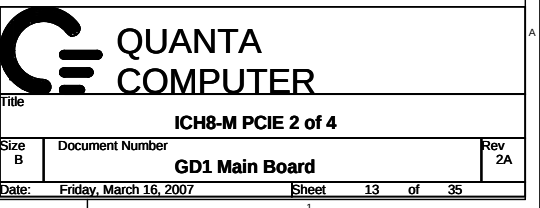
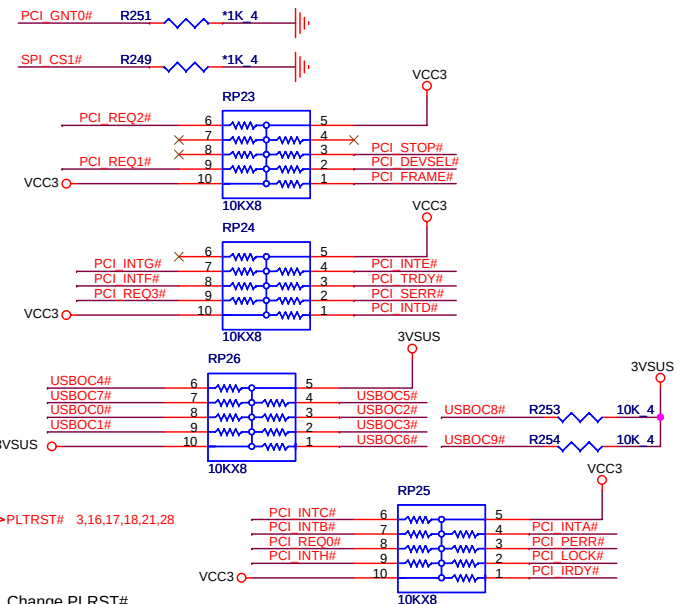
1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



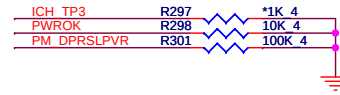
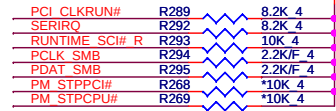
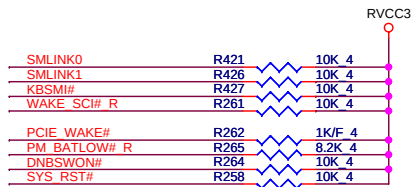
Title			
ICH8-M HOST 1 of 4			
Size	Document Number	Rev	
	GD1 Main Board	2A	
Date:	Friday, March 16, 2007	Sheet	12 of 35

PCI_GNT#3	Low = A16 swap override enabled High = Default
-----------	---

PCI_GNT#0	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC(Default)

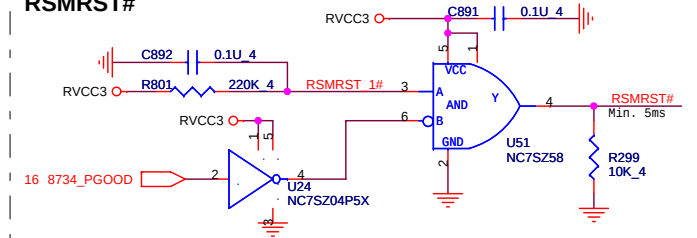


1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.



RSMRST# must be after RVCC3 10 ms

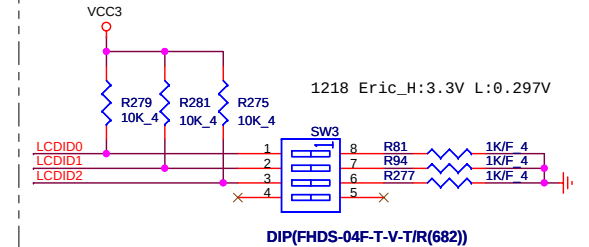
RSMRST#



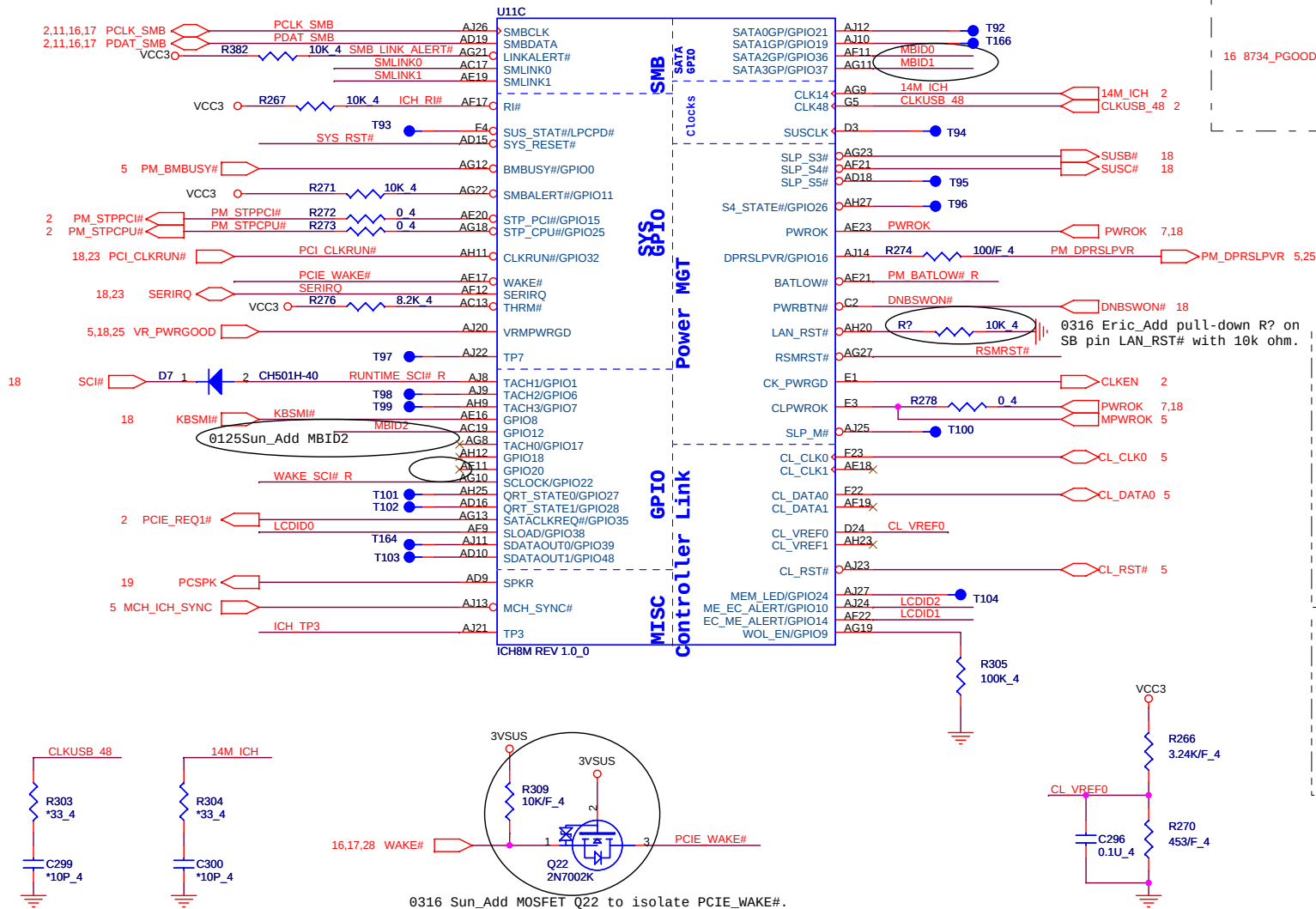
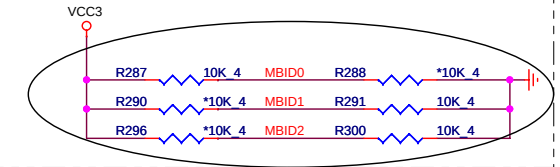
0303 Sun_Correct LCD ID setting.

LCDID	Panel Type
X001	AU0 -Q014TL0206
X010	CPT -CLAA141WB02A

LCD ID



MB ID



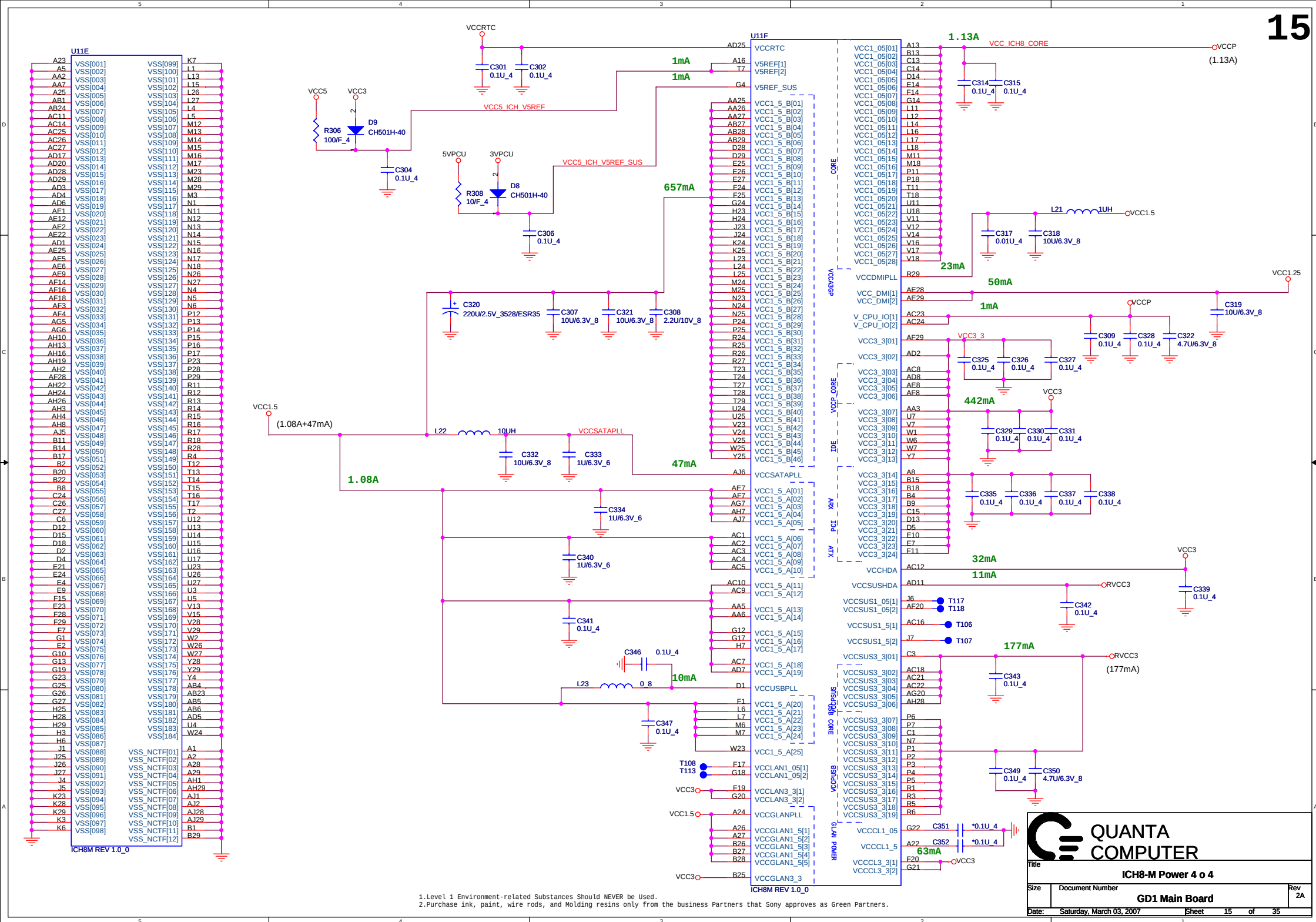
- Level 1 Environment-related Substances Should NEVER be Used.
- Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER

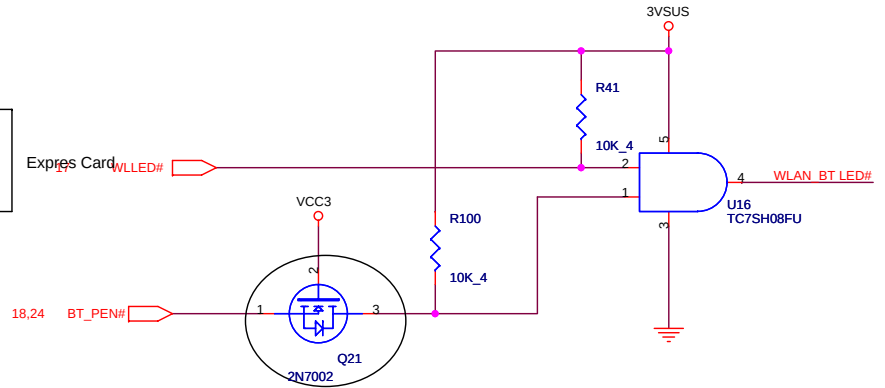
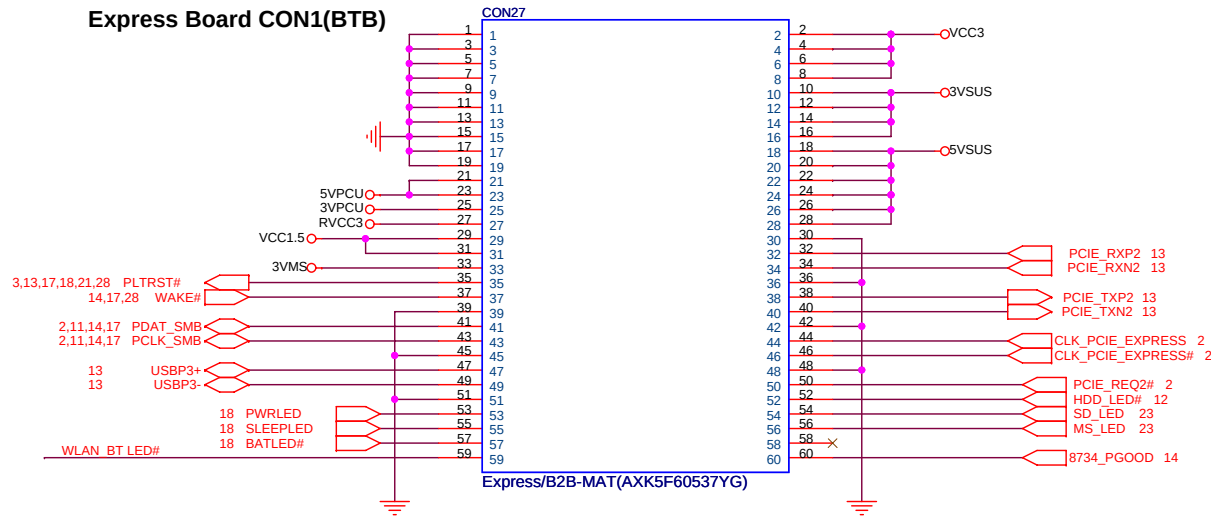
Title: ICH8-M GPIO 3 of 4

Size B Document Number: GD1 Main Board Rev 2A

Date: Friday, March 16, 2007 Sheet 14 of 35

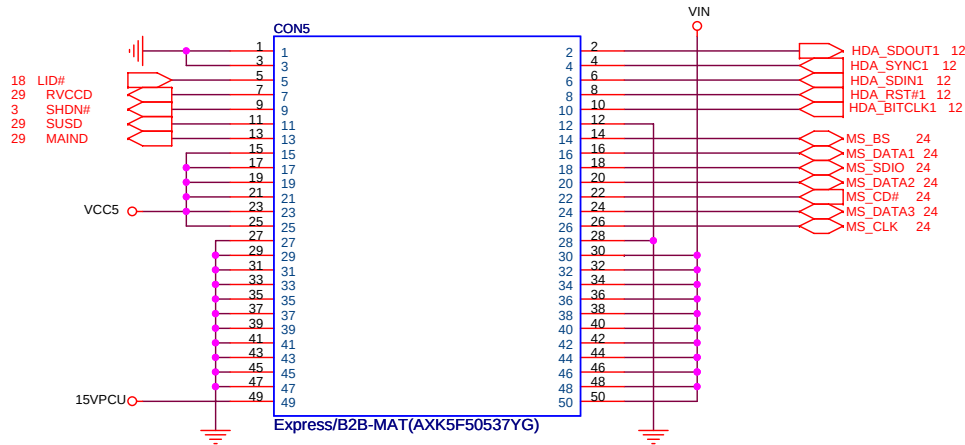


Express Board CON1(BTB)

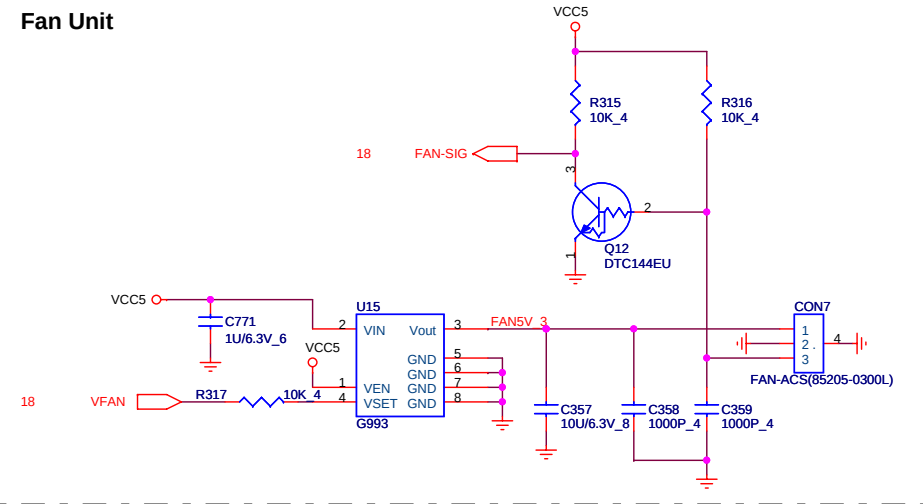


0129_Edward modify BT/WLAN LED circuit

Express Board CON2(BTB)



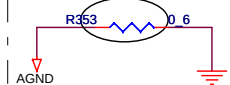
Fan Unit



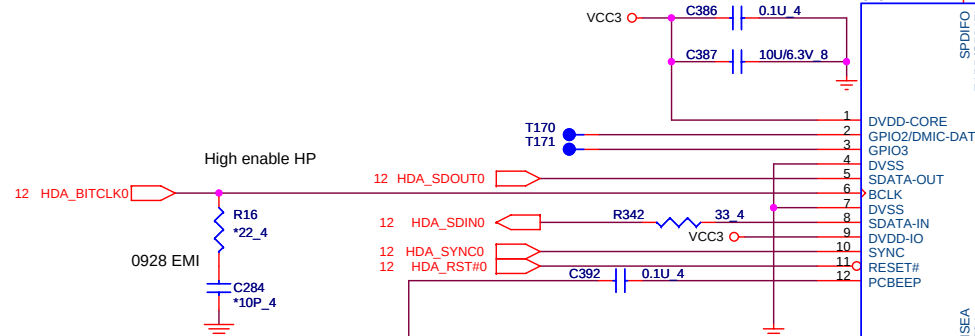
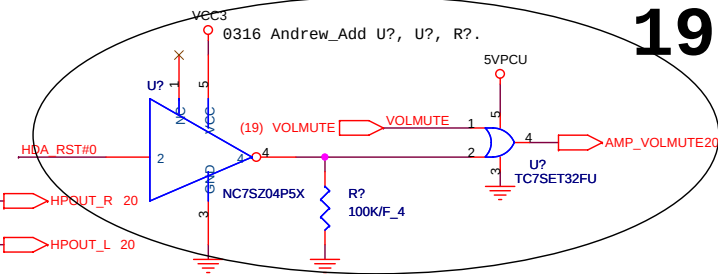
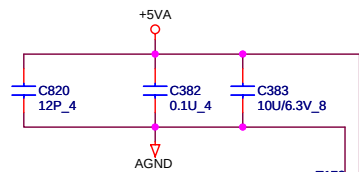
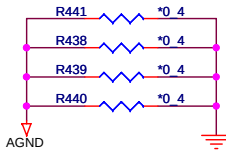
Title			Express Card
Size	Document Number		Rev
B	GD1 Main Board		2A
Date:	Sunday, March 04, 2007	Sheet	16 of 35

- This part should not contain any substances which are specified in SS-00259-1.
- Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

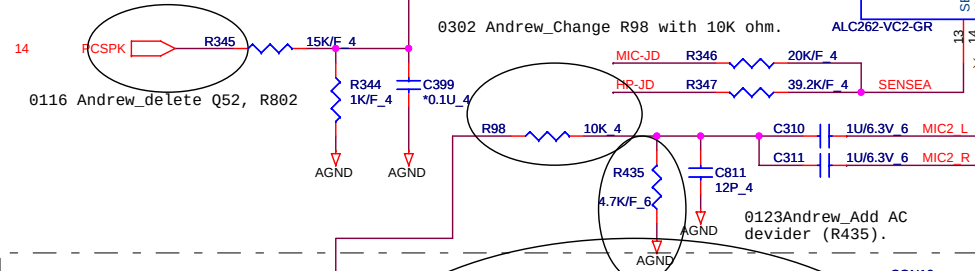
Audio decoupling



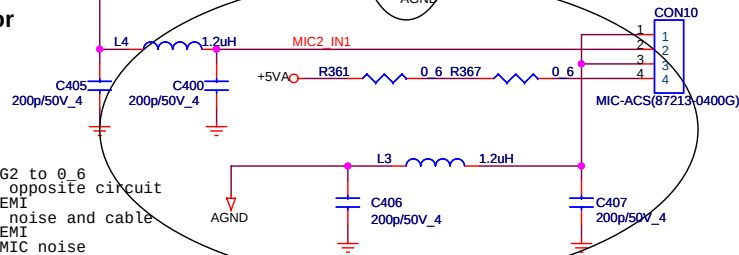
0116 Eric_change from EG31 to 0_6



ALC262

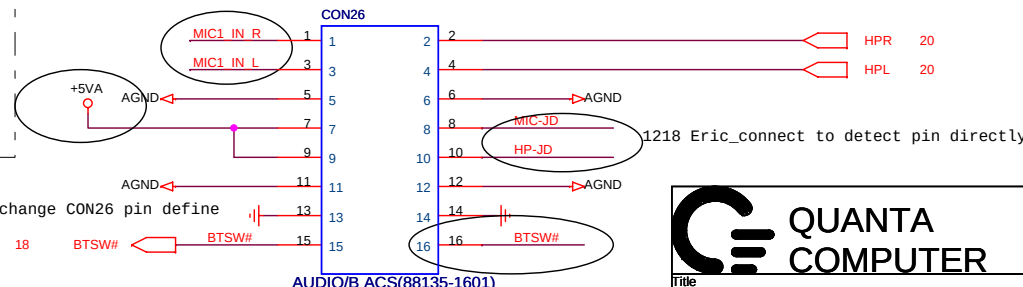


Analog MIC Connector



0116 Eric_change from EG2 to 0_6
 1218 Eric_delete detect opposite circuit
 0122 Andrew_change for EMI
 0123 Andrew_change for noise and cable
 0123 Andrew_change for EMI
 0123 Andrew_change for MIC noise
 0123Andrew_Place those components to close to connector.

Audio/B CON(B2B)



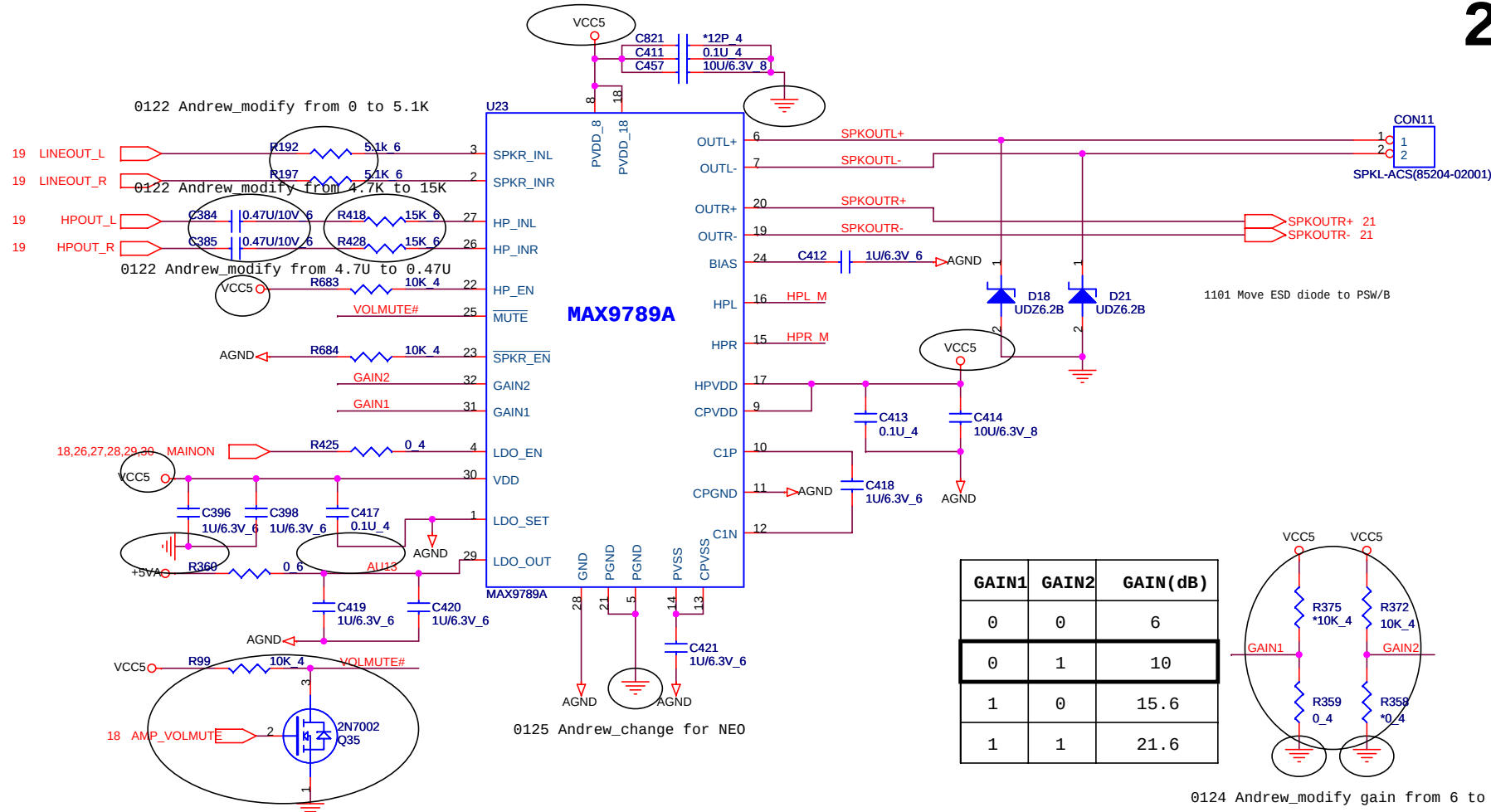
AUDIO/B ACS(88135-1601)

0316 Andrew_Change CON26 pin16 to BT SW#.

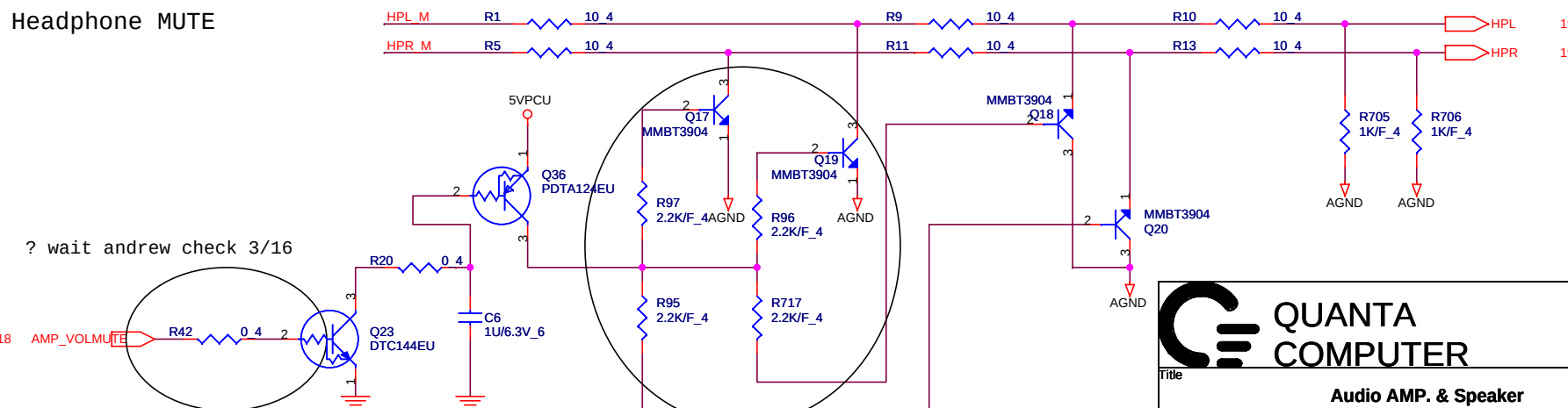
QUANTA COMPUTER

Title			Audio ALC268	
Size	Custom	Document Number	GD1 Main Board	
Date:	Friday, March 16, 2007	Sheet	19	of 35
		Rev	1A	

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



Headphone MUTE

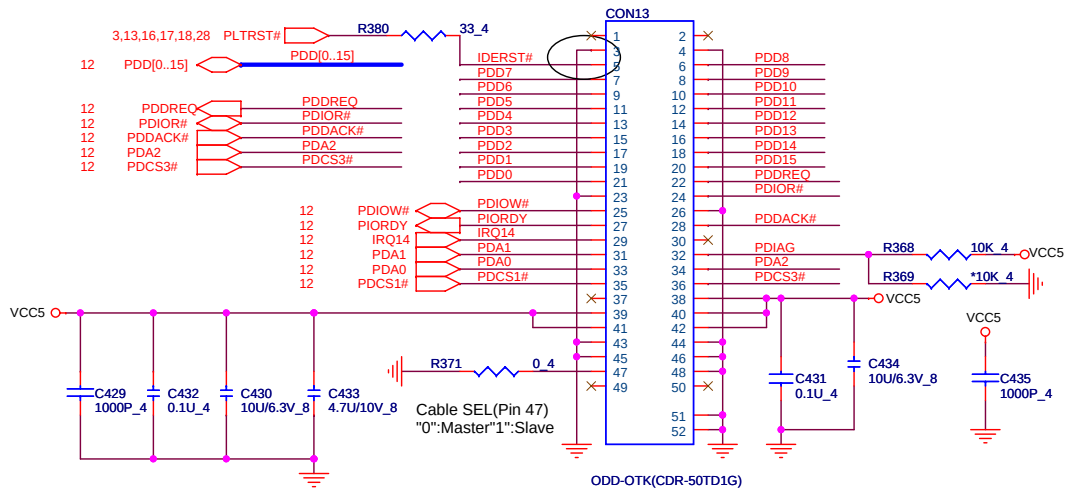


1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

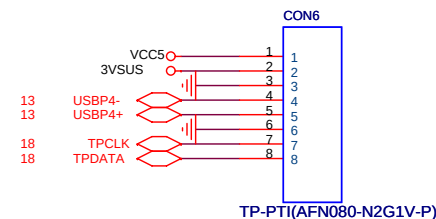
QUANTA COMPUTER		
Title		
Audio AMP. & Speaker		
Size	Document Number	Rev
Custom	GD1 Main Board	1A
Date:	Friday, March 16, 2007	Sheet 20 of 35

ODD CONNECTOR

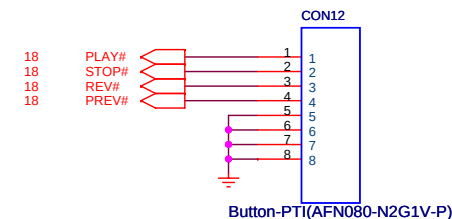
0316 Sun_Connect ODD pin3 to GND.
(NEO suggestion)



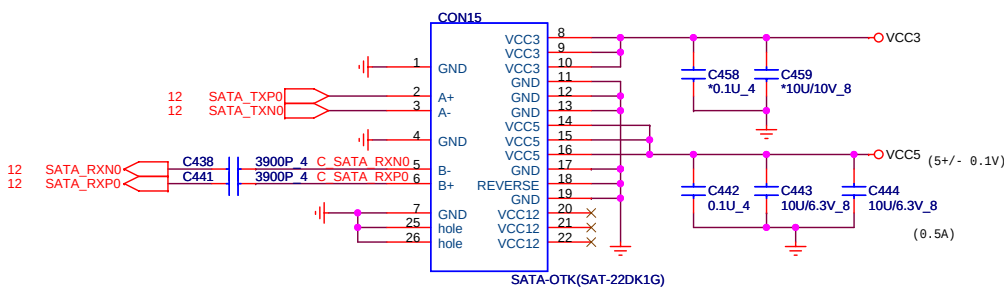
TouchPAD Board CON(FPC)



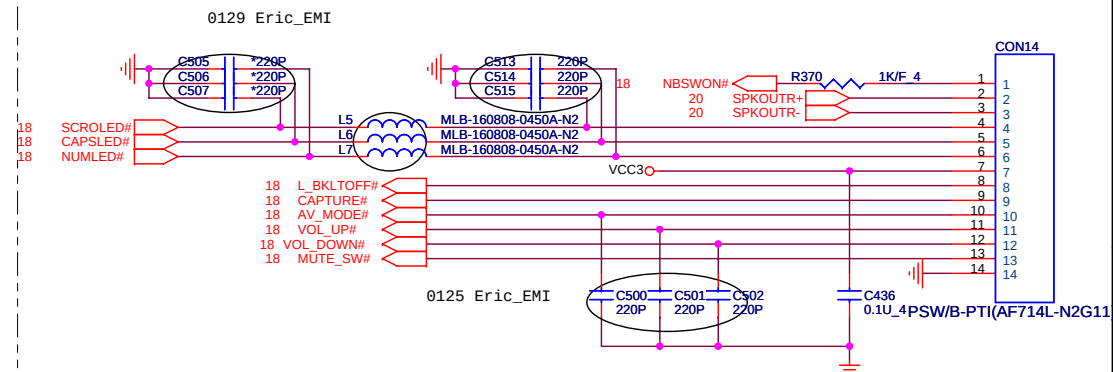
Button CON(FFC)



HDD CONNECTOR



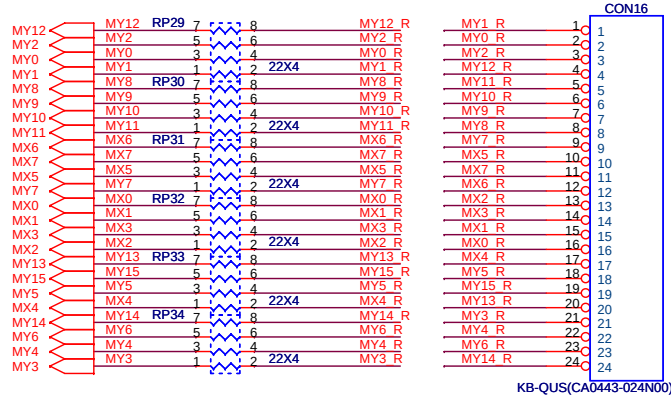
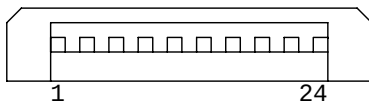
PSW Board CON(Inverse FFC)



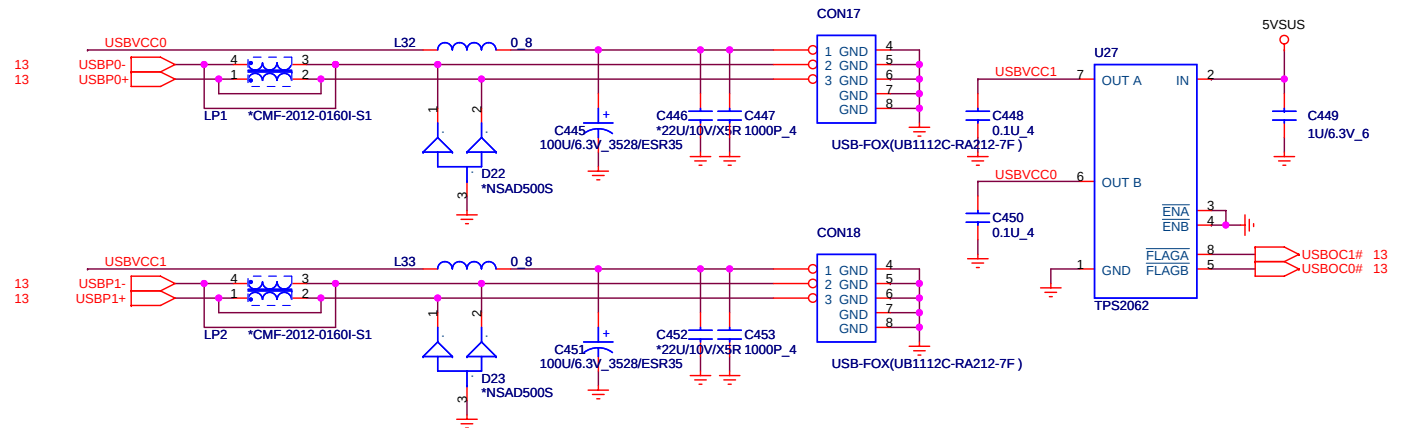
Title		
HDD/ODD/PSW/BUT/TP		
Size	Document Number	Rev
B		2A
GD1 Main Board		
Date:	Friday, March 16, 2007	Sheet 21 of 35

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

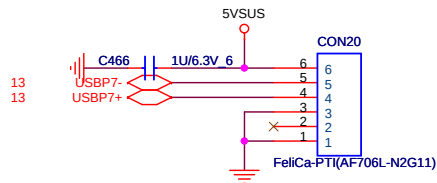
Keyboard



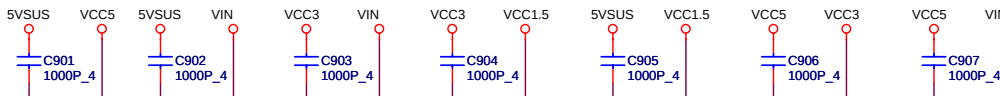
USB Port x 2



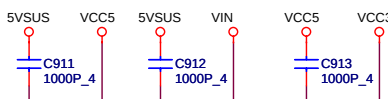
Felica



ODD

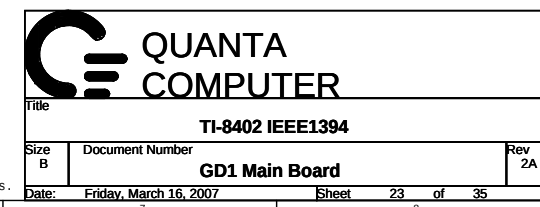


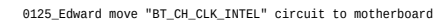
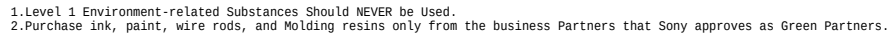
PCI Bus



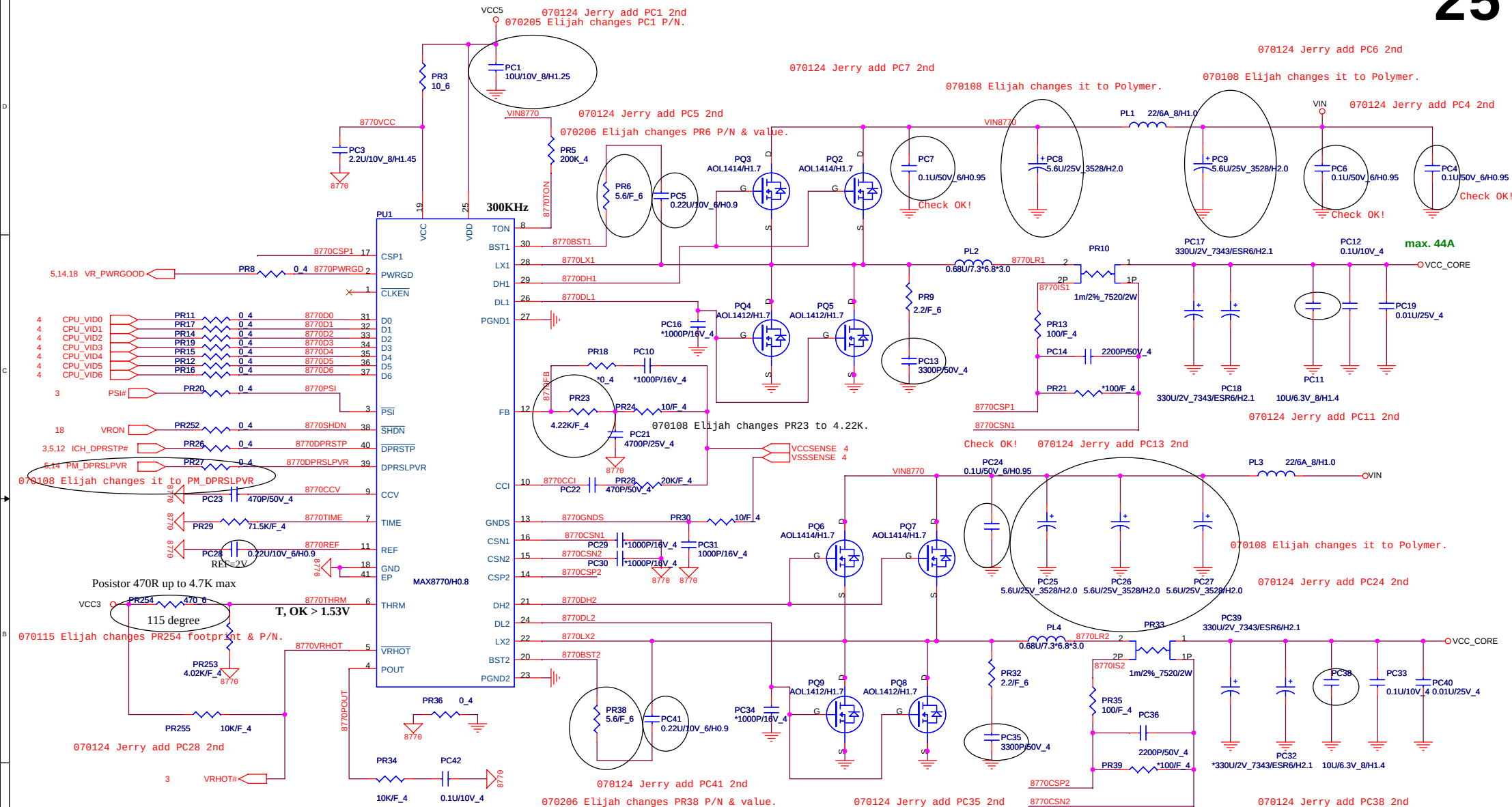
- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER	
Title: K/B ,USB Device	
Size B	Document Number: GD1 Main Board
Date: Sunday, March 04, 2007	Rev 2A
Sheet 22	of 35

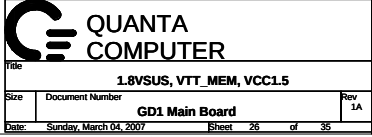




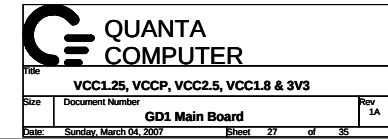
Sheet 24 of 35



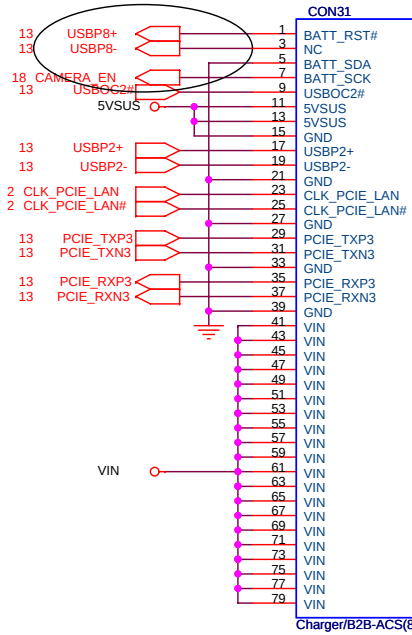
Title		VCC_CORE	
Size	Document Number	GD1 Main Board	
Custom			
Date:	Sunday, March 04, 2007	Sheet	25 of 35
		Rev	1A



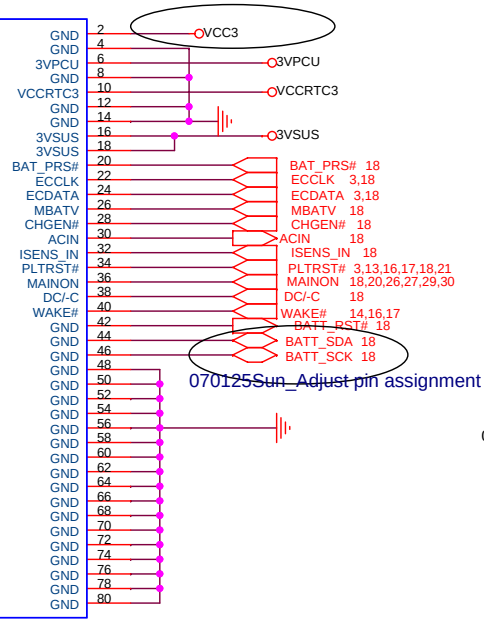
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



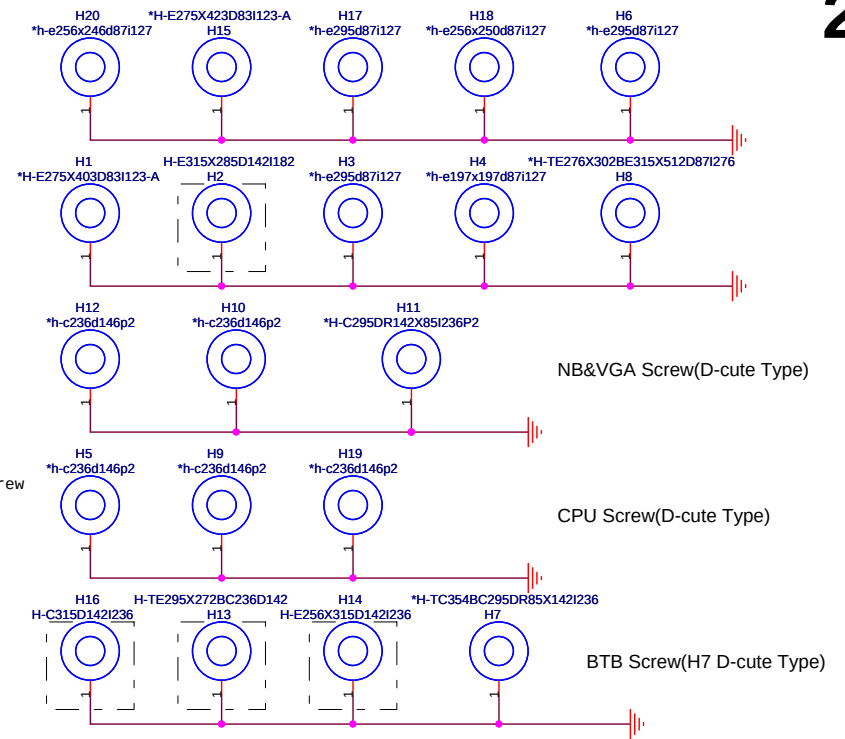
070125Sun_Adjust pin assignment



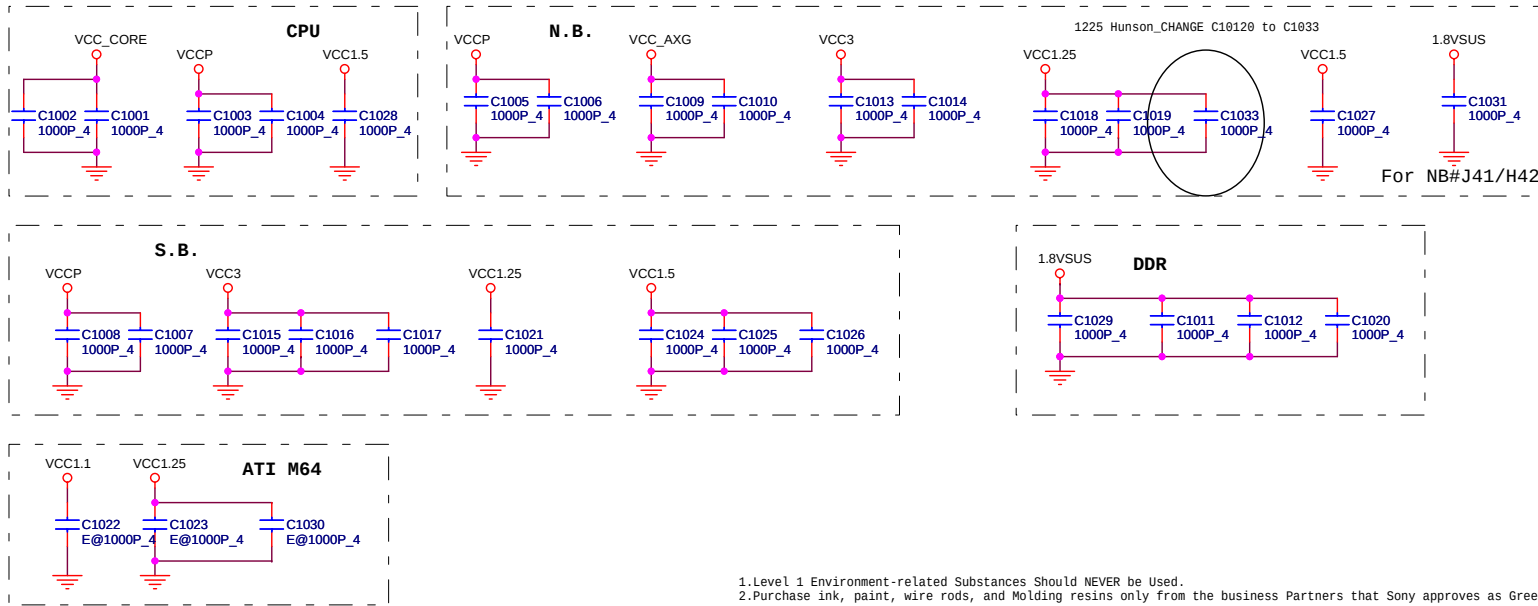
070125Sun_Adjust pin assignment



070125Sun_Adjust pin assignment

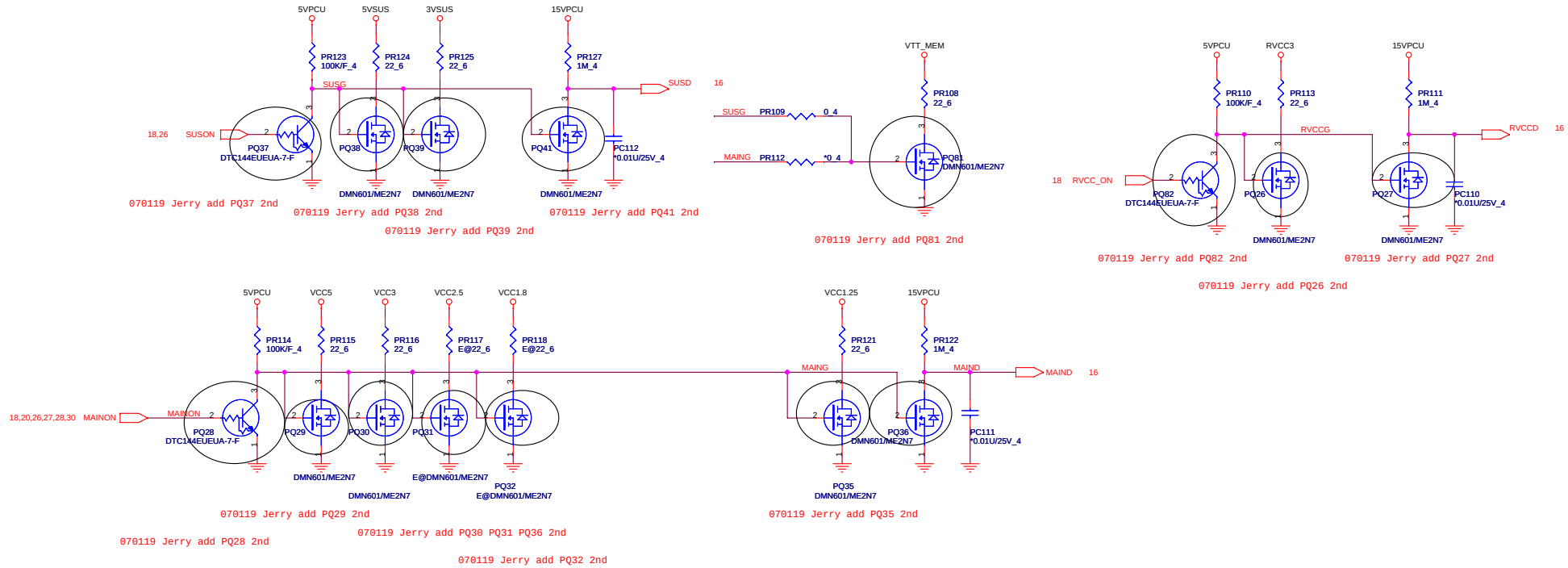


0109 Eric_modify Screw



- Level 1 Environment-related Substances Should NEVER be Used.
- Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER			
Title: Power Charger & LAN CON			
Size B	Document Number		Rev 1A
GD1 Main Board			
Date: Friday, March 16, 2007	Sheet 28 of 35		

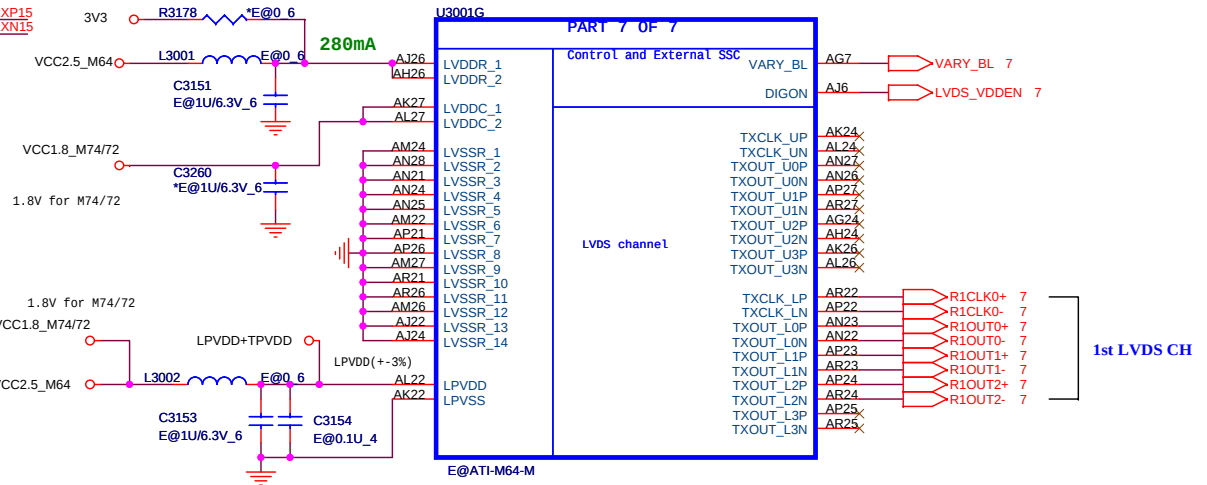
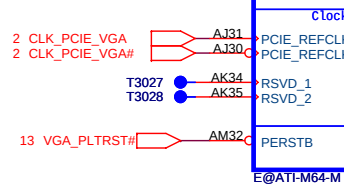
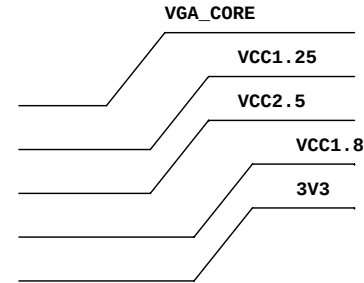


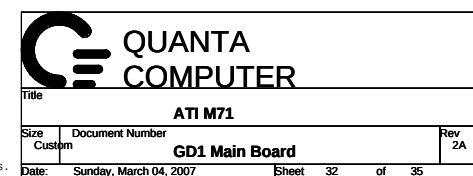
1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 QUANTA COMPUTER		
Title Discharge, SUSG, RVCCD, MAIND		
Size Custom	Document Number GD1 Main Board	Rev 1A
Date: Friday, March 16, 2007 Sheet 29 of 35		

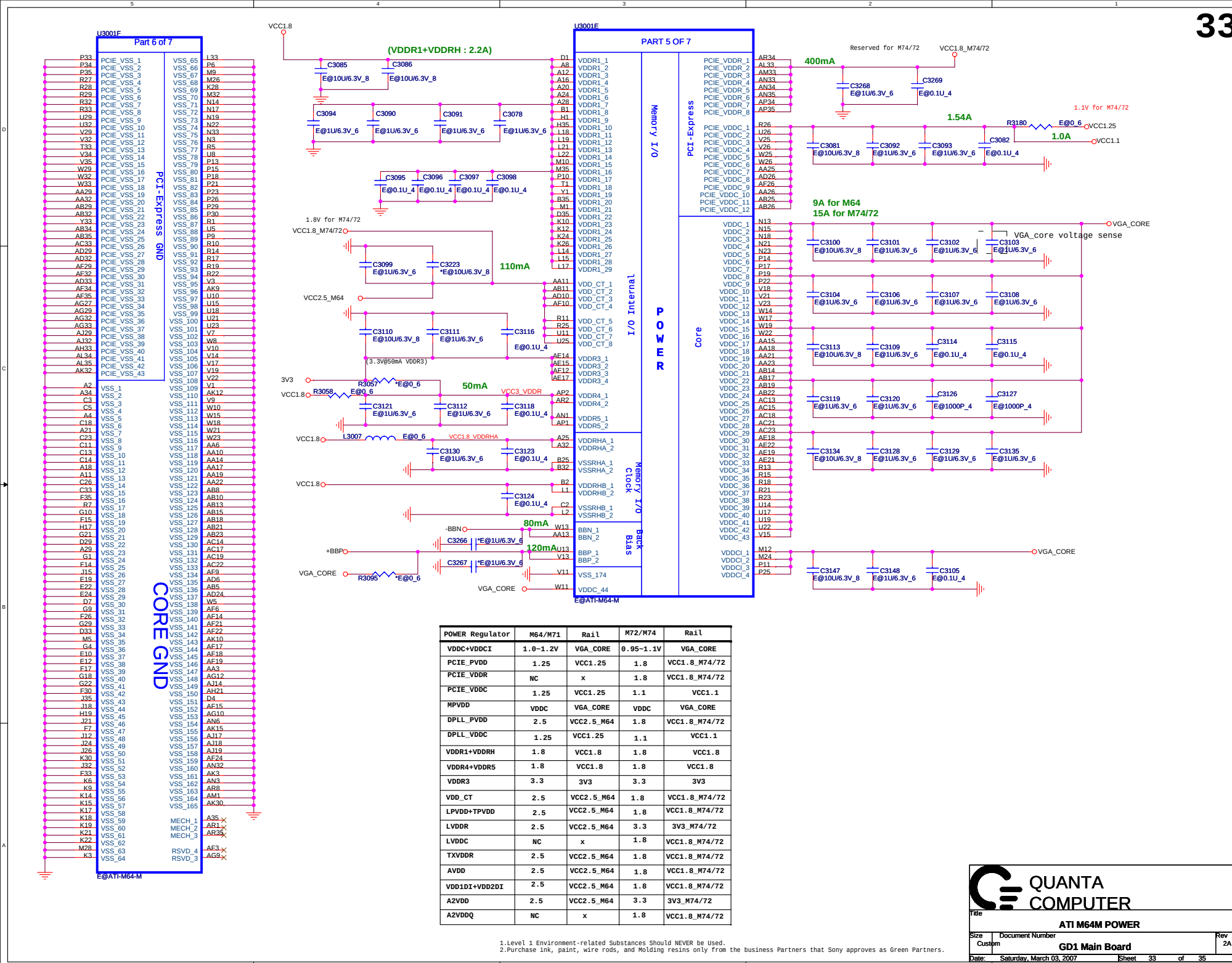


2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



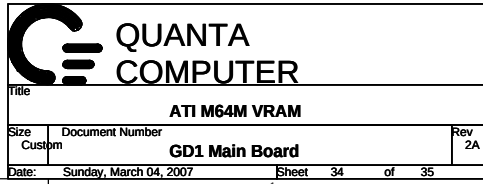


1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners



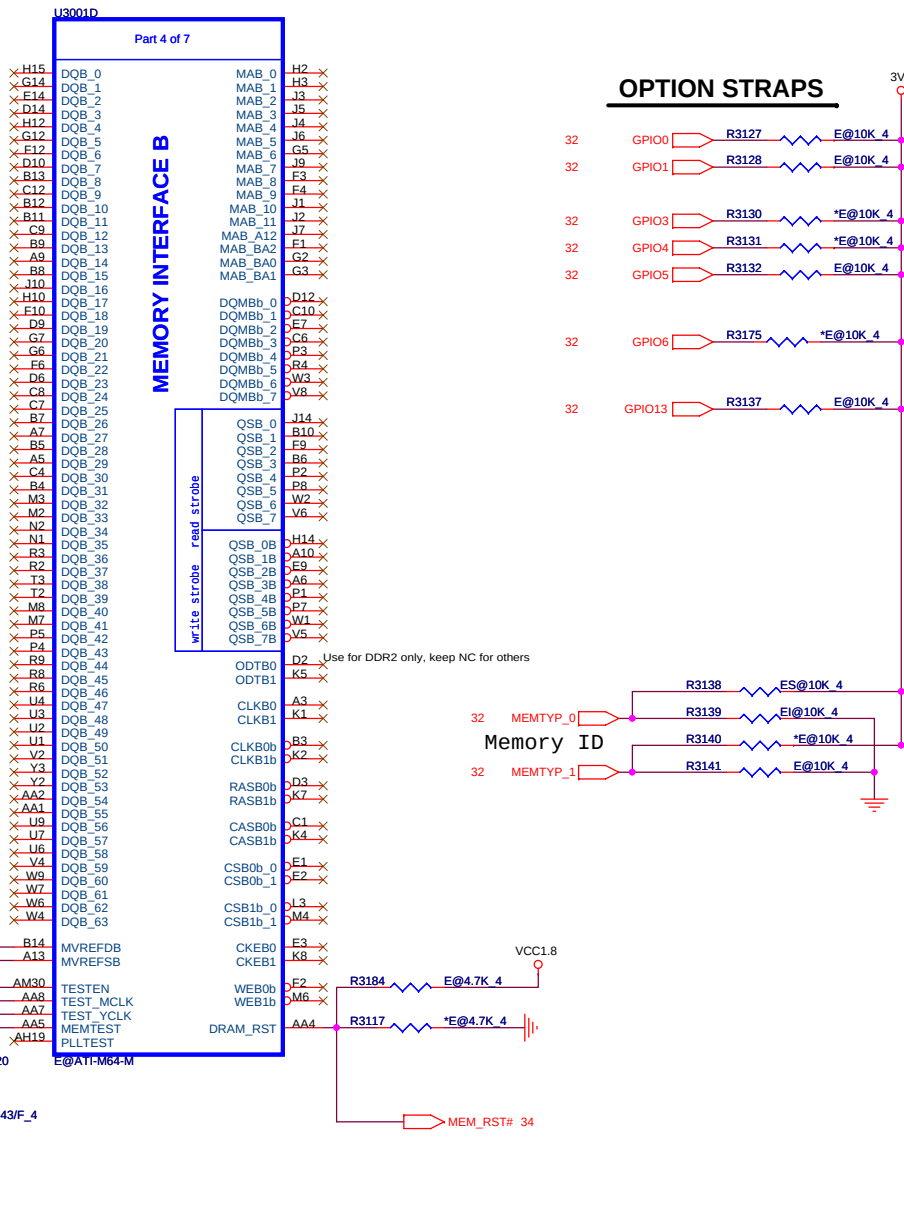
POWER Regulator	M64/M71	Rail	M72/M74	Rail
VDDC+VDDCI	1.0-1.2V	VGA_CORE	0.95-1.1V	VGA_CORE
PCIE_PVDD	1.25	VCC1.25	1.8	VCC1.8_M74/72
PCIE_VDDR	NC	x	1.8	VCC1.8_M74/72
PCIE_VDDC	1.25	VCC1.25	1.1	VCC1.1
MPVDD	VDDC	VGA_CORE	VDDC	VGA_CORE
DPLL_PVDD	2.5	VCC2.5_M64	1.8	VCC1.8_M74/72
DPLL_VDDC	1.25	VCC1.25	1.1	VCC1.1
VDDR1+VDDRH	1.8	VCC1.8	1.8	VCC1.8
VDDR4+VDDR5	1.8	VCC1.8	1.8	VCC1.8
VDDR3	3.3	3V3	3.3	3V3
VDD_CT	2.5	VCC2.5_M64	1.8	VCC1.8_M74/72
LPVDD+TPVDD	2.5	VCC2.5_M64	1.8	VCC1.8_M74/72
LVDDR	2.5	VCC2.5_M64	3.3	3V3_M74/72
LVDDC	NC	x	1.8	VCC1.8_M74/72
TXVDDR	2.5	VCC2.5_M64	1.8	VCC1.8_M74/72
AVDD	2.5	VCC2.5_M64	1.8	VCC1.8_M74/72
VDD1DI+VDD2DI	2.5	VCC2.5_M64	1.8	VCC1.8_M74/72
A2VDD	2.5	VCC2.5_M64	3.3	3V3_M74/72
A2VDDQ	NC	x	1.8	VCC1.8_M74/72

2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners



1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

Channel B



M64-M Strap

STRAPS	PIN	DESCRIPTION	ASIC DEFAULT
TX_PWRS_ENB	GPIO0	Transmitter Power Savings Enable 0: 50% Tx output swing 1: full Tx output swing	0 (internal pull-down)
TX_DEEMPH_EN	GPIO1	(recommended) Transmitter De-emphasis Enable 0: Tx de-emphasis disabled 1: Tx de-emphasis enabled (recommended)	0 (internal pull-down)
Reserved	GPIO(3:2)	ATI internal use only. Other logic must not affect this signal during RESET.(GPIO2=VDD_VCL)	00
DEBUG_ACCESS	GPIO4	Strap to set the debug muxes to bring out DEBUG signals even if registers are inaccessible	0 (internal pull-down)
PLL_IBIAS_RD	GPIO[6:5]	Bias Current for the PCI Express PHY PLL	01
Reserved	GPIO8	ATI internal use only. Other logic must not affect this signal during RESET.(Serial ROM 0 Output from ROM)	0
ROMIDCFG(3:0)	GPIO(9,13:11)	If no ROM attached, controls chip IDIs. If rom attached identifies ROM type 000x - No ROM, MEM_AP_SIZE=00 128MB 001x - No ROM, MEM_AP_SIZE=01 256MB 010x - No ROM, MEM_AP_SIZE=10 64MB(Default) 011x - No ROM, MEM_AP_SIZE=11 Reserved 1000 - Parallel ROM, chip IDIs from ROM 1001 - Serial AT25F1024 ROM (Atmel), chip IDIs from ROM 1010 - Serial AT45DB011 ROM (Atmel), chip IDIs from ROM 1011 - Serial M25P10 ROM (ST), chip IDIs from ROM 1100 - Serial M25P05 ROM (ST), chip IDIs from ROM 1100 - Serial NX25F011B ROM (ISSI), chip IDIs from ROM	GPIO[9,13,12,11] 0000 (internal pull-down)
VIP_DEVICE	VSYNC	Indicates if any slave VIP host devices drove this pin low during reset. 0- Slave VIP host port device present. 1-No slave VIP port devices reporting presence during reset	0 (internal pull-down)
Reserved	PCIE_TEST	ATI internal use only. Other logic must not affect this signal during RESET.(Serial ROM 0 Output from ROM)	0
Reserved	HSYNC	ATI internal use only. Other logic must not affect this signal during RESET.(Serial ROM 0 Output from ROM)	0

VRAM Straps

REV. 0.1

STRAPS	PIN	DESCRIPTION	VALUE
MEMTYPE[1:0]	GPIO 27,26	Memory identification for BIOS 00 - Infineon GDDR3 : 2M x 32 bits x 4 banks(Default) 01 - SAMSUNG GDDR3 : 2M x 32 bits x 4 banks 10 - RESERVED 11 - RESERVED	00

MEMTYPE1	MEMTYPE0	R3140	R3141	R3138	R3139	
0	0	NC	10K	NC	10K	00 - Infineon GDDR3 : 2M x 32 bits x 4 banks
0	1	NC	10K	10K	NC	01 - SAMSUNG GDDR3 : 2M x 32 bits x 4 banks
1	0	10K	NC	10K	10K	10 - RESERVED
1	1	10K	NC	10K	NC	11 - RESERVED



**QUANTA
COMPUTER**

Title			ATI M64-M STRAP		
Size	Document Number	Rev			2A
Custom	GD1 Main Board				
Date:	Sunday, March 04, 2007	Sheet	35	of	35


```

Page02 6125 Eric_Aad CPU0B011 critical to support F33 CPU3
Page02 6126 Eric_Aad CPU0B011 critical to support F33 CPU3
Page02 6127 Eric_Aad modify #P1 #P4 #P5 #P6 to 22#2
Page02 6128 Hsuan_Nien add #P1 #P4 #P5 #P6 to 22#2
Page02 6129 Eric_Aad add #P1 #P4 #P5 #P6 to 22#2
Page02 6130 Eric_Aad add #P1 #P4 #P5 #P6 to 22#2
Page02 6131 Eric_Aad modify #P4 from 22 to 39
Page02 6132 Eric_Aad modify #P5 from 22 to 39
Page02 6133 Eric_Aad modify #P6 from 22 to 39
Page02 6134 Hsuan_Nien #P4 #P5 #P6 #P7 #P8 #P9 #P10 #P11 #P12 #P13 #P14 #P15 #P16 #P17 #P18 #P19 #P20 #P21 #P22 #P23 #P24 #P25 #P26 #P27 #P28 #P29 #P30 #P31 #P32 #P33 #P34 #P35 #P36 #P37 #P38 #P39 #P40 #P41 #P42 #P43 #P44 #P45 #P46 #P47 #P48 #P49 #P50 #P51 #P52 #P53 #P54 #P55 #P56 #P57 #P58 #P59 #P60 #P61 #P62 #P63 #P64 #P65 #P66 #P67 #P68 #P69 #P70 #P71 #P72 #P73 #P74 #P75 #P76 #P77 #P78 #P79 #P80 #P81 #P82 #P83 #P84 #P85 #P86 #P87 #P88 #P89 #P90 #P91 #P92 #P93 #P94 #P95 #P96 #P97 #P98 #P99 #P100 #P101 #P102 #P103 #P104 #P105 #P106 #P107 #P108 #P109 #P110 #P111 #P112 #P113 #P114 #P115 #P116 #P117 #P118 #P119 #P120 #P121 #P122 #P123 #P124 #P125 #P126 #P127 #P128 #P129 #P130 #P131 #P132 #P133 #P134 #P135 #P136 #P137 #P138 #P139 #P140 #P141 #P142 #P143 #P144 #P145 #P146 #P147 #P148 #P149 #P150 #P151 #P152 #P153 #P154 #P155 #P156 #P157 #P158 #P159 #P160 #P161 #P162 #P163 #P164 #P165 #P166 #P167 #P168 #P169 #P170 #P171 #P172 #P173 #P174 #P175 #P176 #P177 #P178 #P179 #P180 #P181 #P182 #P183 #P184 #P185 #P186 #P187 #P188 #P189 #P190 #P191 #P192 #P193 #P194 #P195 #P196 #P197 #P198 #P199 #P200 #P201 #P202 #P203 #P204 #P205 #P206 #P207 #P208 #P209 #P210 #P211 #P212 #P213 #P214 #P215 #P216 #P217 #P218 #P219 #P220 #P221 #P222 #P223 #P224 #P225 #P226 #P227 #P228 #P229 #P230 #P231 #P232 #P233 #P234 #P235 #P236 #P237 #P238 #P239 #P240 #P241 #P242 #P243 #P244 #P245 #P246 #P247 #P248 #P249 #P250 #P251 #P252 #P253 #P254 #P255 #P256 #P257 #P258 #P259 #P260 #P261 #P262 #P263 #P264 #P265 #P266 #P267 #P268 #P269 #P270 #P271 #P272 #P273 #P274 #P275 #P276 #P277 #P278 #P279 #P280 #P281 #P282 #P283 #P284 #P285 #P286 #P287 #P288 #P289 #P290 #P291 #P292 #P293 #P294 #P295 #P296 #P297 #P298 #P299 #P300 #P301 #P302 #P303 #P304 #P305 #P306 #P307 #P308 #P309 #P310 #P311 #P312 #P313 #P314 #P315 #P316 #P317 #P318 #P319 #P320 #P321 #P322 #P323 #P324 #P325 #P326 #P327 #P328 #P329 #P330 #P331 #P332 #P333 #P334 #P335 #P336 #P337 #P338 #P339 #P340 #P341 #P342 #P343 #P344 #P345 #P346 #P347 #P348 #P349 #P350 #P351 #P352 #P353 #P354 #P355 #P356 #P357 #P358 #P359 #P360 #P361 #P362 #P363 #P364 #P365 #P366 #P367 #P368 #P369 #P370 #P371 #P372 #P373 #P374 #P375 #P376 #P377 #P378 #P379 #P380 #P381 #P382 #P383 #P384 #P385 #P386 #P387 #P388 #P389 #P390 #P391 #P392 #P393 #P394 #P395 #P396 #P397 #P398 #P399 #P400 #P401 #P402 #P403 #P404 #P405 #P406 #P407 #P408 #P409 #P410 #P411 #P412 #P413 #P414 #P415 #P416 #P417 #P418 #P419 #P420 #P421 #P422 #P423 #P424 #P425 #P426 #P427 #P428 #P429 #P430 #P431 #P432 #P433 #P434 #P435 #P436 #P437 #P438 #P439 #P440 #P441 #P442 #P443 #P444 #P445 #P446 #P447 #P448 #P449 #P450 #P451 #P452 #P453 #P454 #P455 #P456 #P457 #P458 #P459 #P460 #P461 #P462 #P463 #P464 #P465 #P466 #P467 #P468 #P469 #P470 #P471 #P472 #P473 #P474 #P475 #P476 #P477 #P478 #P479 #P480 #P481 #P482 #P483 #P484 #P485 #P486 #P487 #P488 #P489 #P490 #P491 #P492 #P493 #P494 #P495 #P496 #P497 #P498 #P499 #P500 #P501 #P502 #P503 #P504 #P505 #P506 #P507 #P508 #P509 #P510 #P511 #P512 #P513 #P514 #P515 #P516 #P517 #P518 #P519 #P520 #P521 #P522 #P523 #P524 #P525 #P526 #P527 #P528 #P529 #P530 #P531 #P532 #P533 #P534 #P535 #P536 #P537 #P538 #P539 #P540 #P541 #P542 #P543 #P544 #P545 #P546 #P547 #P548 #P549 #P550 #P551 #P552 #P553 #P554 #P555 #P556 #P557 #P558 #P559 #P560 #P561 #P562 #P563 #P564 #P565 #P566 #P567 #P568 #P569 #P570 #P571 #P572 #P573 #P574 #P575 #P576 #P577 #P578 #P579 #P580 #P581 #P582 #P583 #P584 #P585 #P586 #P587 #P588 #P589 #P590 #P591 #P592 #P593 #P594 #P595 #P596 #P597 #P598 #P599 #P600 #P601 #P602 #P603 #P604 #P605 #P606 #P607 #P608 #P609 #P610 #P611 #P612 #P613 #P614 #P615 #P616 #P617 #P618 #P619 #P620 #P621 #P622 #P623 #P624 #P625 #P626 #P627 #P628 #P629 #P630 #P631 #P632 #P633 #P634 #P635 #P636 #P637 #P638 #P639 #P640 #P641 #P642 #P643 #P644 #P645 #P646 #P647 #P648 #P649 #P650 #P651 #P652 #P653 #P654 #P655 #P656 #P657 #P658 #P659 #P660 #P661 #P662 #P663 #P664 #P665 #P666 #P667 #P668 #P669 #P670 #P671 #P672 #P673 #P674 #P675 #P676 #P677 #P678 #P679 #P680 #P681 #P682 #P683 #P684 #P685 #P686 #P687 #P688 #P689 #P690 #P691 #P692 #P693 #P694 #P695 #P696 #P697 #P698 #P699 #P700 #P701 #P702 #P703 #P704 #P705 #P706 #P707 #P708 #P709 #P710 #P711 #P712 #P713 #P714 #P715 #P716 #P717 #P718 #P719 #P720 #P721 #P722 #P723 #P724 #P725 #P726 #P727 #P728 #P729 #P730 #P731 #P732 #P733 #P734 #P735 #P736 #P737 #P738 #P739 #P740 #P741 #P742 #P743 #P744 #P745 #P746 #P747 #P748 #P749 #P750 #P751 #P752 #P753 #P754 #P755 #P756 #P757 #P758 #P759 #P760 #P761 #P762 #P763 #P764 #P765 #P766 #P767 #P768 #P769 #P770 #P771 #P772 #P773 #P774 #P775 #P776 #P777 #P778 #P779 #P780 #P781 #P782 #P783 #P784 #P785 #P786 #P787 #P788 #P789 #P
```