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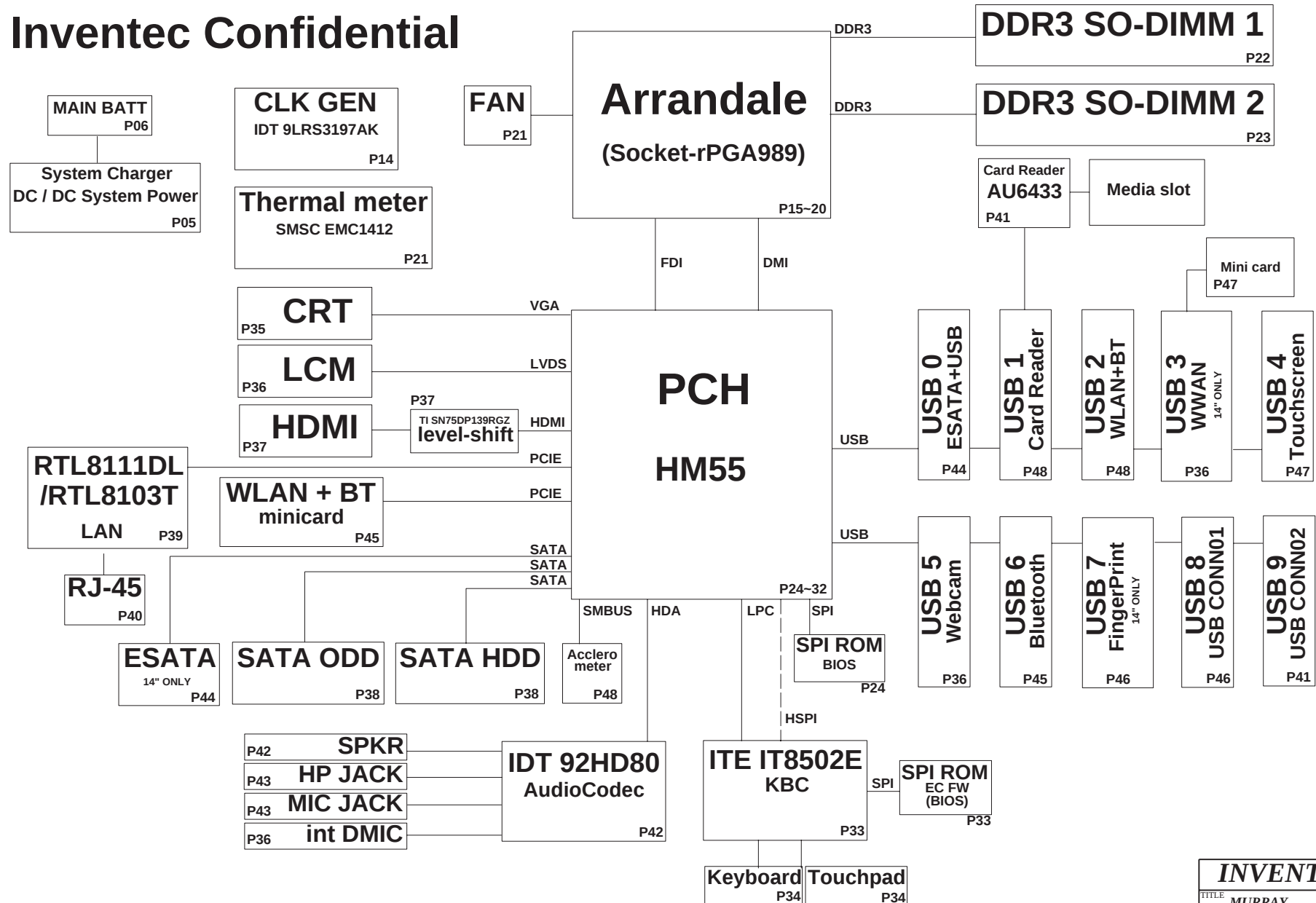
- 31. PCH-8
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INVENTEC

TITLE			
MURRAY			
Index			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03
SHEET		2	OF 48

CHANGE by Chou, Leo 1-Mar-2010

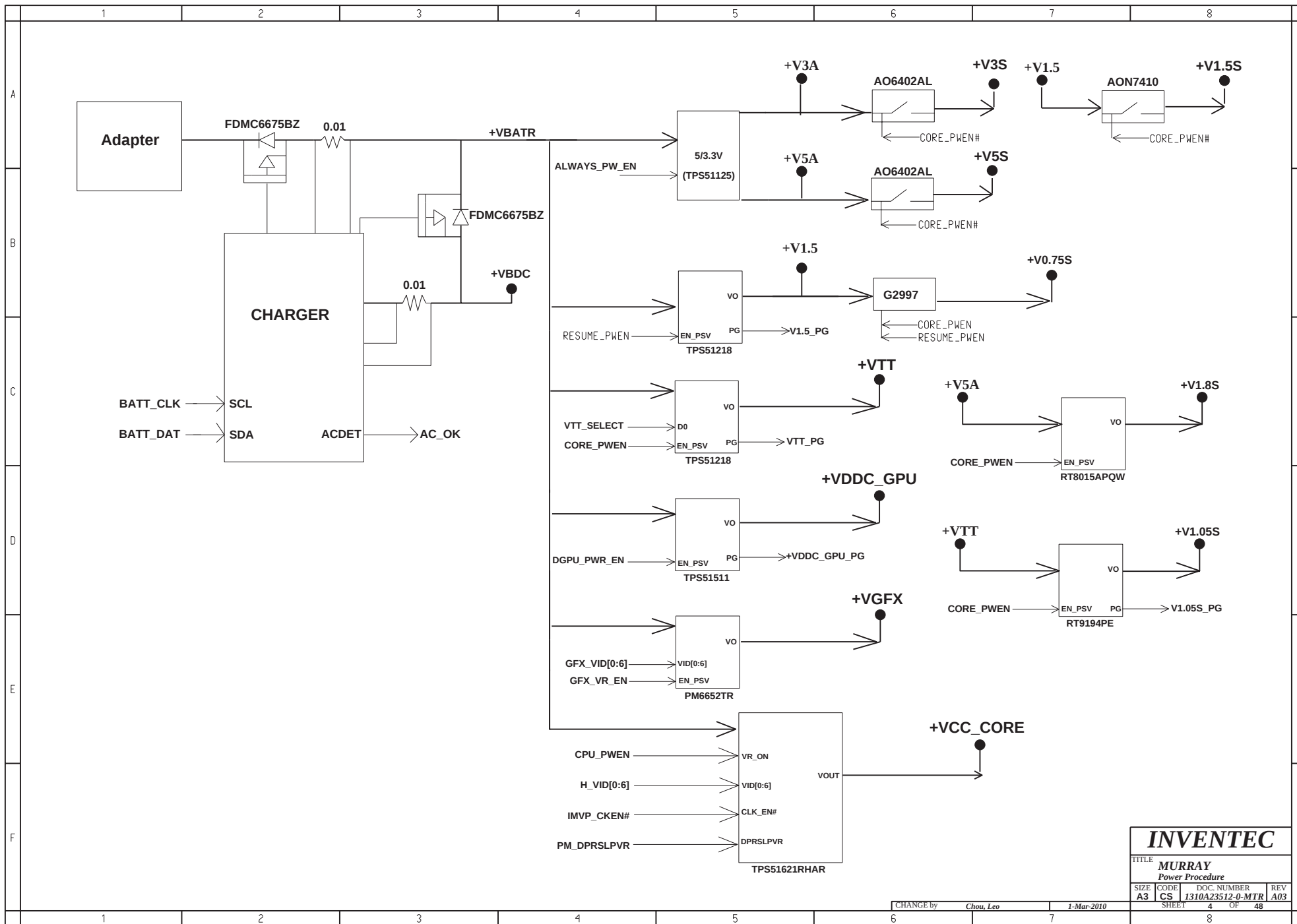
Inventec Confidential



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TITLE			
MURRAY			
Block Diagram			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03

CHANGE by	Chou, Leo	1-Mar-2010	SHEET	3	OF	48
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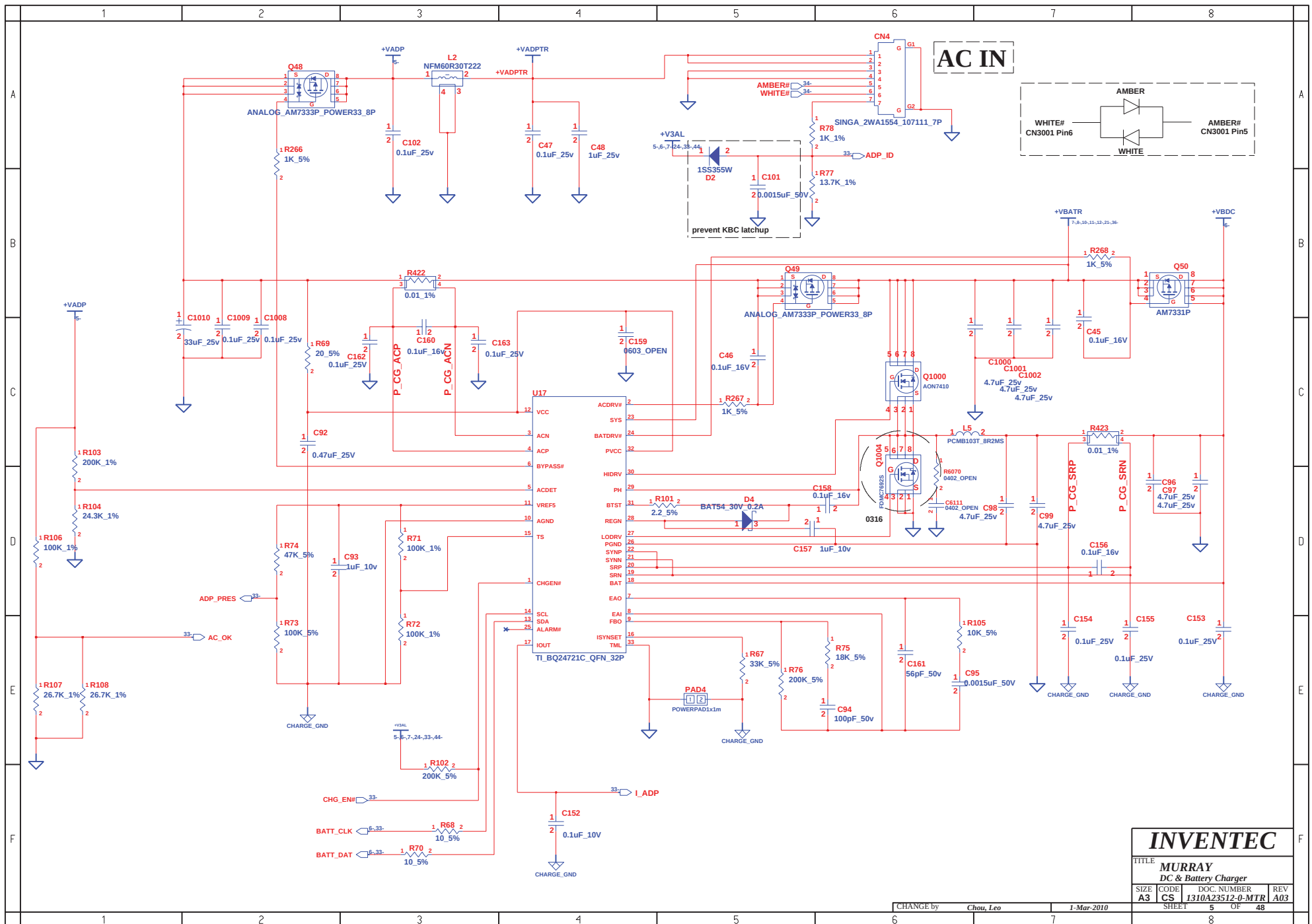
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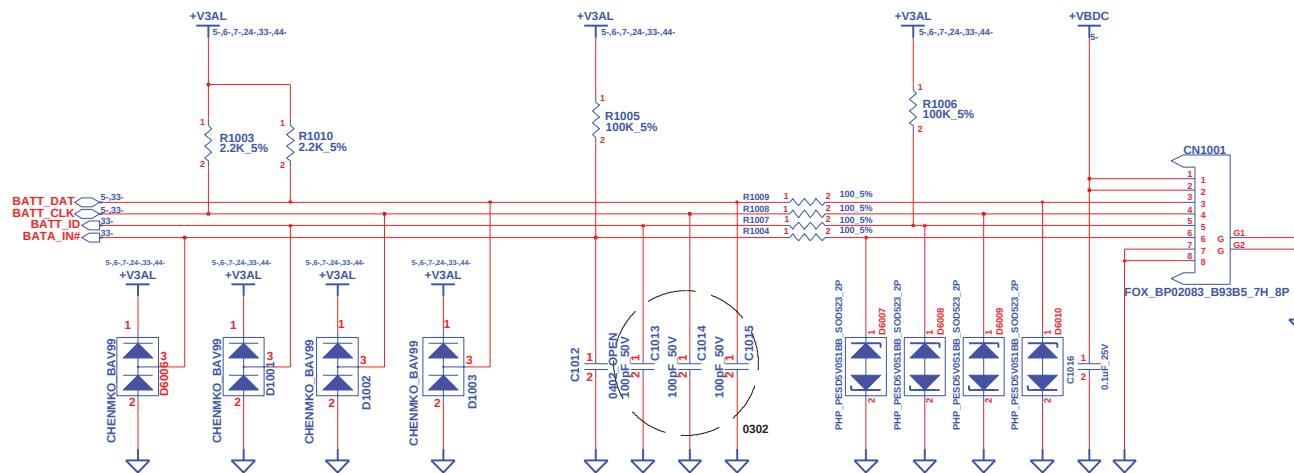
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MURRAY
Power Procedure

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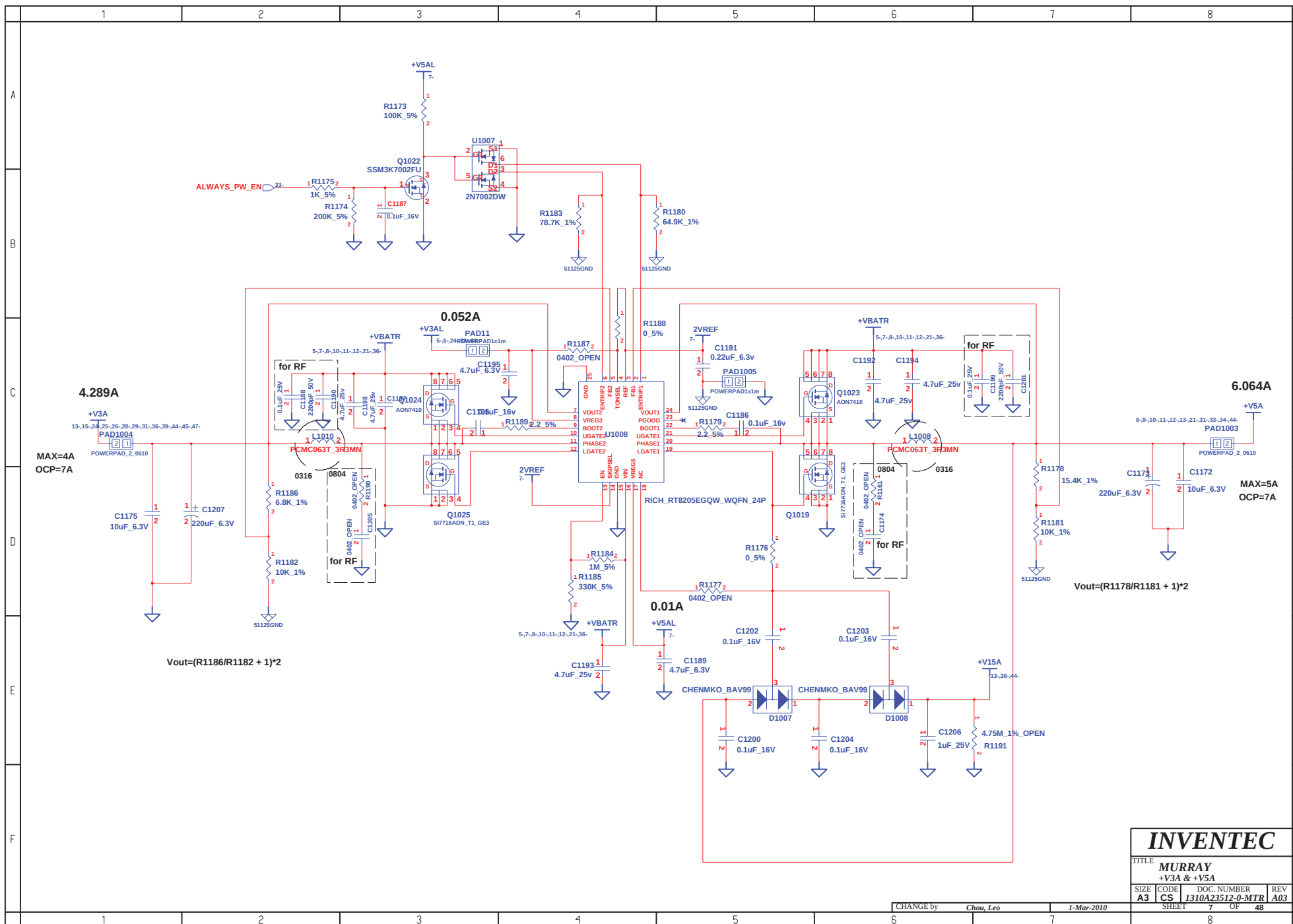
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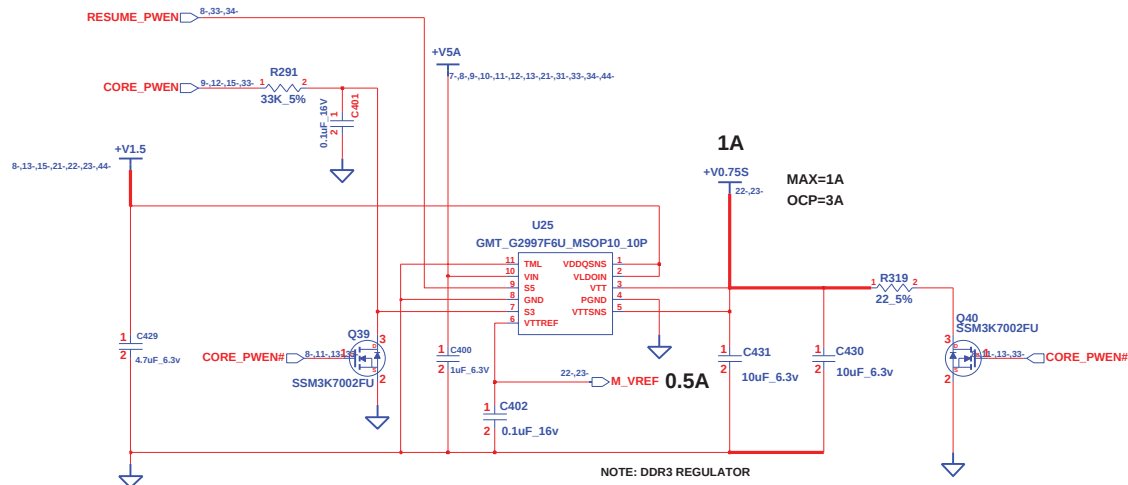
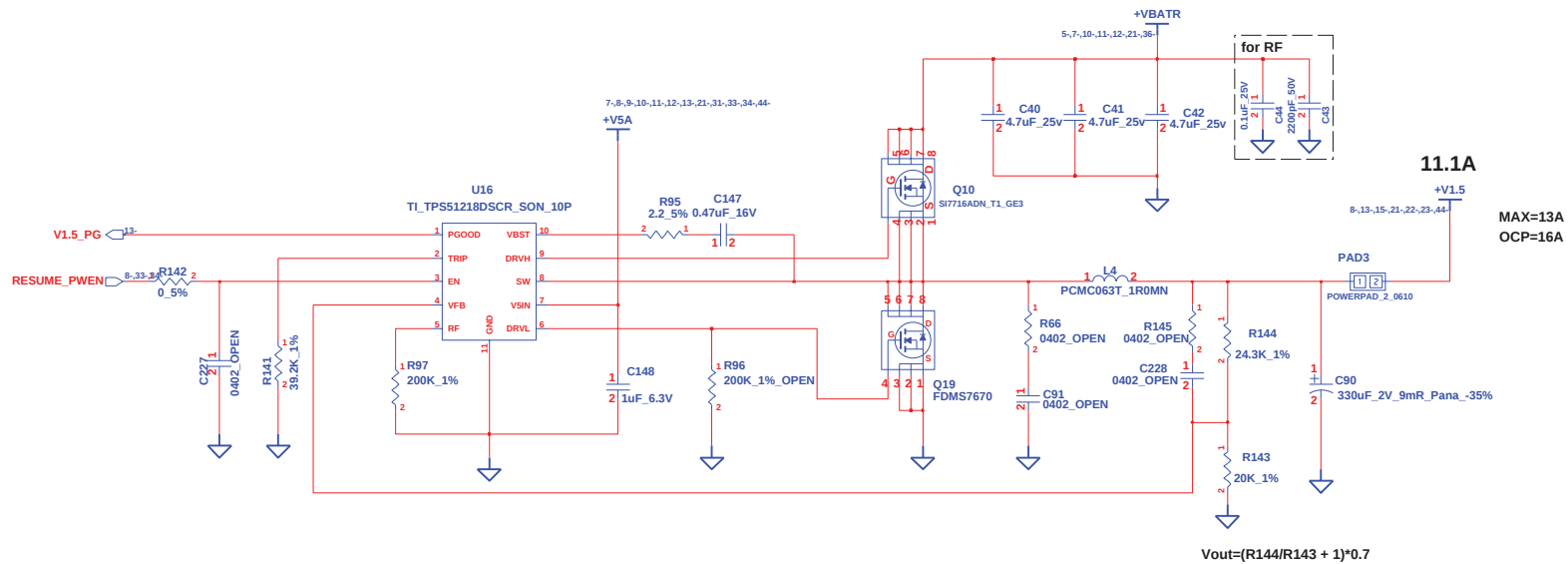
CHANGE by *Chou, Leo* 1-Mar-2010





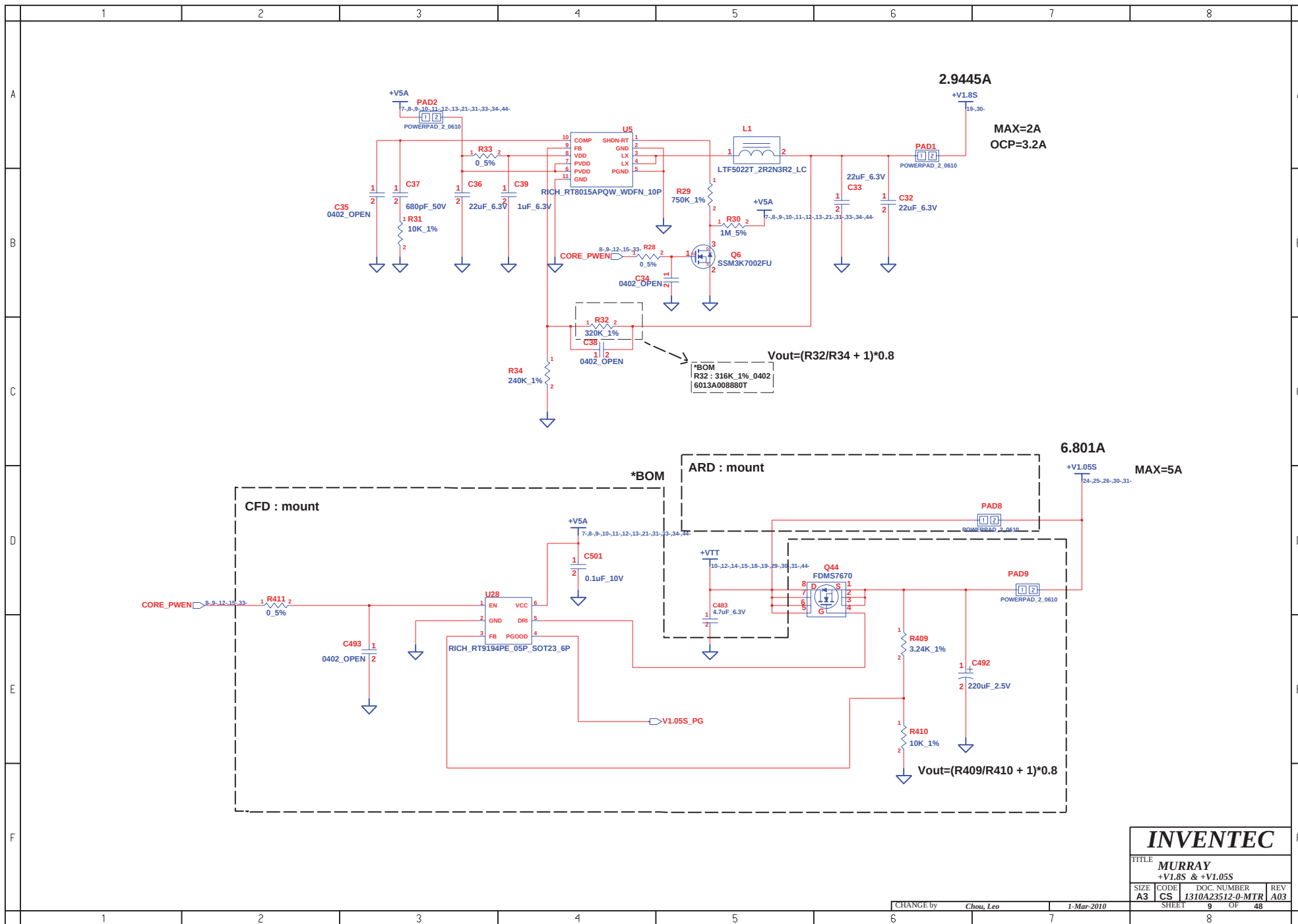
INVENTEC			
TITLE MURRAY Battery CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
CHANGE by Chou, Leo		1-Mar-2010	
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TITLE			
MURRAY			
+V1.5 & +V0.75			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03
CHANGE by		1-Mar-2010	
SHEET		8 OF 48	

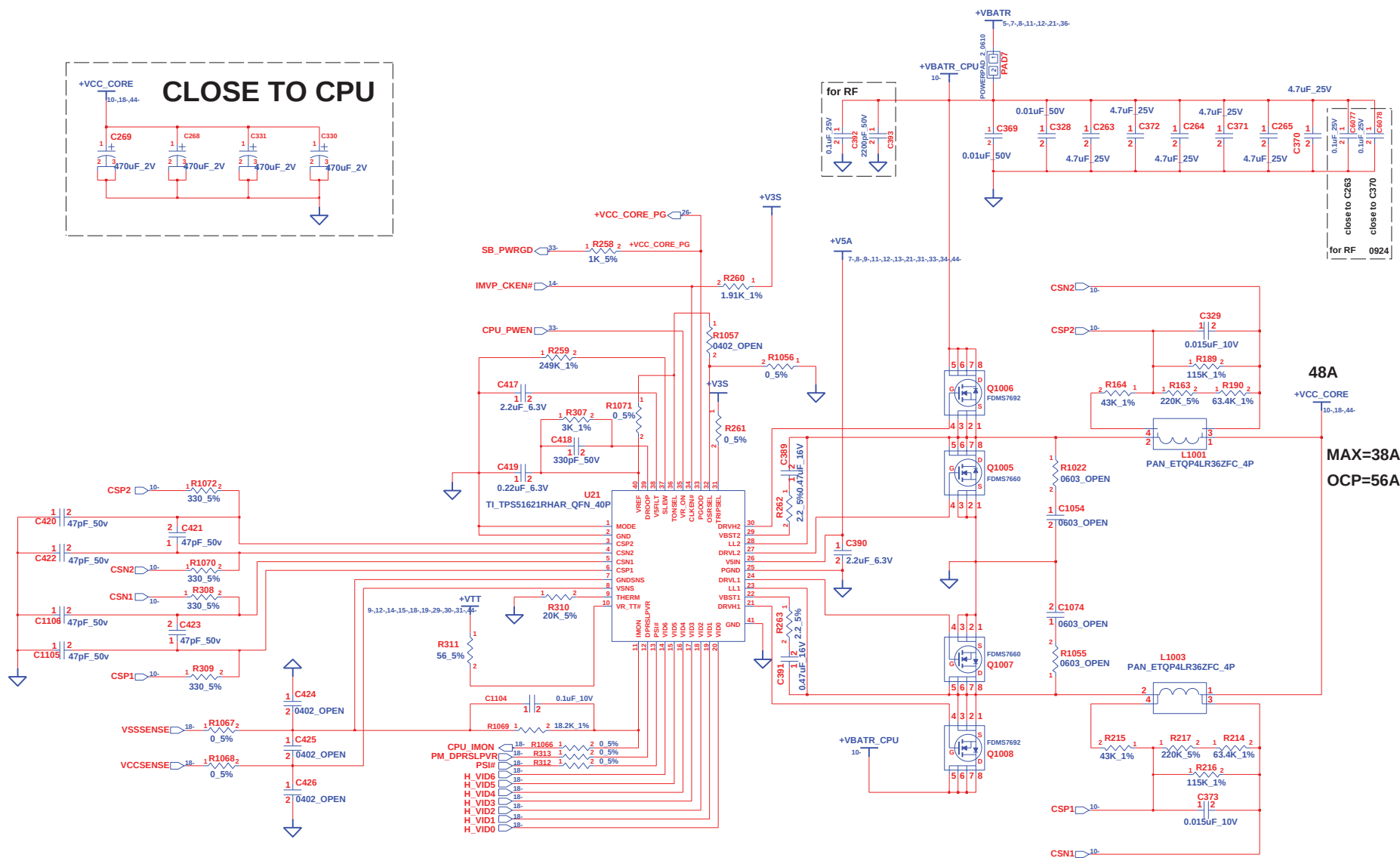
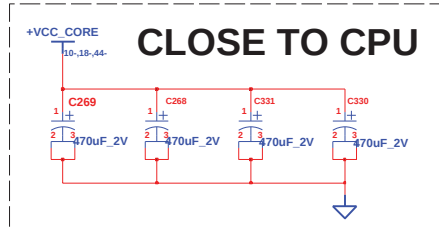


INVENTEC

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MURRAY			
+V1.8S & +V1.05S			
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CHANGE by Chou, Leo 1-Mar-2010

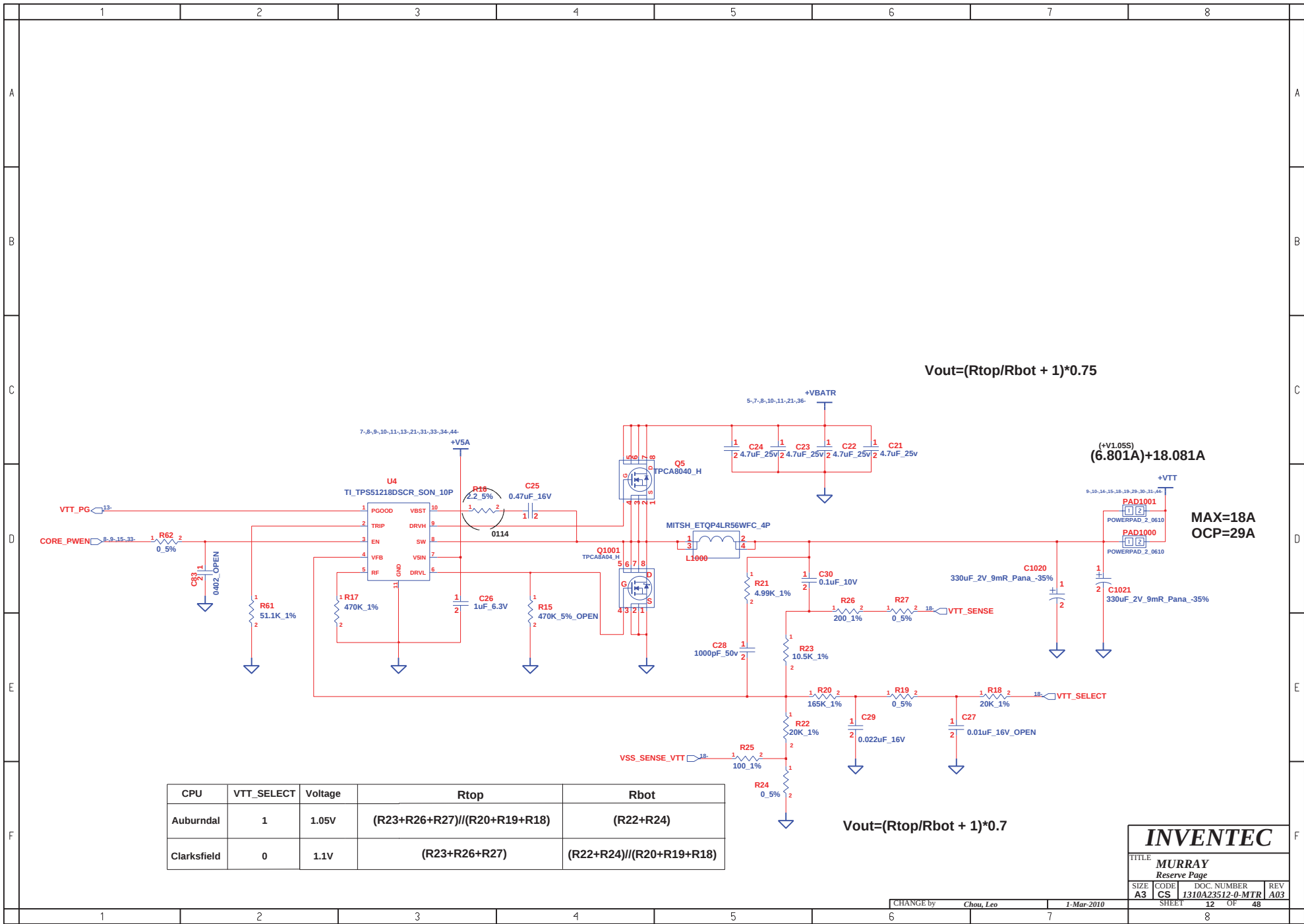
SHEET 9 OF 48



INVENTEC

TITLE			
MURRAY			
+VCC_CORE			
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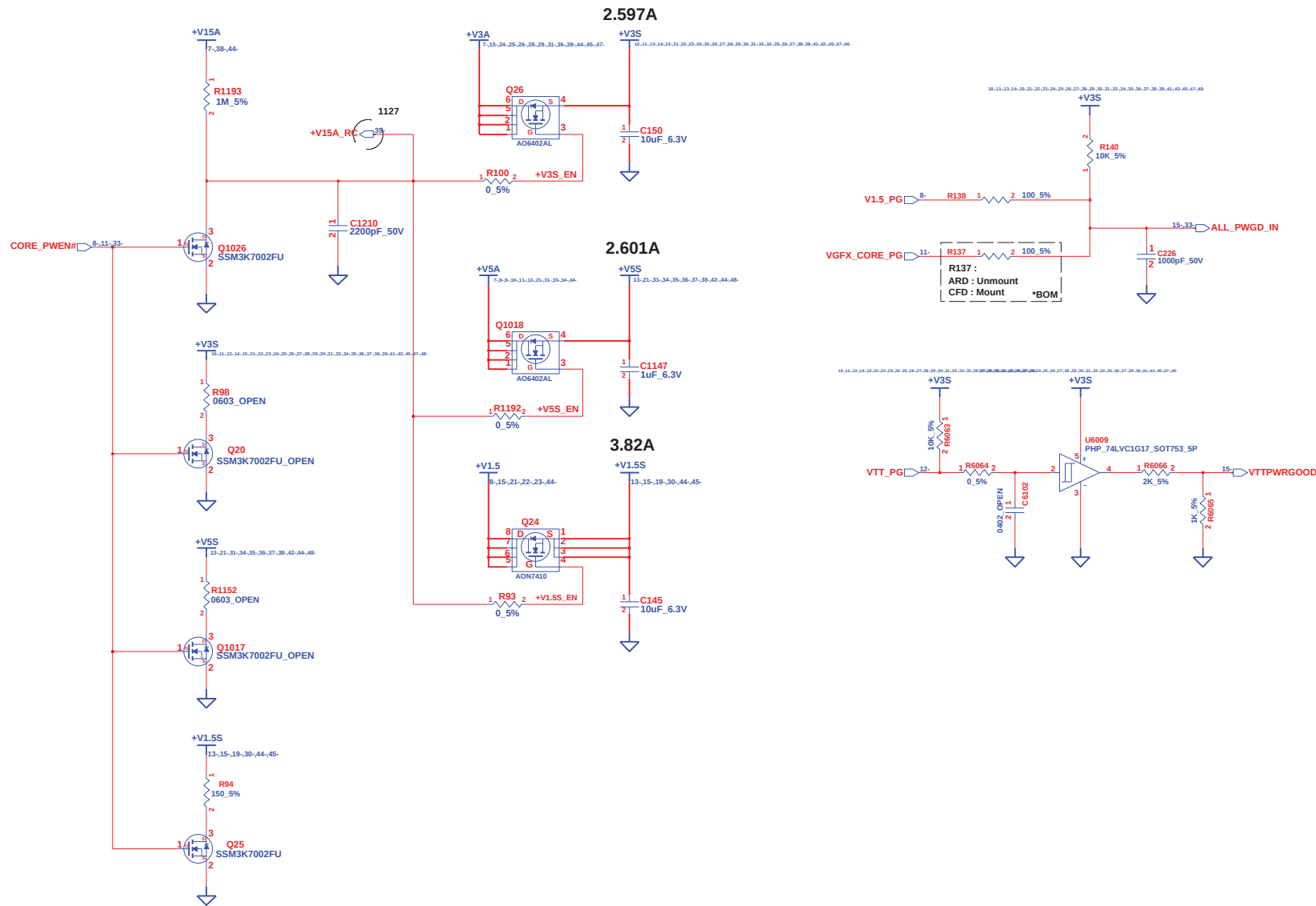
CHANGE by Chou, Leo 1-Mar-2010



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TITLE MURRAY Reserve Page			
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SHEET 12		OF 48	

CHANGE by Chou, Leo 1-Mar-2010

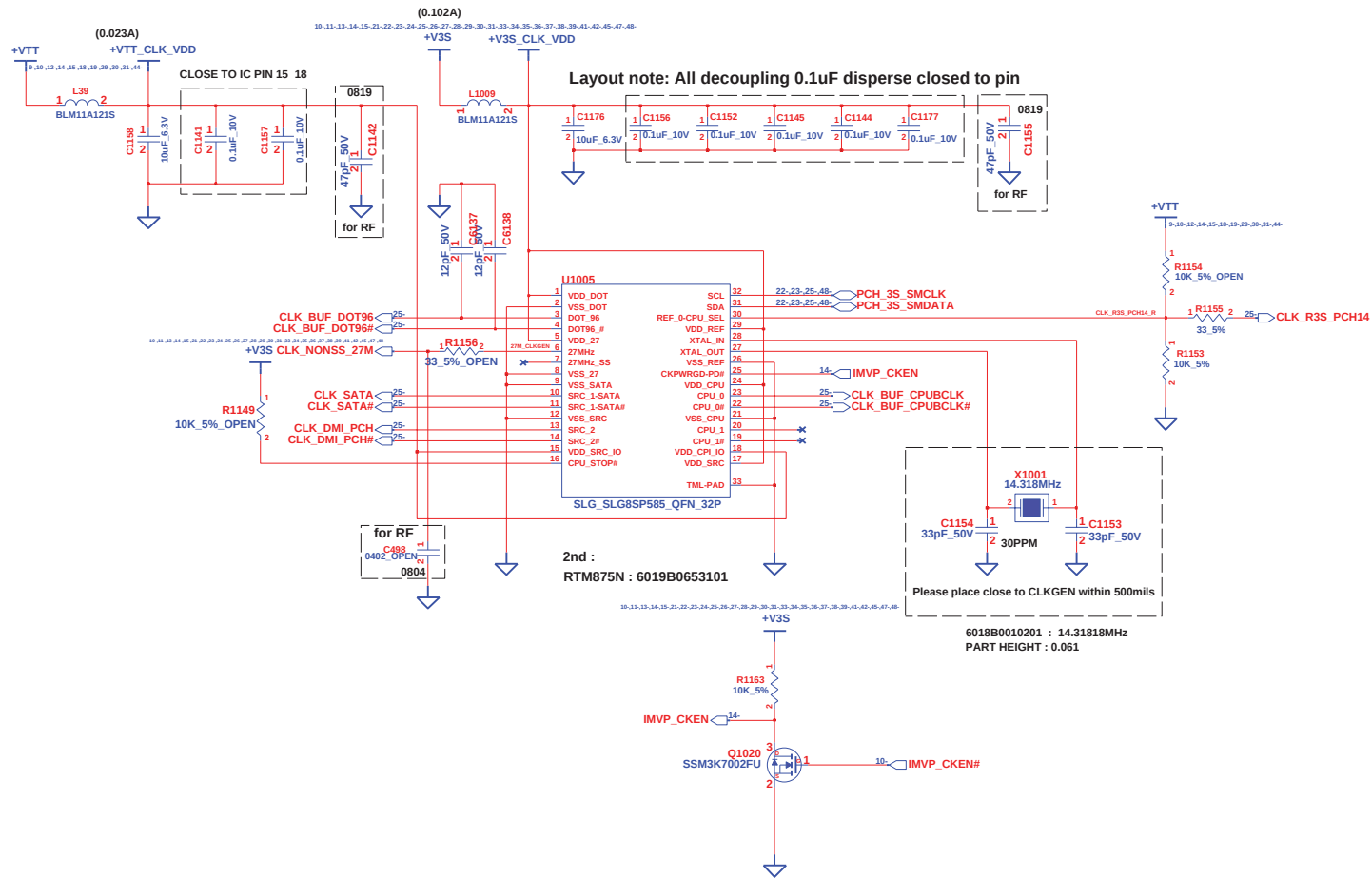


INVENTEC

TITLE			
MURRAY			
+V5S & +V3S			
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CHANGE by Chou, Leo 1-Mar-2010

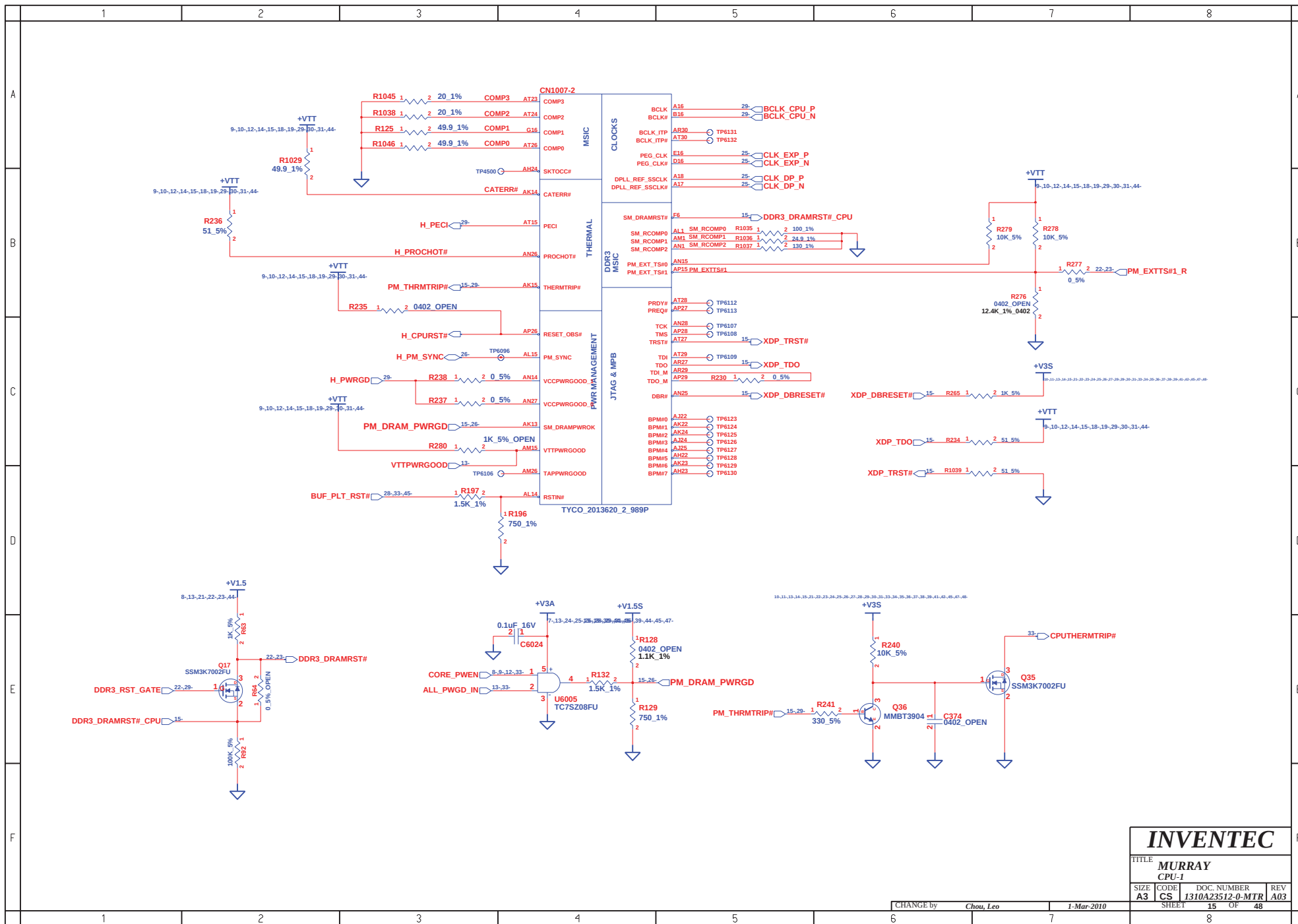
SHEET 13 OF 48

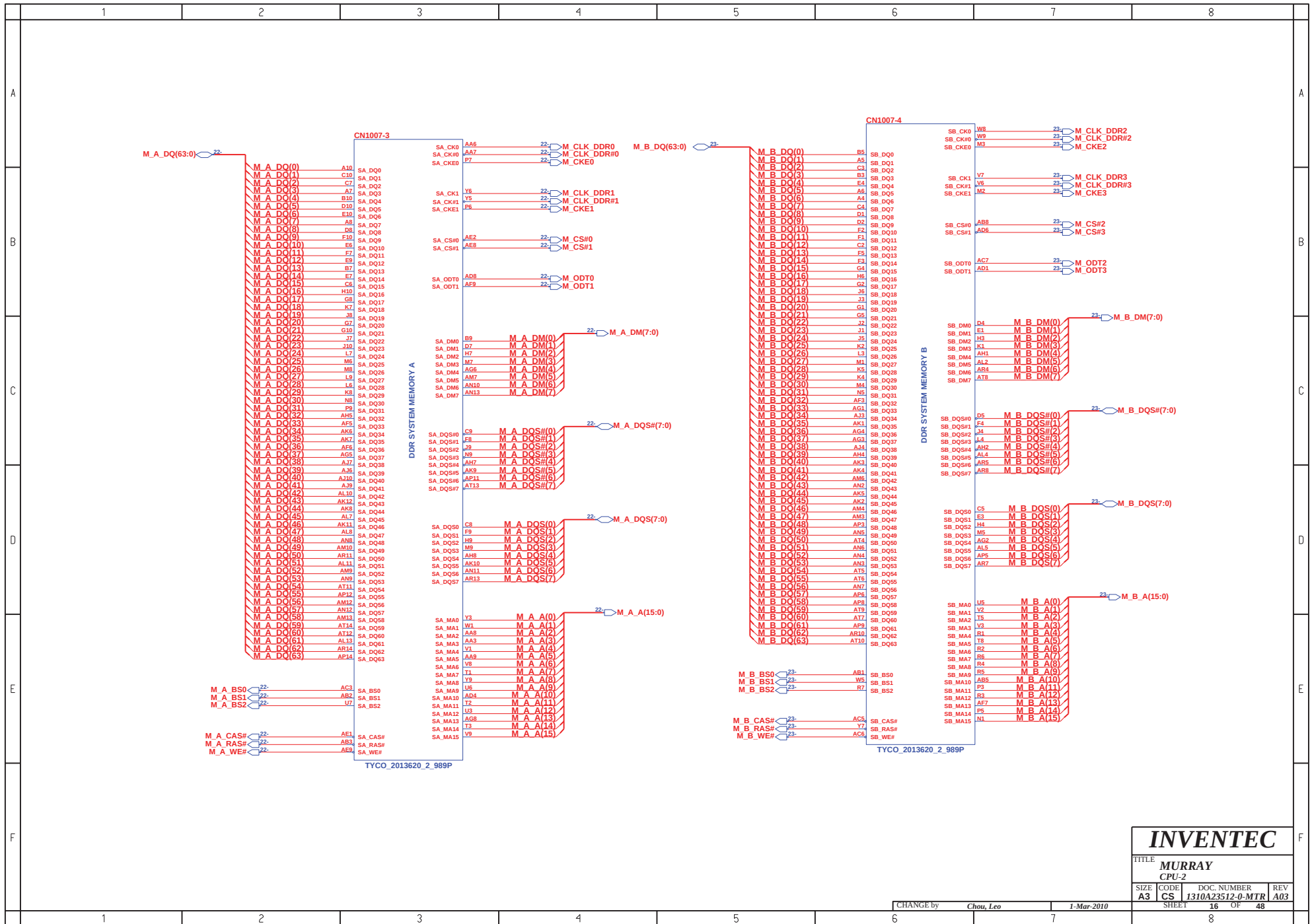


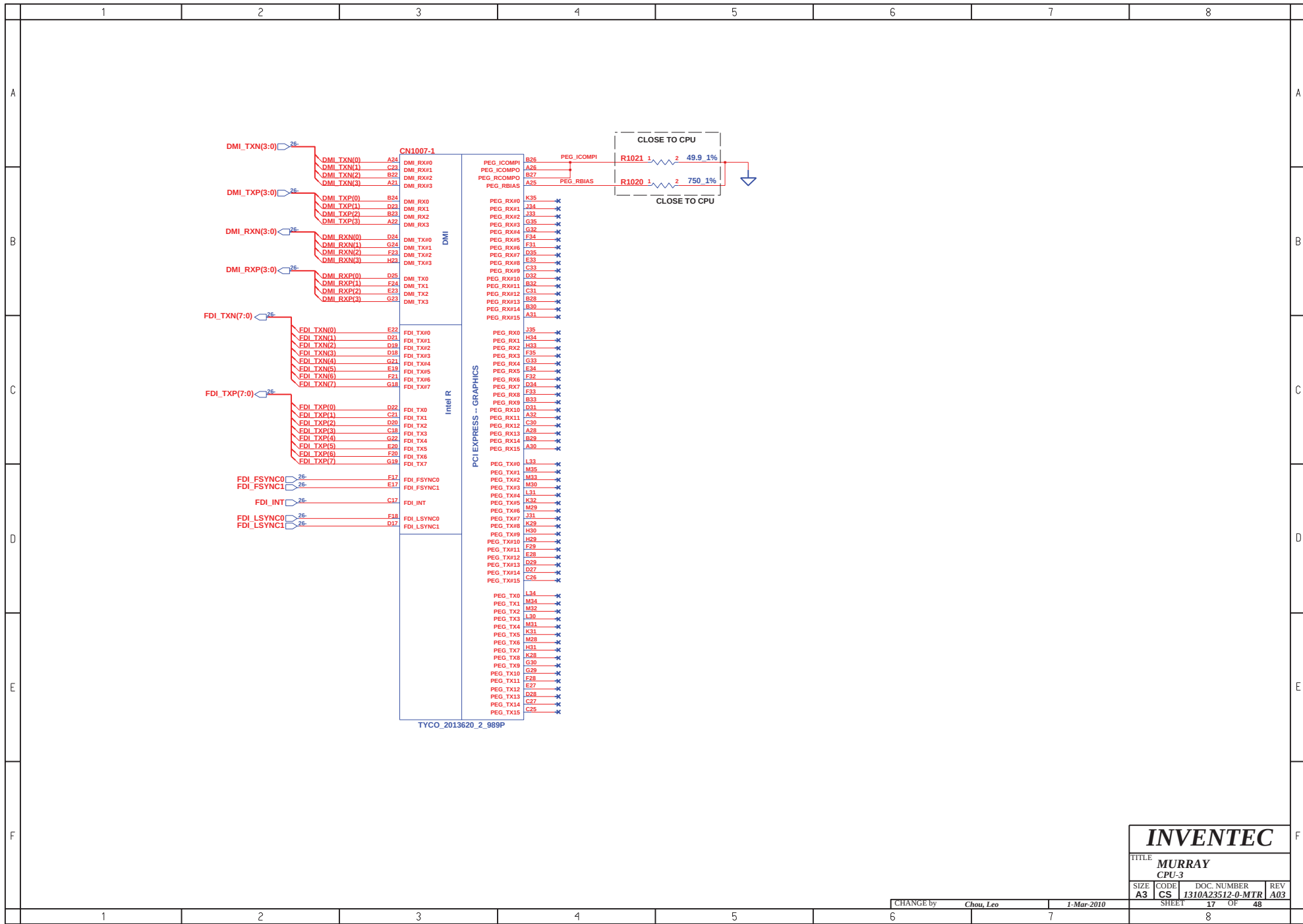
INVENTEC

TITLE MURRAY CLK GEN			
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SHEET 14		OF 48	

CHANGE by Chou, Leo 1-Mar-2010







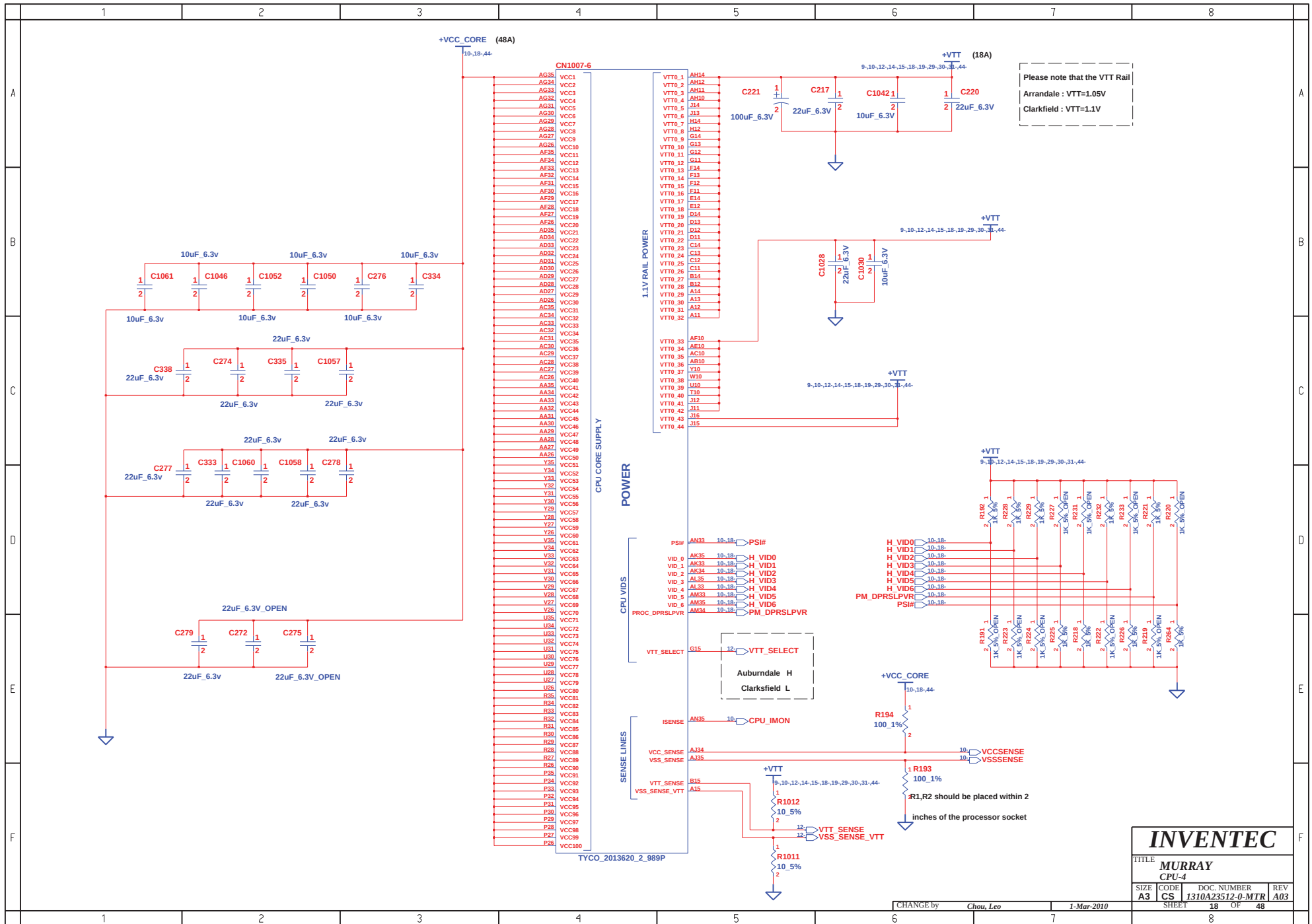
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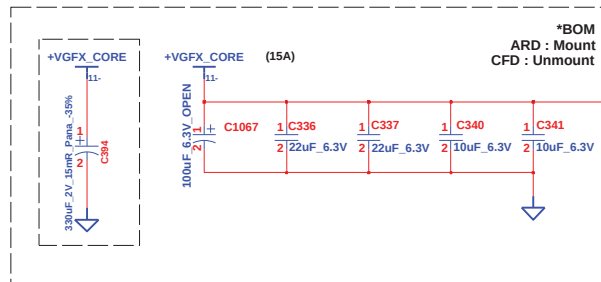
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MURRAY
CPU-3

SIZE	CODE	DOC. NUMBER	REV
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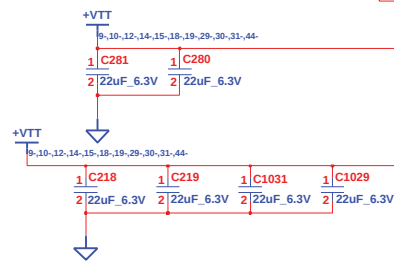
CHANGE by *Chou, Leo* 1-Mar-2010

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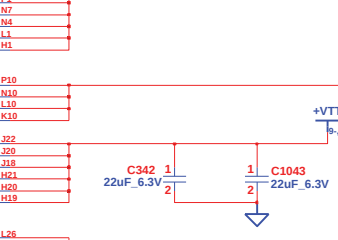
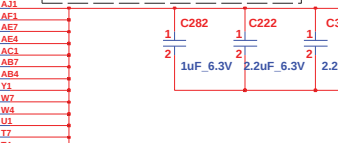
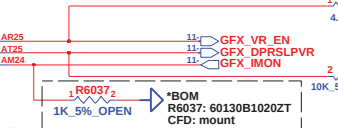
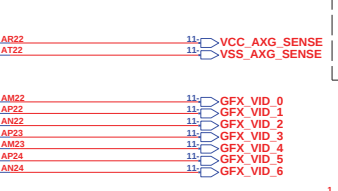
Please note that the VTT Rail Values are
Auburndale VTT=1.05V
Clarksfield VTT=1.1V



- CN1007-7
- AT21 VAXG1
 - AT19 VAXG2
 - AT18 VAXG3
 - AT18 VAXG4
 - AP21 VAXG5
 - AP21 VAXG6
 - AP18 VAXG7
 - AP18 VAXG8
 - AP21 VAXG9
 - AP19 VAXG10
 - AP18 VAXG11
 - AP18 VAXG12
 - VAXG13
 - AN19 VAXG14
 - AN18 VAXG15
 - VAXG16
 - AM21 VAXG17
 - AM18 VAXG18
 - AM18 VAXG19
 - VAXG20
 - AL19 VAXG22
 - AL18 VAXG23
 - VAXG24
 - AK21 VAXG25
 - AK18 VAXG26
 - AK18 VAXG27
 - AK18 VAXG28
 - AJ21 VAXG29
 - AJ18 VAXG30
 - AJ18 VAXG31
 - AJ18 VAXG32
 - AH18 VAXG33
 - AH18 VAXG34
 - AH18 VAXG35
 - AH18 VAXG36

- SENSELINES
- GFX_VID_0
 - GFX_VID_1
 - GFX_VID_2
 - GFX_VID_3
 - GFX_VID_4
 - GFX_VID_5
 - GFX_VID_6
- GRAPHICS_VIDS
- GFX_VR_EN
 - GFX_DPRSPLVR
 - GFX_IMON
- DDR3-1.5V RAILS
- VDDQ1
 - VDDQ2
 - VDDQ3
 - VDDQ4
 - VDDQ5
 - VDDQ6
 - VDDQ7
 - VDDQ8
 - VDDQ9
 - VDDQ10
 - VDDQ11
 - VDDQ12
 - VDDQ13
 - VDDQ14
 - VDDQ15
 - VDDQ16
 - VDDQ17
 - VDDQ18
- POWER
- VTT45
 - VTT46
 - VTT47
- PEG & DMI
- VTT48
 - VTT49
 - VTT50
 - VTT51
 - VTT52
 - VTT53
 - VTT54
 - VTT55
 - VTT56
 - VTT57
 - VTT58

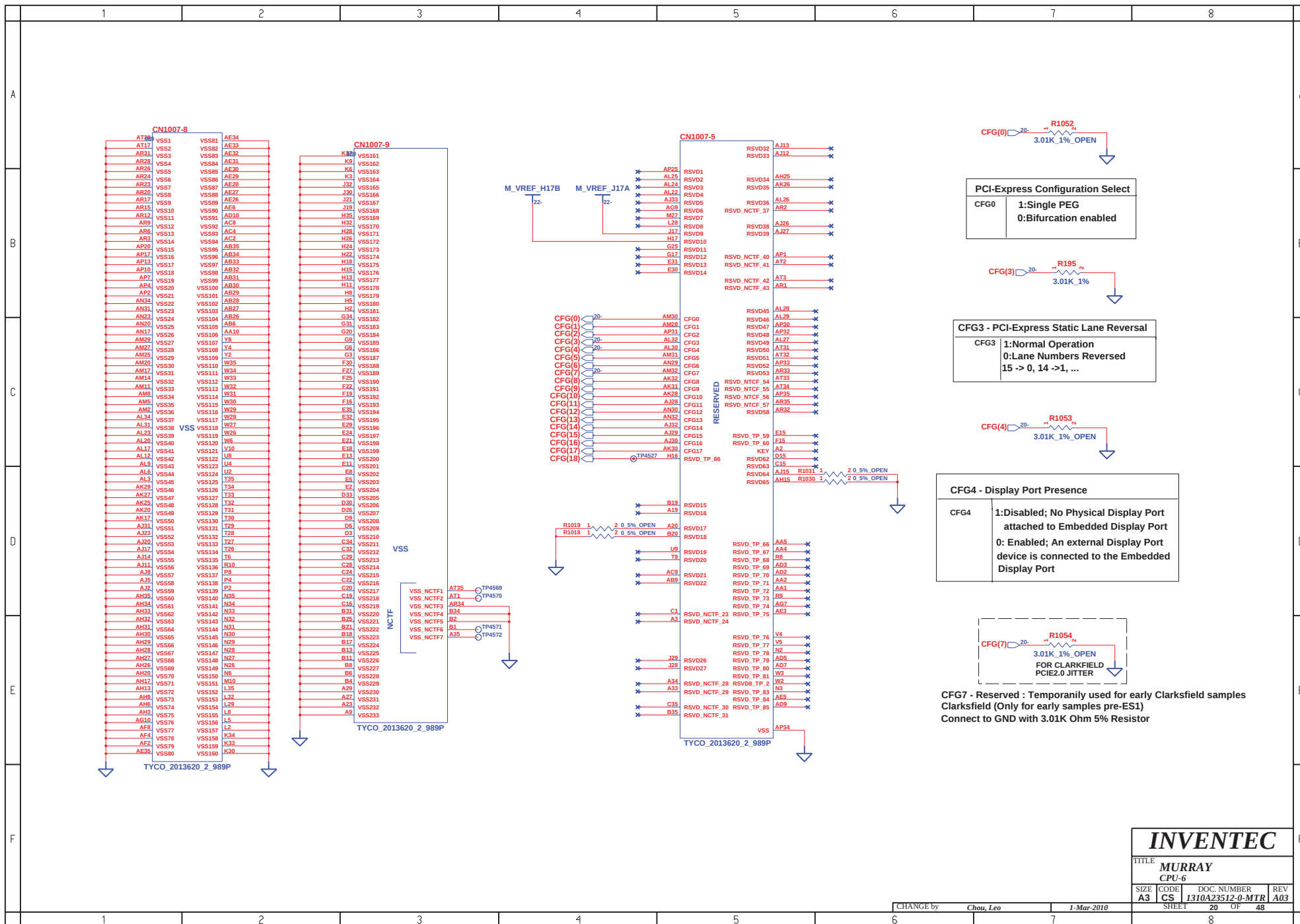
- TYCO_2013620_2_989P
- VTT1.59
 - VTT1.60
 - VTT1.61
 - VTT1.62
 - VTT1.63
 - VTT1.64
 - VTT1.65
 - VTT1.66
 - VTT1.67
 - VTT1.68
- VCCPLL1
- VCCPLL2
 - VCCPLL3



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TITLE			
MURRAY			
CPU-5			
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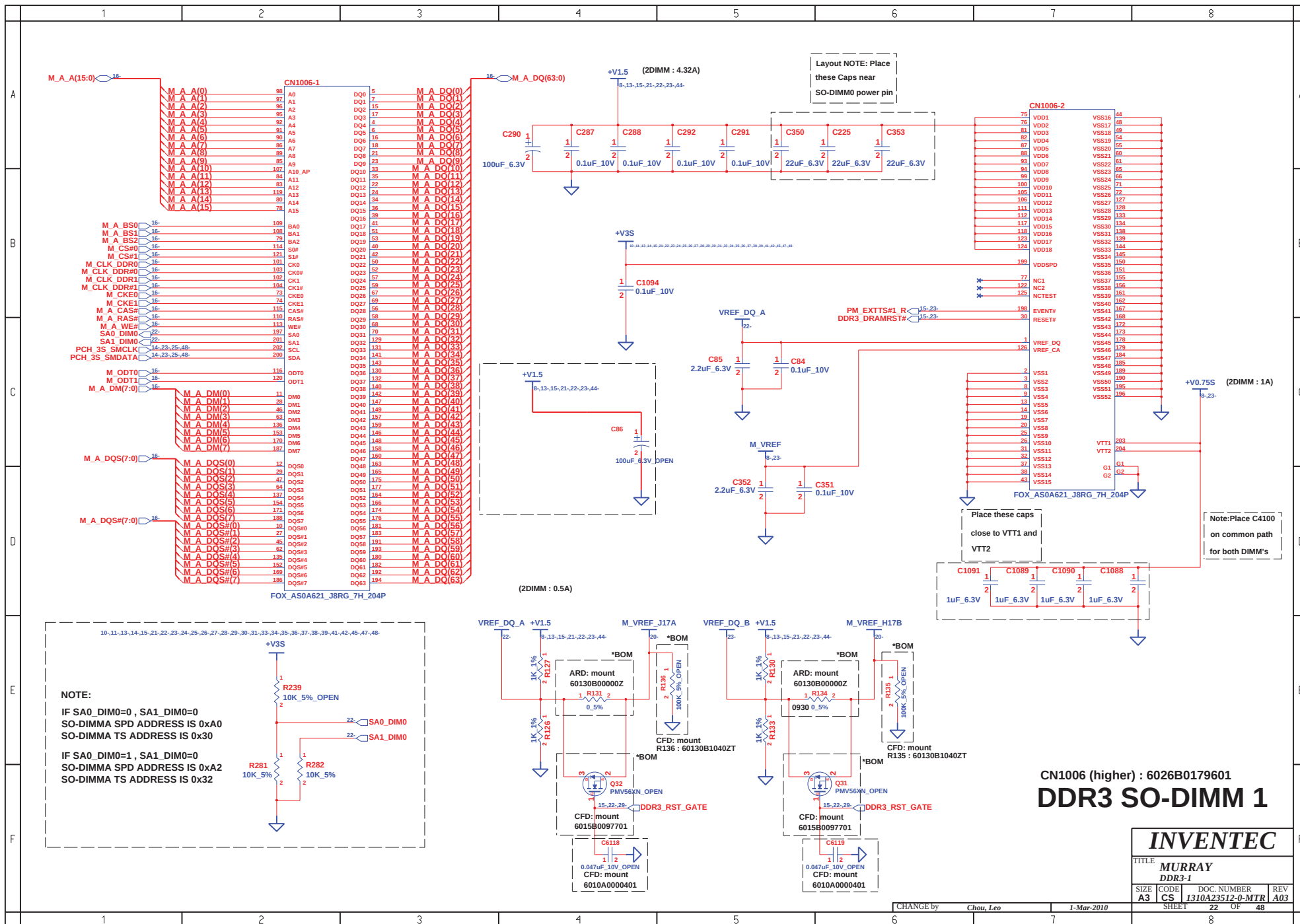
CHANGE by Chou, Leo 1-Mar-2010

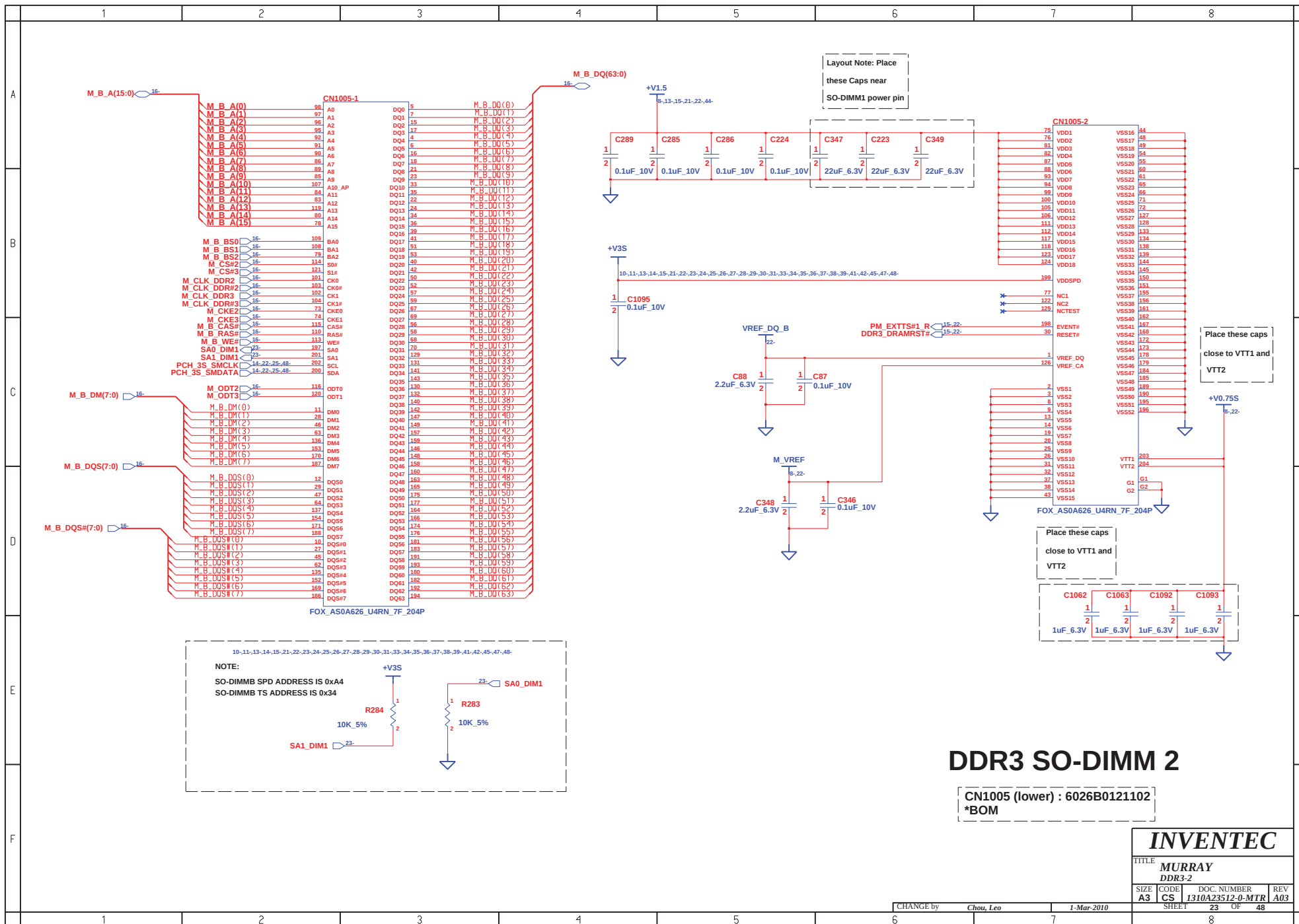


INVENTEC

TITLE MURRAY CPU-6			
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
SHEET 20		OF 48	

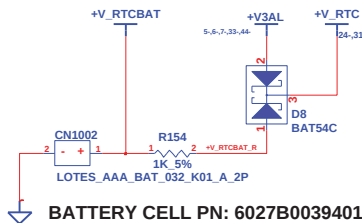
CHANGE by Chou, Leo 1-Mar-2010





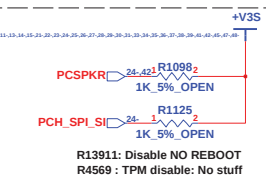
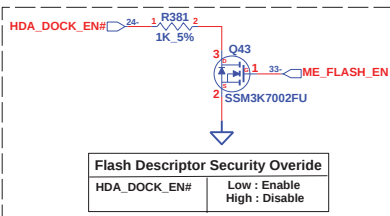
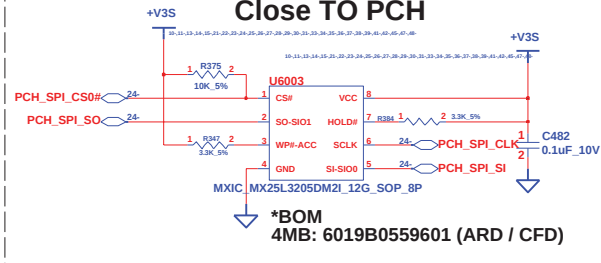
*BOM (for HP)
PCH(HM55) : 6019B0713401

RTC BATTERY

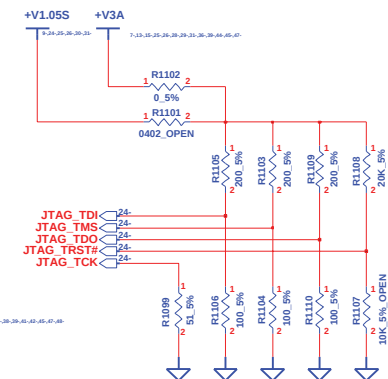
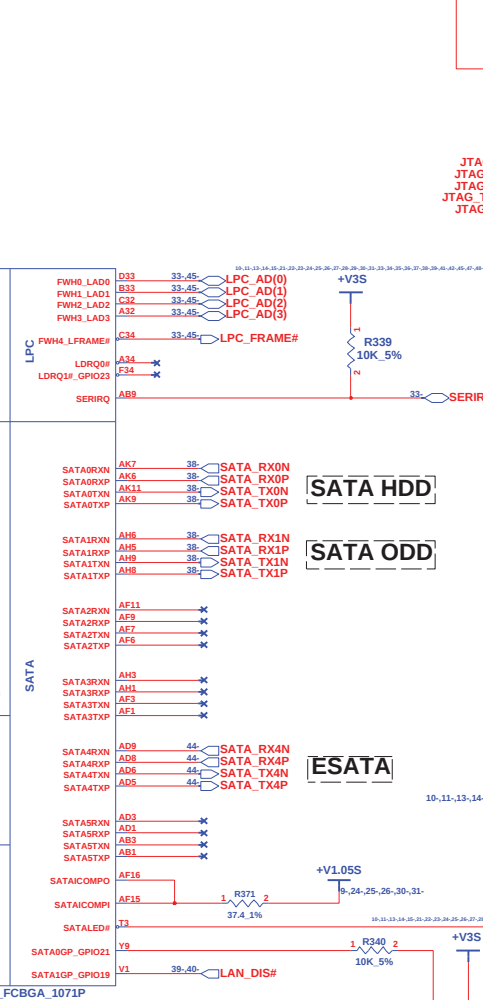
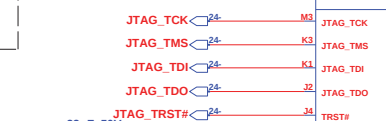
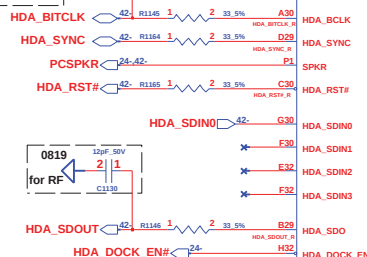
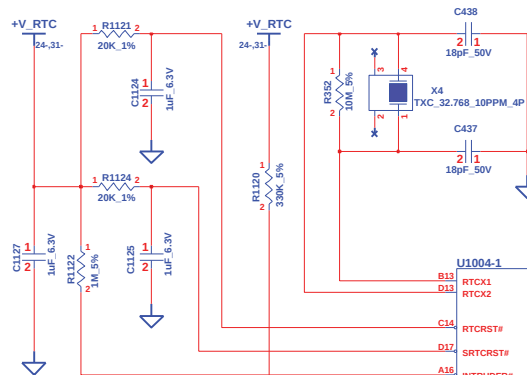


SPI_MISO:
No series resistor if 1.5"-6.5"
with 1 SPI device
Use a 33ohm series resistor
close to PCH if using 2 SPI device

Close TO PCH



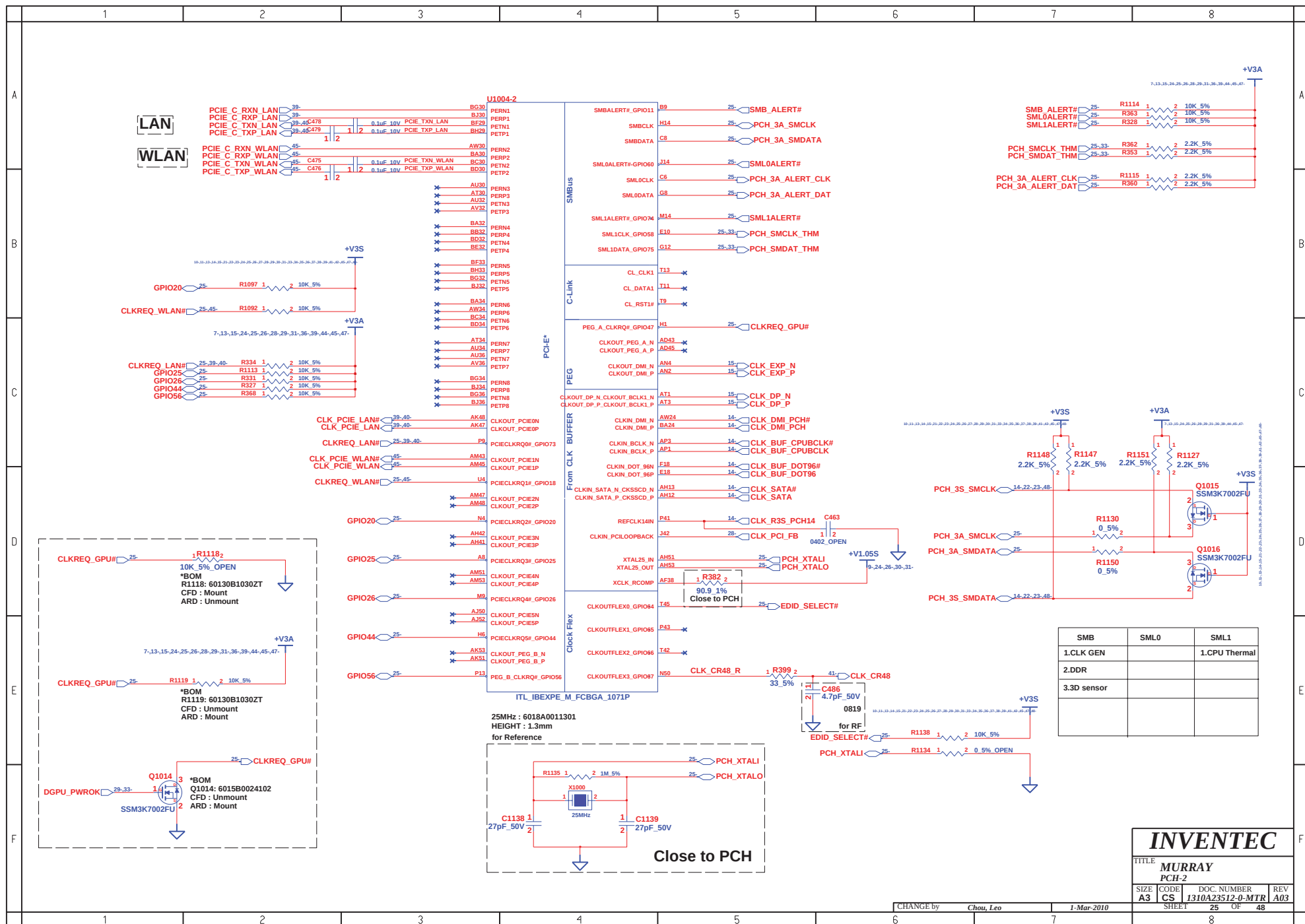
INTVRMEN :
Integrated SUS 1.1V VRM Enable
High - Enable Internal VRs

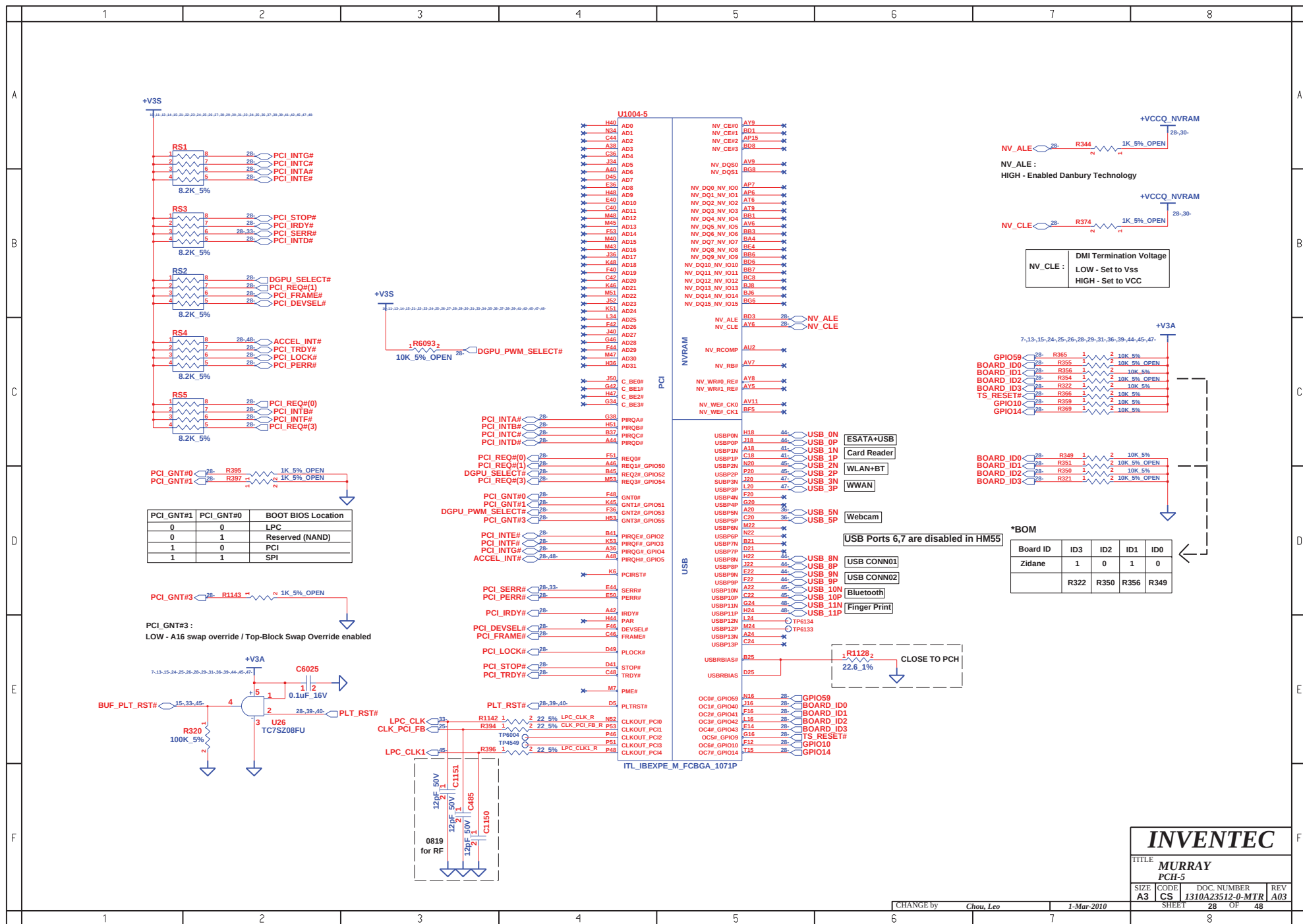


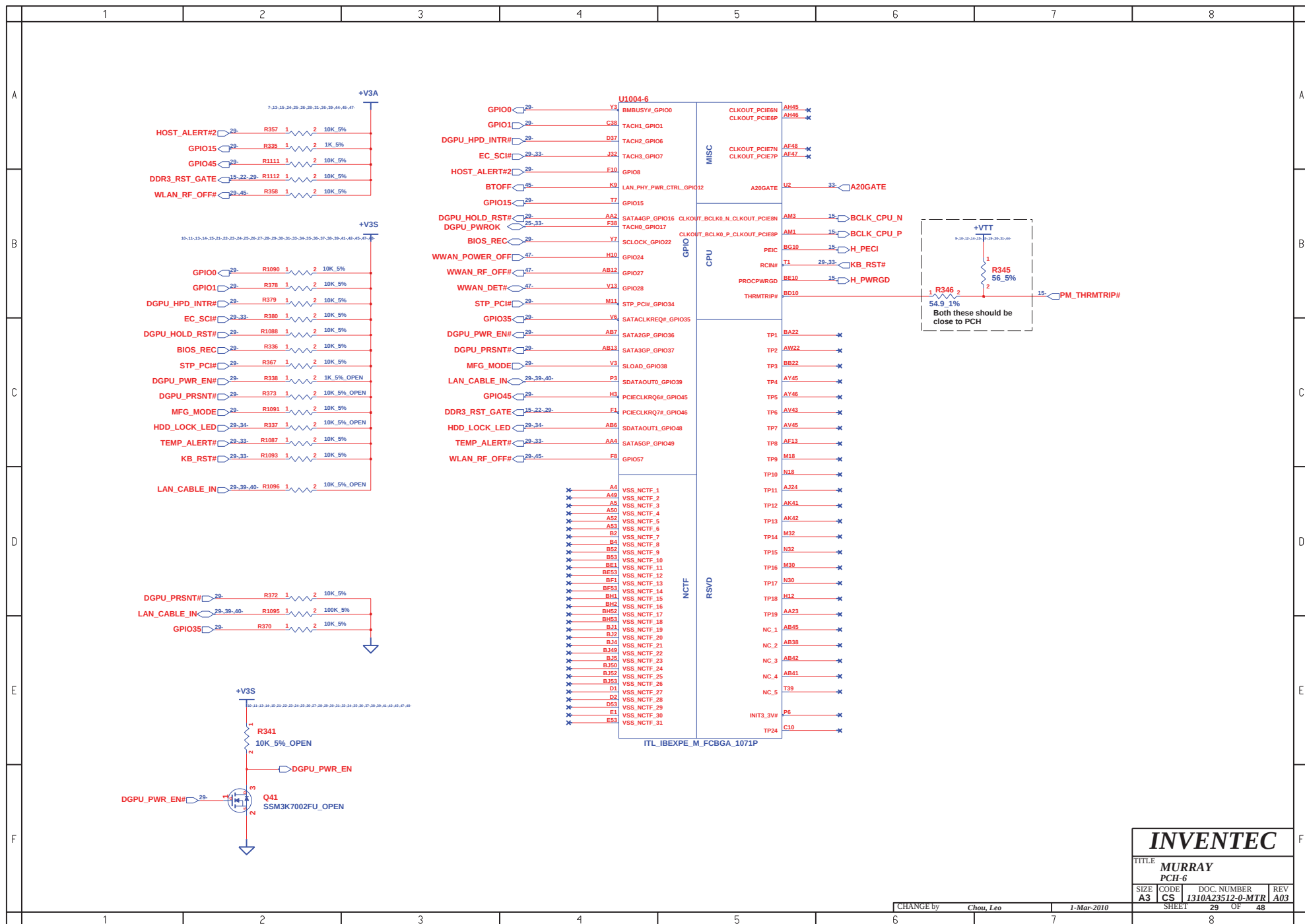
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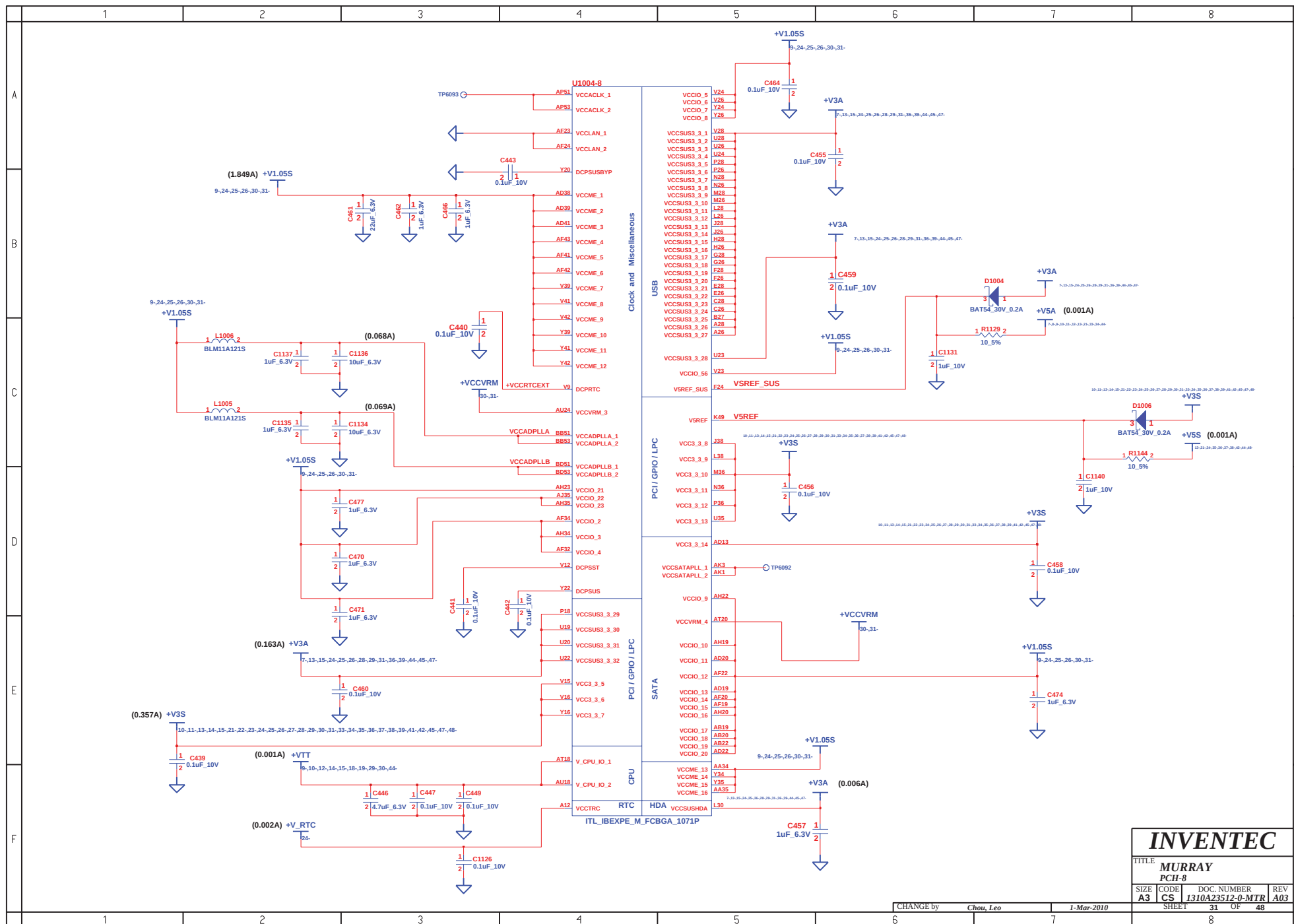
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SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
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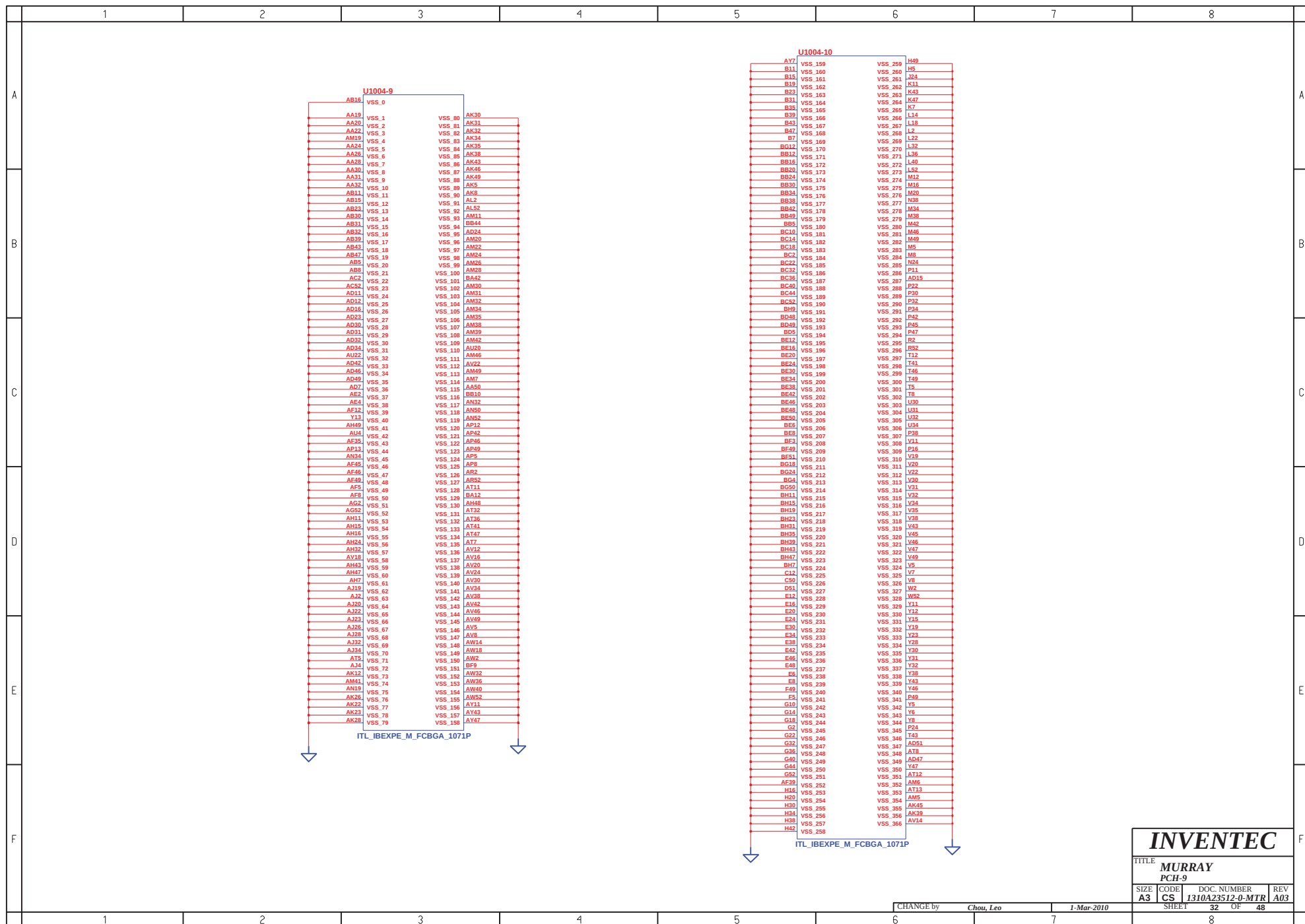
CHANGE by Chou, Leo 1-Mar-2010

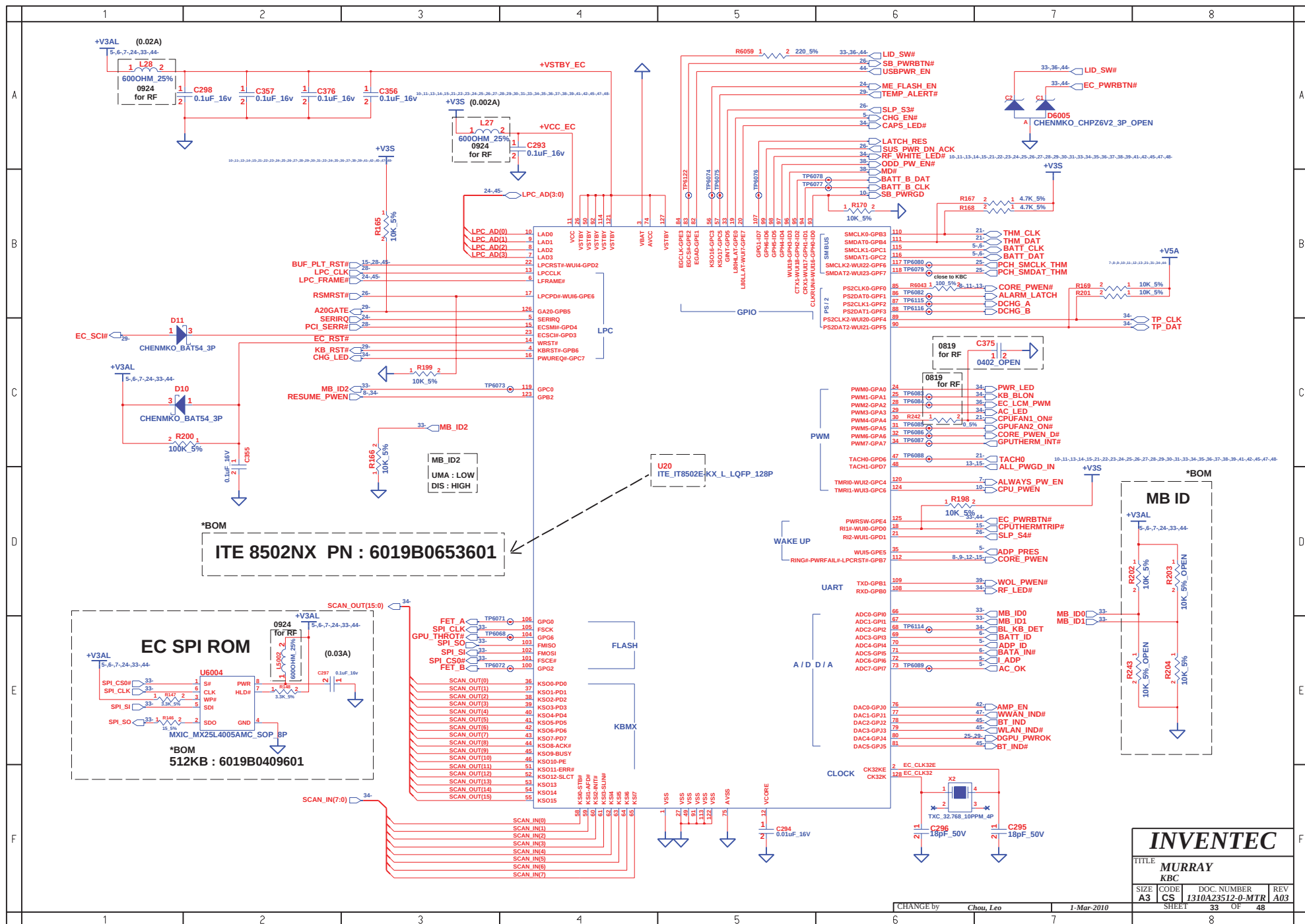


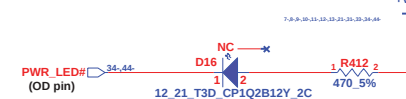
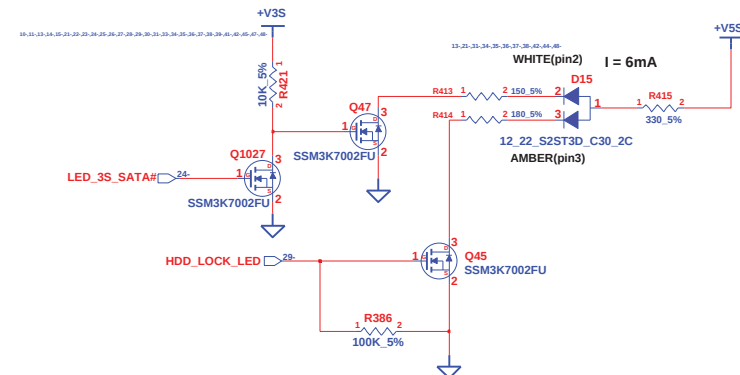
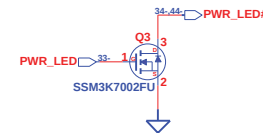
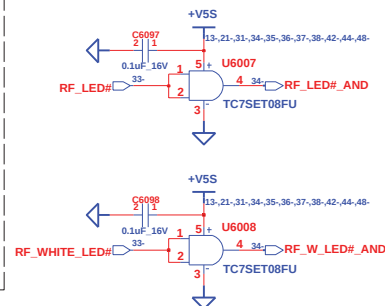
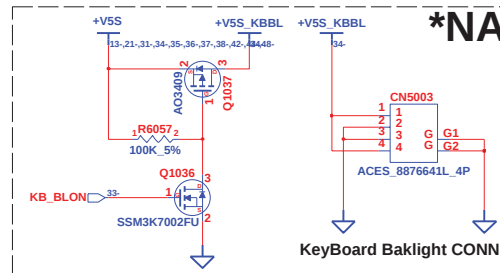
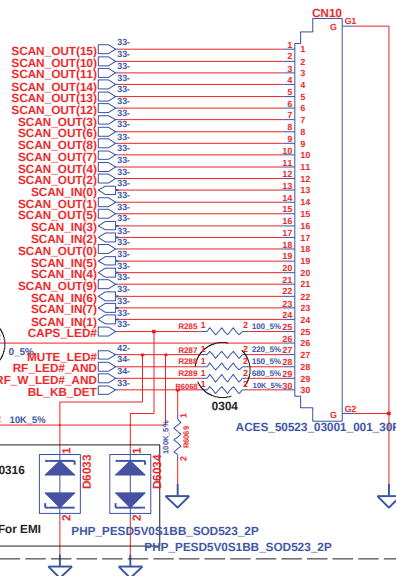
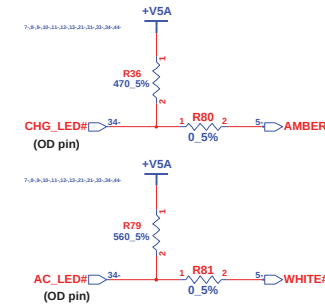
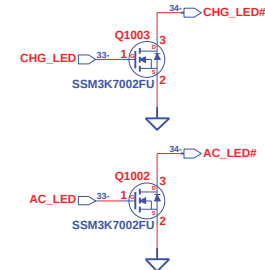
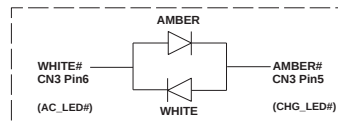
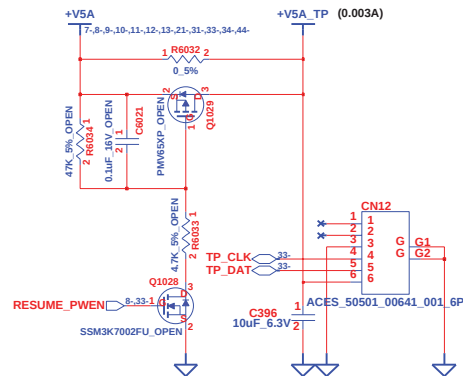




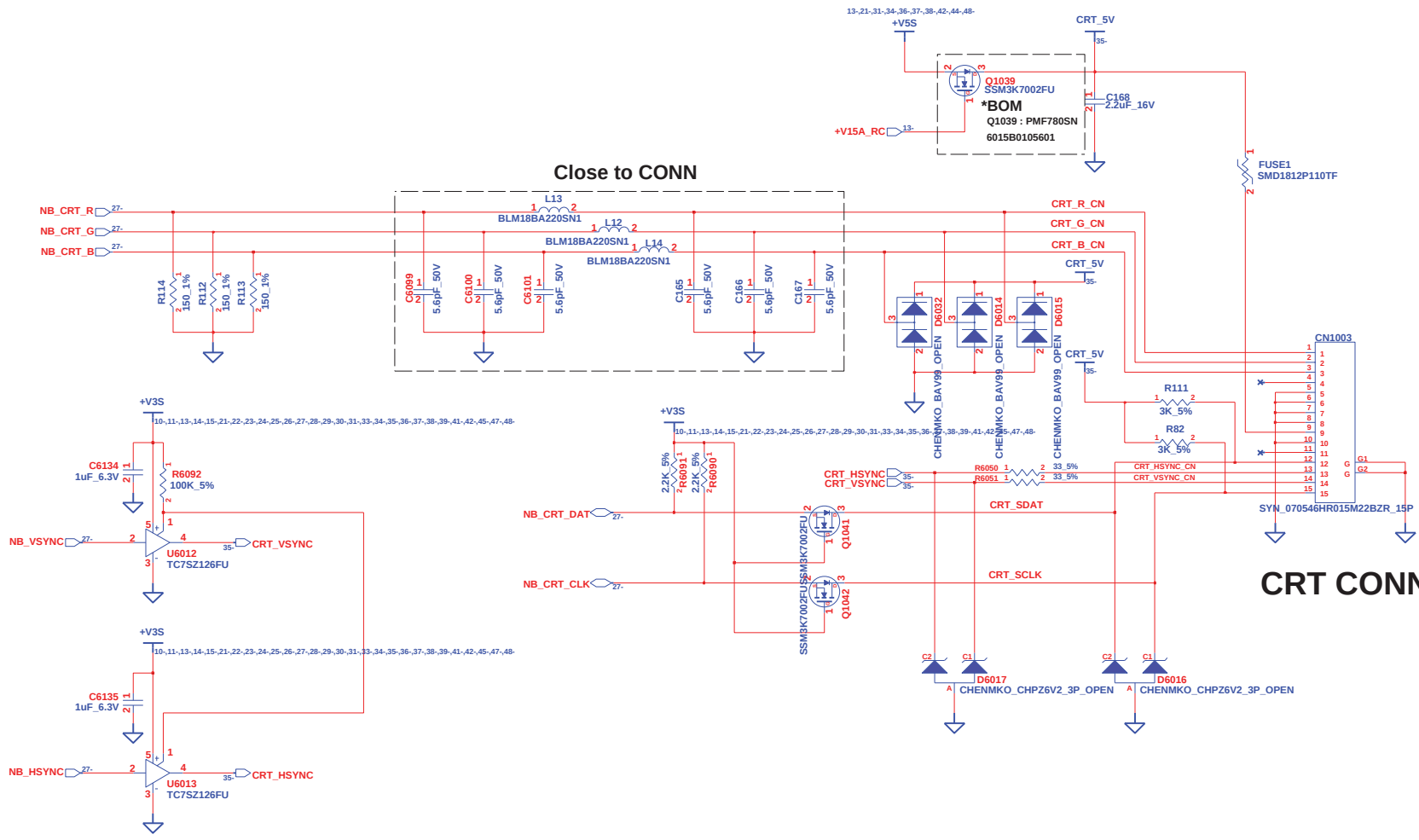








INVENTEC			
TITLE MURRAY <i>Keyboard & TouchPad</i>			
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
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CRT CONN

INVENTEC

TITLE			
MURRAY			
CRT			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03

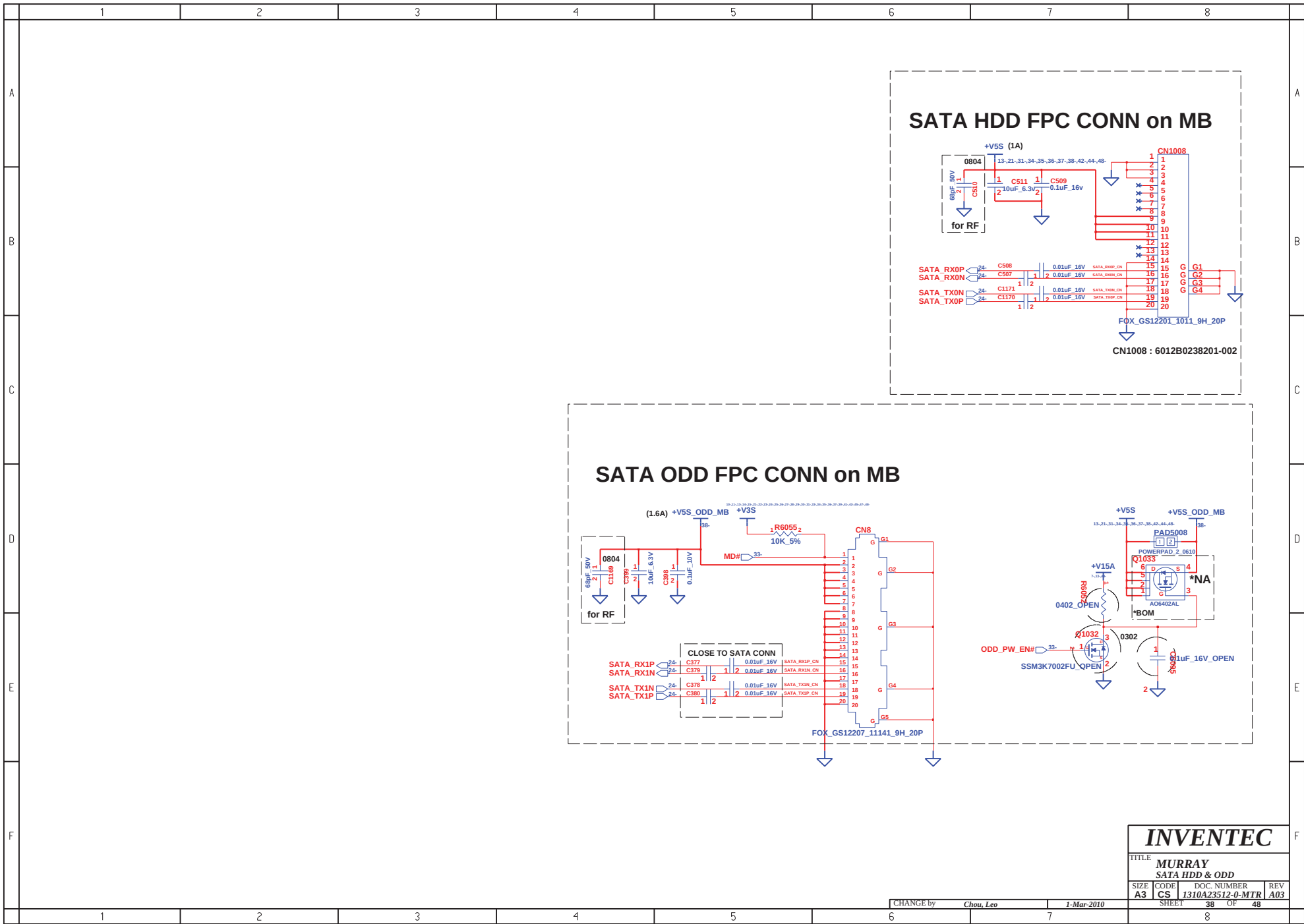
CHANGE by	Chou, Leo	1-Mar-2010
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SHEET	35	OF	48
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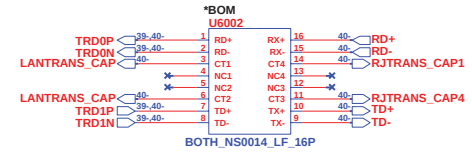
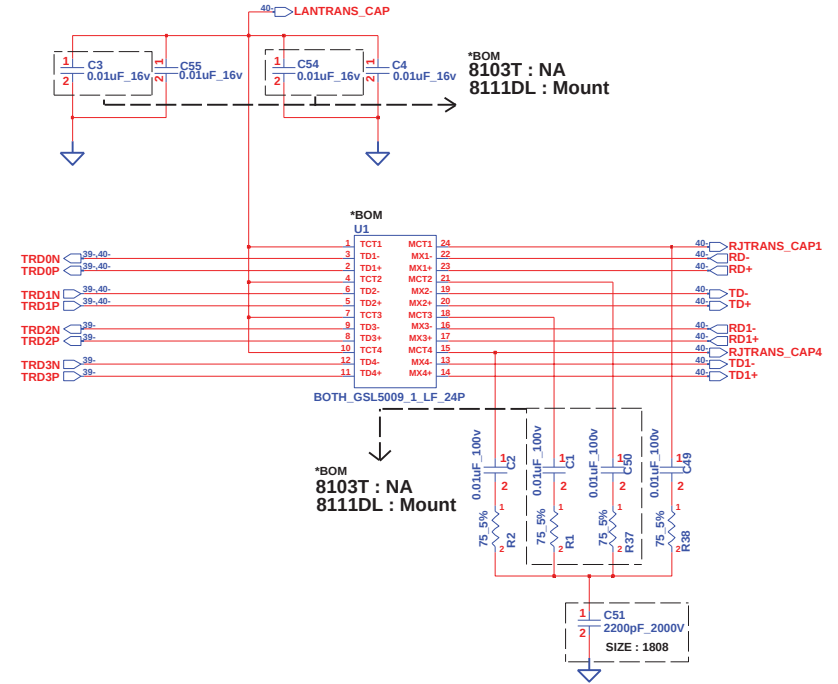
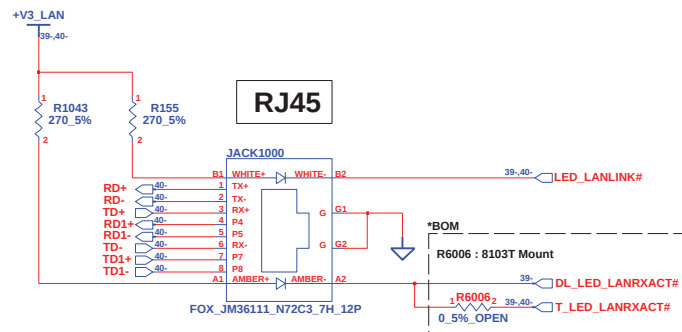
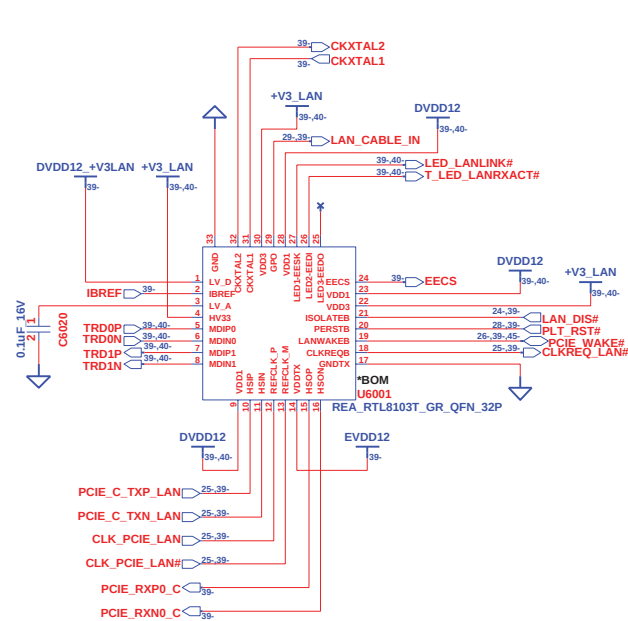
***BOM**



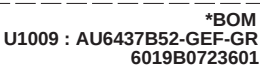




LAYOUT NOTE : Place termination resistors and caps as close to LAN controller as possible

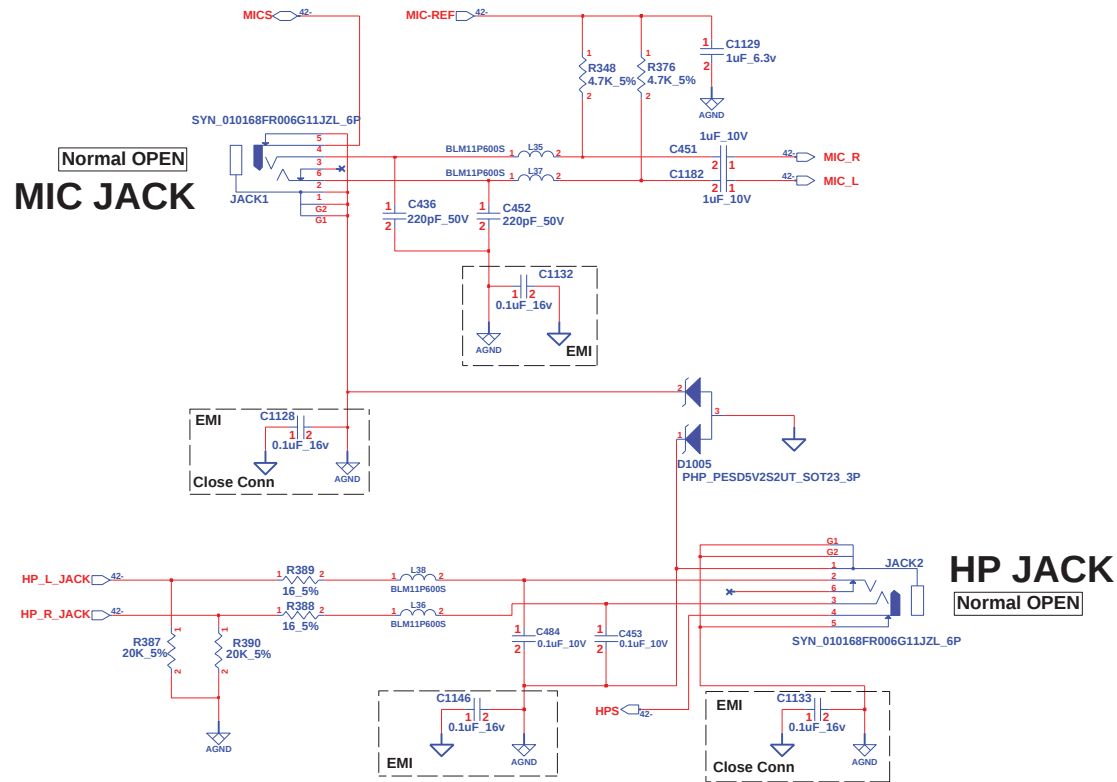


INVENTEC				
TITLE MURRAY RJ-45				
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03	
CHANGE by Chou, Leo		1-Mar-2010		
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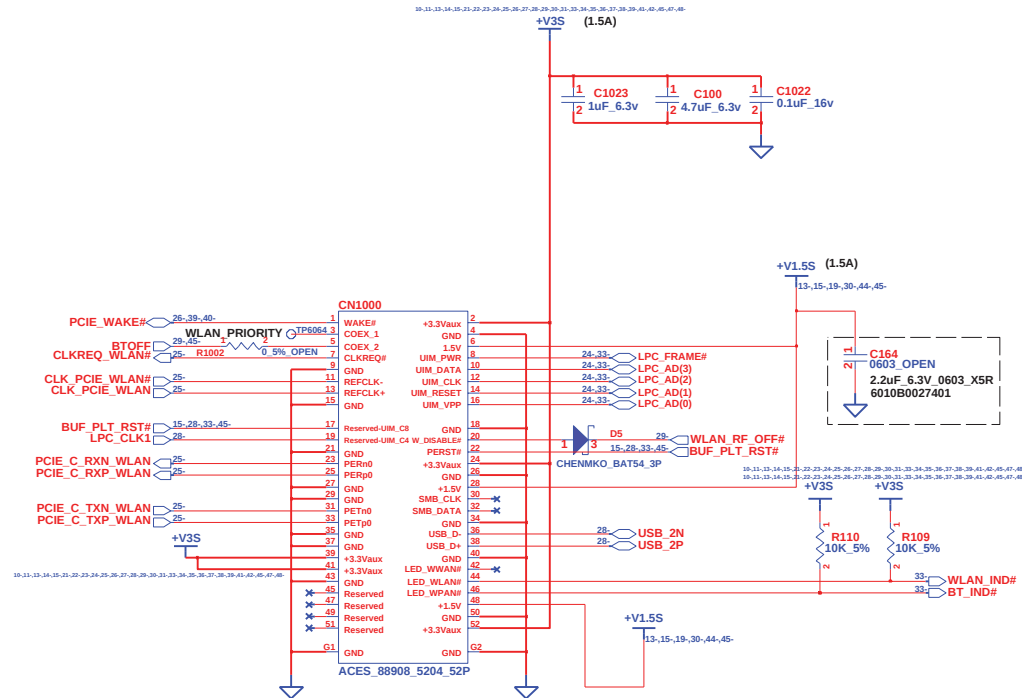
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SHEET 41 OF 48			

CHANGE by	<i>Chou, Leo</i>	<i>1-Mar-2010</i>
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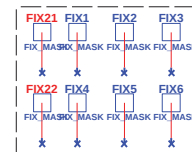
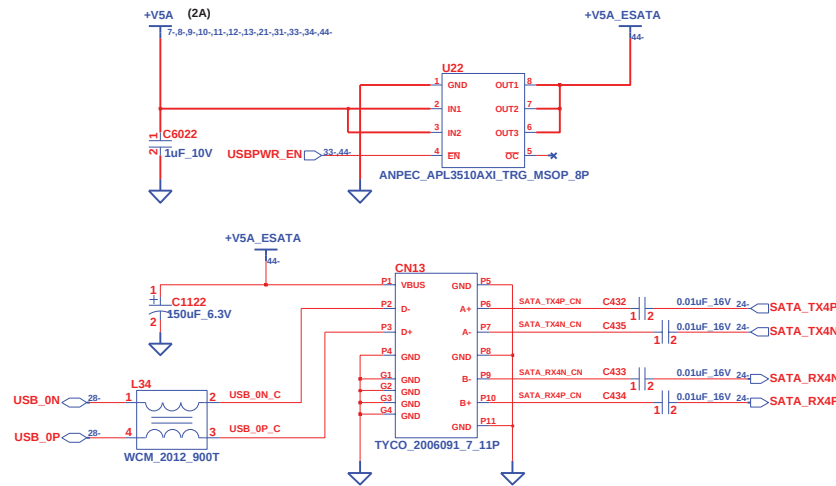
INVENTEC			
TITLE MURRAY HP JACK & MIC JACK			
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
CHANGE by Chou, Leo		1-Mar-2010	
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WLAN CONN (MINICARD)

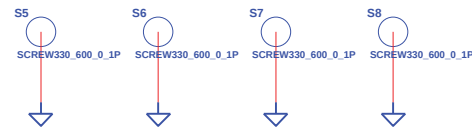


INVENTEC			
TITLE MURRAY WLAN, BT & WebCAM			
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
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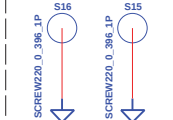
ESATA CONN (without Re-Driver chip)



CPU's screws



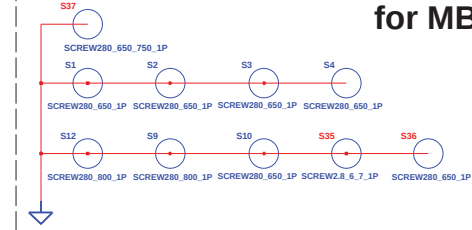
for PCH



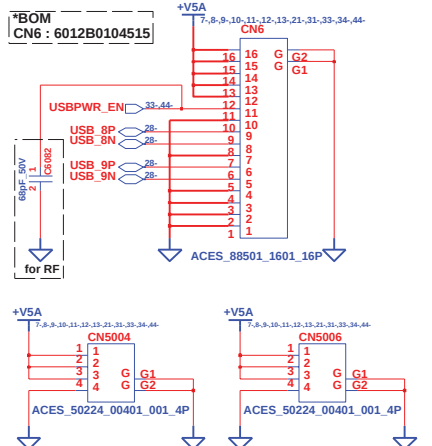
NUT P/N :
6052B0160501

*BOM

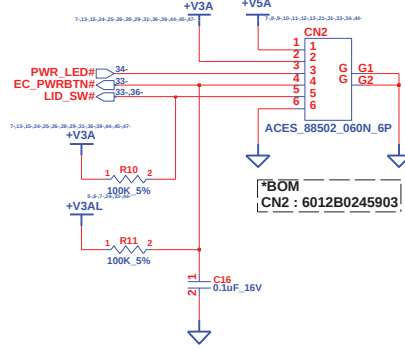
for MB



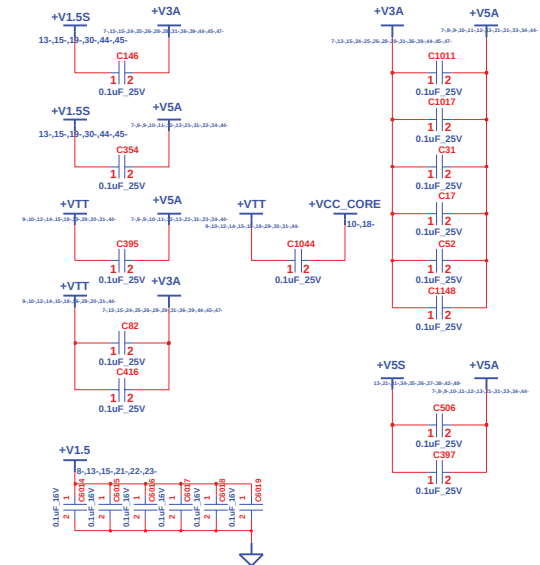
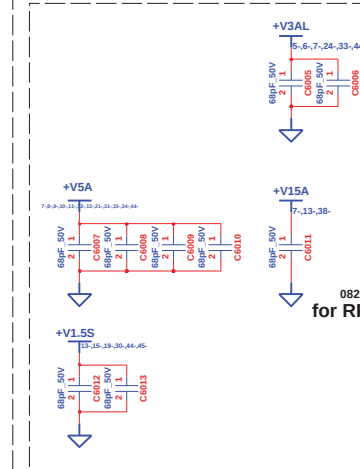
USB BOARD Cable on MB



PBN Cable CN on MB



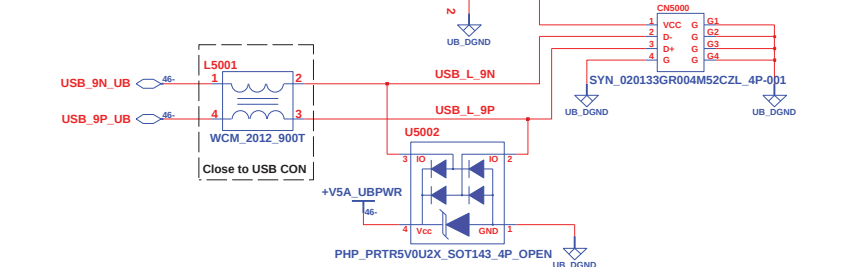
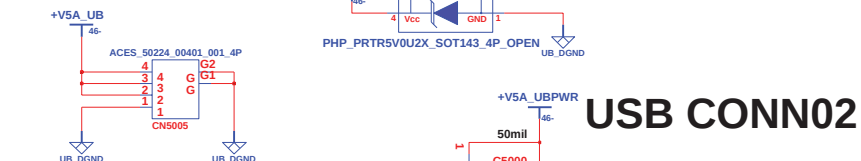
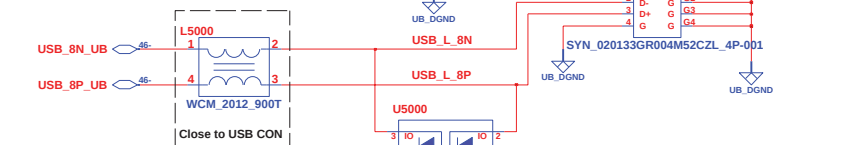
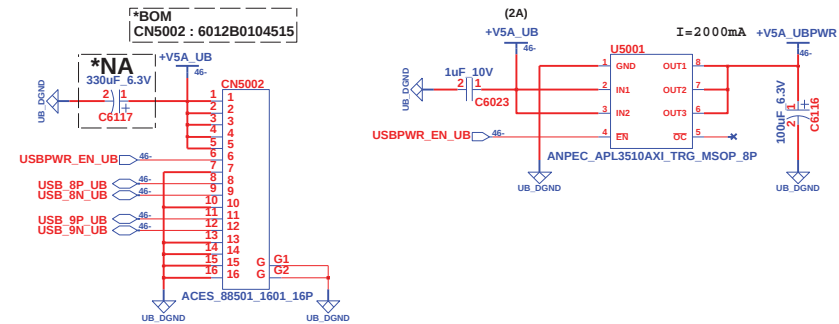
EMI CAPS



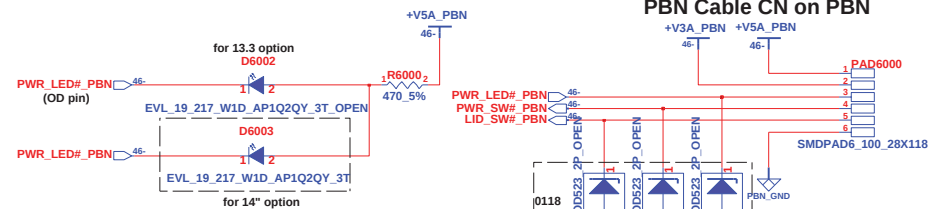
INVENTEC

TITLE MURRAY ESATA, SCREW & LID			
SIZE A3	CODE CS	DOC. NUMBER 1310A23512-0-MTR	REV A03
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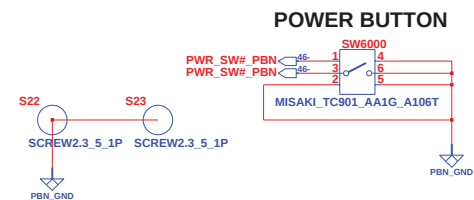
USB BOARD



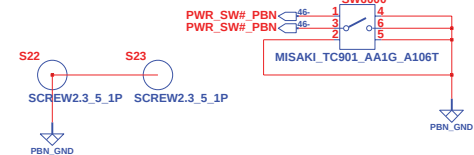
Power Button Board



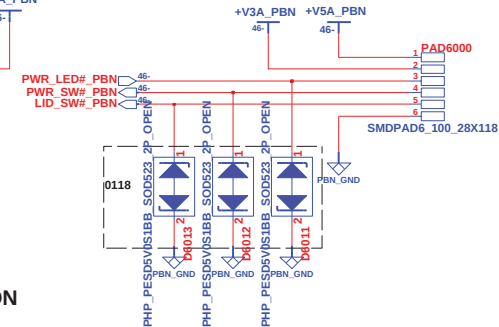
POWER LED



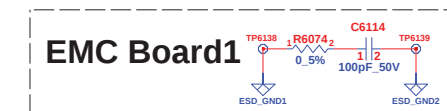
POWER BUTTON



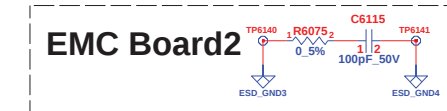
PBN Cable CN on PBN



EMC Board1



EMC Board2



INVENTEC

TITLE			
MURRAY			
USB, PowerButton (DB)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03
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A



C

D

E

F

A

1

10

1

E

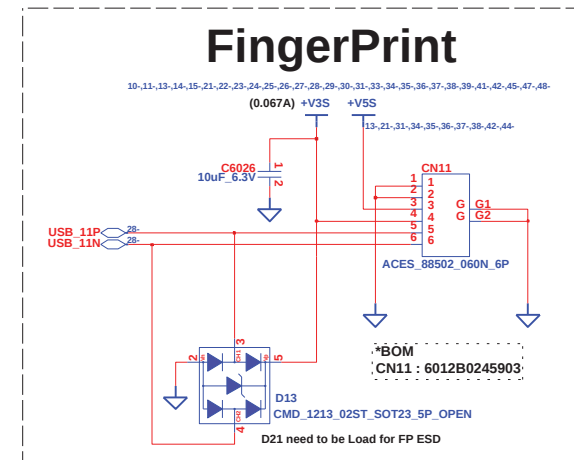
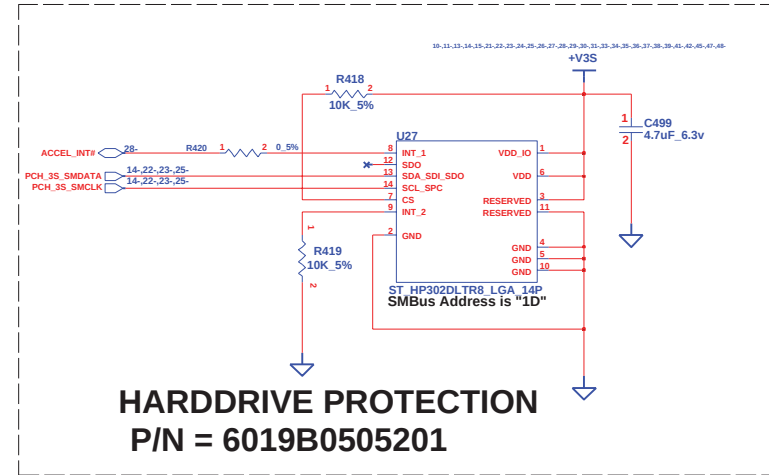
F

TITLE			
MURRAY WWAN(Option)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03
SHEET		47 OF	48

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OPTION PART



INVENTEC

TITLE			
MURRAY			
Touchscreen, FP & HD LOCK(Option)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23512-0-MTR	A03
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